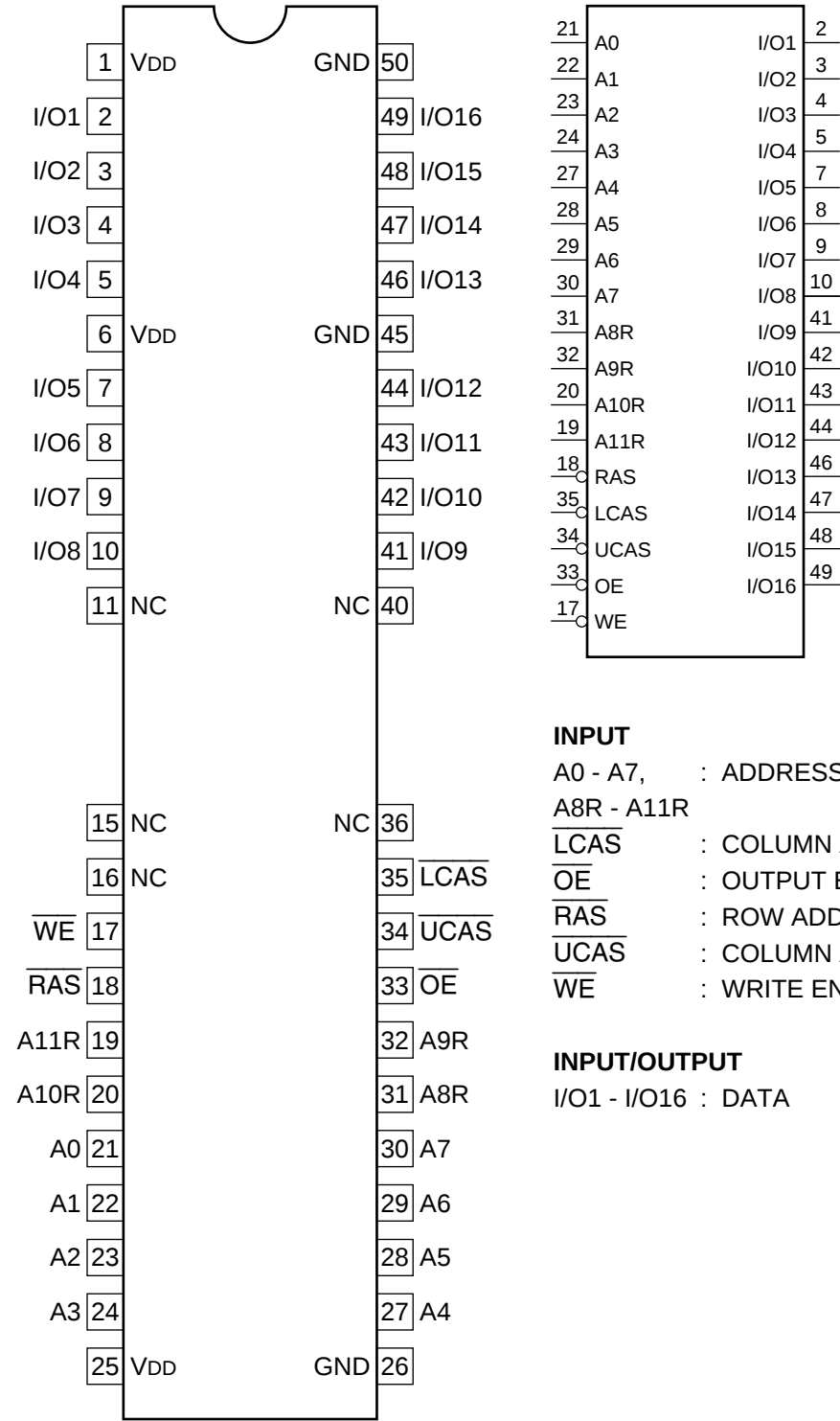


C-MOS 16 M (1,048,576 × 16)-BIT DRAM
—TOP VIEW—



INPUT

A0 - A7, : ADDRESS
A8R - A11R
LCAS : COLUMN ADDRESS STROBE (LOWER)
OE : OUTPUT ENABLE
RAS : ROW ADDRESS STROBE
UCAS : COLUMN ADDRESS STROBE (UPPER)
WE : WRITE ENABLE

INPUT/OUTPUT

I/O1 - I/O16 : DATA