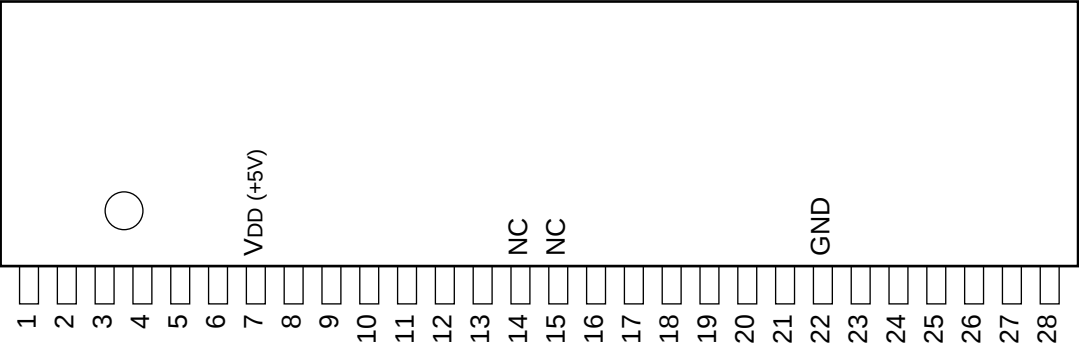

C-MOS 2M (262,214 X 8)-BIT FIELD MEMORY
-TOP VIEW-



(VDD = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	WE	15	—	NC
2	I	IE	16	I	RE
3	I	D in 0	17	I	OE
4	I	D in 1	18	O	D out 7
5	I	D in 2	19	O	D out 6
6	I	D in 3	20	O	D out 5
7	—	VDD	21	O	D out 4
8	I	D in 4	22	—	GND
9	I	D in 5	23	O	D out 3
10	I	D in 6	24	O	D out 2
11	I	D in 7	25	O	D out 1
12	I	RSTW	26	O	D out 0
13	I	SWCK	27	I	RSTR
14	—	NC	28	I	SRCK

D in 0 - D in 7 ; DATA INPUTS
D out 0 - D out 7 ; DATA OUTPUTS
IE ; INPUT ENABLE INPUT
OE ; OUTPUT ENABLE INPUT
RE ; READ ENABLE INPUT
RSTR ; RESET READ INPUT
RSTW ; RESET WRITE INPUT
SRCK ; SERIAL READ CLOCK INPUT
SWCK ; SERIAL WRITE CLOCK INPUT
WE ; WRITE ENABLE INPUT

