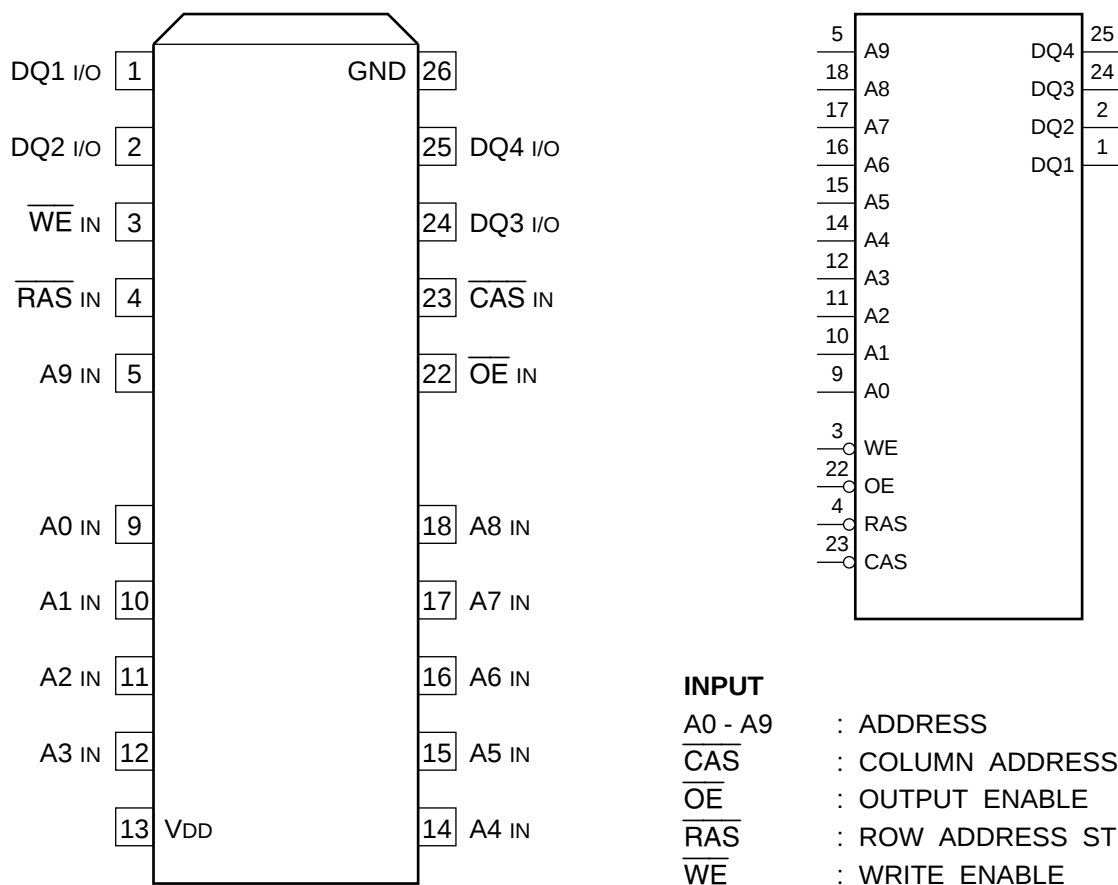


C-MOS 4 M (1,048,576 × 4)-BIT DRAM

—TOP VIEW—

**INPUT**

A0 - A9 : ADDRESS
 $\overline{\text{CAS}}$: COLUMN ADDRESS STROBE
 $\overline{\text{OE}}$: OUTPUT ENABLE
 $\overline{\text{RAS}}$: ROW ADDRESS STROBE
 $\overline{\text{WE}}$: WRITE ENABLE

INPUT/OUTPUT

DQ1 - DQ4 : DATA

