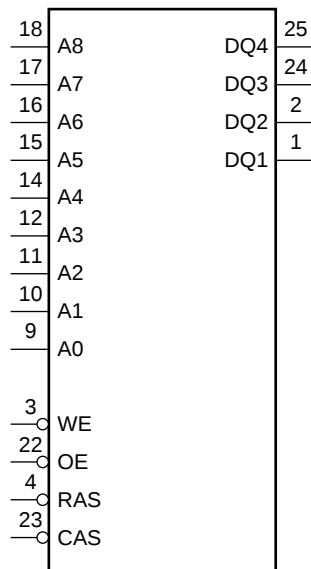
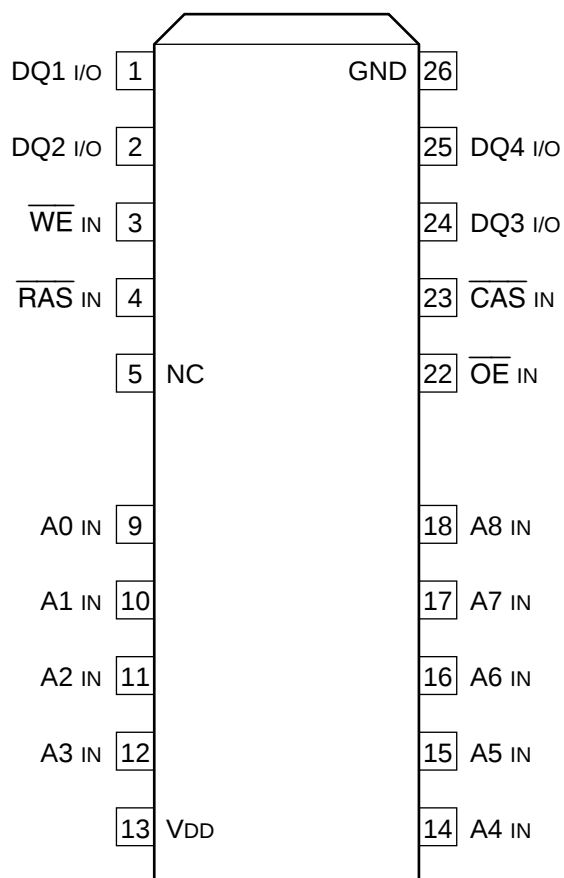


C-MOS (262,144 WORD × 4)-BIT DRAM

—TOP VIEW—

**INPUT**

A0 - A8 : ADDRESS

 $\overline{CAS}$: COLUMN ADDRESS STROBE $\overline{OE}$: OUTPUT ENABLE $\overline{RAS}$: ROW ADDRESS STROBE $\overline{WE}$: WRITE ENABLE**INPUT/OUTPUT**

DQ1 - DQ4 : DATA