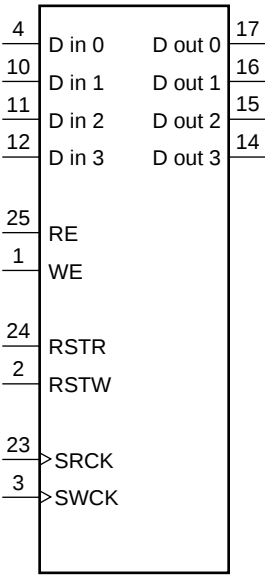
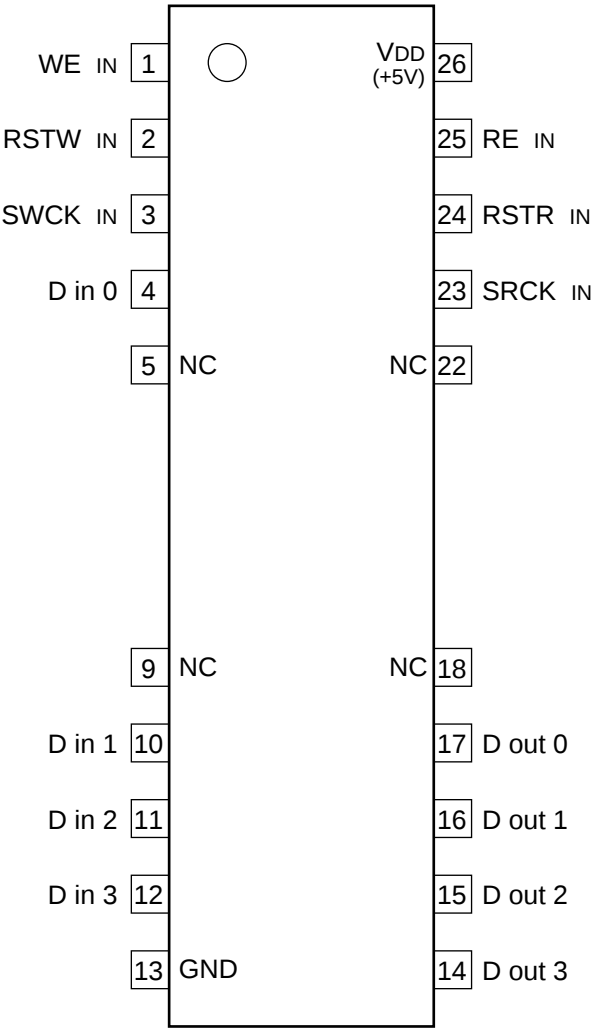


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C-MOS 1M (262,263 X 4)-BIT FIELD MEMORY  
-TOP VIEW-



D in 0 - D in 3 ; DATA INPUTS  
D out 0 - D out 3 ; DATA OUTPUTS  
RE ; READ ENABLE INPUT  
RSTR ; RESET READ INPUT  
RSTW ; RESET WRITE INPUT  
SRCK ; SERIAL READ CLOCK  
SWCK ; SERIAL WRITE CLOCK  
WE ; WRITE ENABLE INPUT

