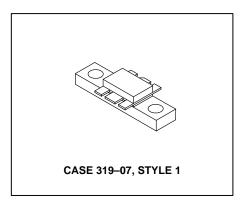
The RF Line NPN Silicon RF Power Transistor

... designed for 12.5 volt UHF large-signal, common-base amplifier applications in industrial and commercial FM equipment operating in the range of 806-960 MHz.

- Specified 12.5 Volt, 870 MHz Characteristics
 - Output Power = 30 Watts Power Gain = 5.2 dB Min Efficiency = 50% Min
- Series Equivalent Large-Signal Characterization
- Internally Matched Input for Broadband Operation
- Tested for Load Mismatch Stress at All Phase Angles with 20:1 VSWR @ High Line and RF Overdrive
- Gold Metallized, Emitter Ballasted for Long Life and Resistance to Metal Migration
- · Silicon Nitride Passivated

MRF844

30 W, 870 MHz RF POWER TRANSISTOR NPN SILICON



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	16	Vdc
Collector–Base Voltage	V _{CBO}	36	Vdc
Emitter–Base Voltage	VEBO	4.0	Vdc
Collector Current — Continuous	IC	10.9	Adc
Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C	P _D	115 0.66	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case (2)		1.5	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I _C = 50 mAdc, I _B = 0)	V(BR)CEO	16	_	_	Vdc
Collector–Emitter Breakdown Voltage (IC = 50 mAdc, VBE = 0)	V(BR)CES	36	_	_	Vdc
Emitter–Base Breakdown Voltage (I _E = 10 mAdc, I _C = 0)	V(BR)EBO	4.0	_	_	Vdc
Collector Cutoff Current (V _{CB} = 15 Vdc, I _E = 0)	ICBO	_	_	10	mAdc

NOTES: (continued)

1. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF amplifier.

2. Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques.



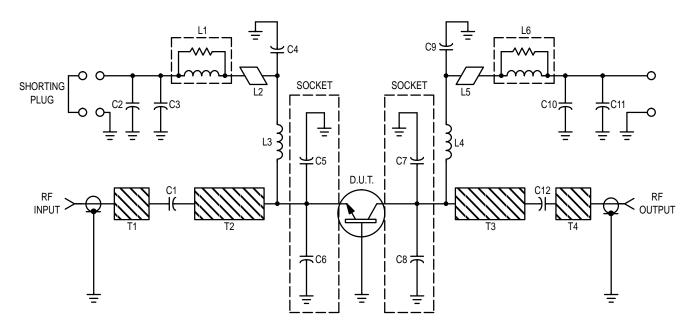


ELECTRICAL CHARACTERISTICS — **continued** $(T_C = 25^{\circ}C)$ unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS					
DC Current Gain (I _C = 2.0 Adc, V _{CE} = 5.0 Vdc)	hFE	10	40	_	_
DYNAMIC CHARACTERISTICS	-				
Output Capacitance (V _{CB} = 12.5 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	60	90	pF
FUNCTIONAL TESTS					
Common–Base Amplifier Power Gain (Pout = 30 W, V _{CC} = 12.5 Vdc, f = 870 MHz)	G _{PB}	5.2	6.0	_	dB
Collector Efficiency (P _{Out} = 30 W, V _{CC} = 12.5 Vdc, f = 870 MHz)	η	50	55	_	%
Load Mismatch Stress (V _{CC} = 15.5 Vdc, P _{in} = 12 W (3), f = 870 MHz, VSWR = 20:1, all phase angles)	_	No Degradation in Output Power			

NOTE:

^{3.} P_{in} = 150% of the typical input power requirement for 30 W output power @ 12.5 Vdc.



C1, C12 — 50 pF, 100 Mil Chip Capacitor

C2, C11 — 15 μ F, 20 V Tantalum C3, C10 — 1000 pF, 350 V UNELCO

C4, C9 — 91 pF Mini-Underwood

C5 — 15 pF Mini–Underwood

C6 — 15 pF Mini–Underwood

C7 — 18 pF Mini-Underwood

C8 — 24 pF Mini-Underwood

L1, L6 — 11 Turns 20 AWG Around 10 Ω 1/2 W Resistor

L2, L5 — Ferrite Bead L3, L4 — 4 Turn 20 AWG 0.2" I.D. T1, T4 — $Z_O = 50 \Omega$

T2 — Z_O = 38 Ω ℓ = λ /4 @ 838 MHz T3 — Z_O = 26 Ω ℓ = λ /4 @ 838 MHz

Figure 1. 870 MHz Test Circuit

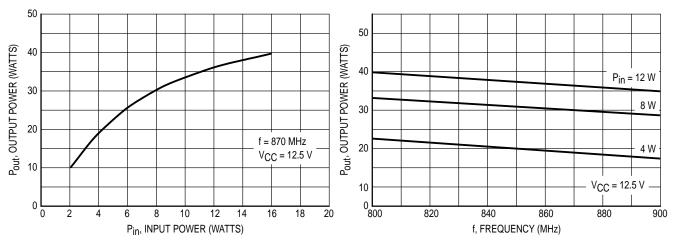


Figure 2. Output Power versus Input Power

Figure 3. Output Power versus Frequency

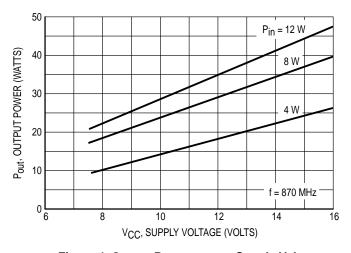
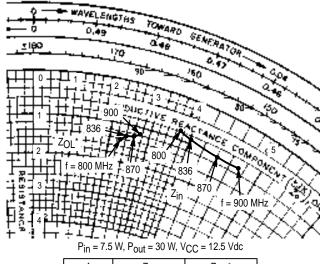


Figure 4. Output Power versus Supply Voltage

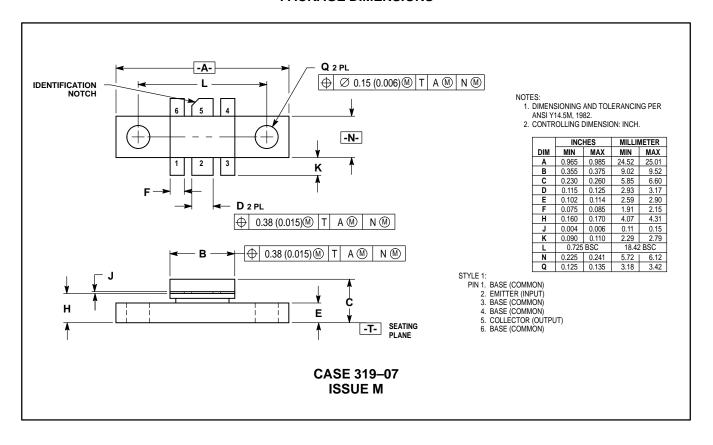


f	Z _{in}	Z _{OL} *
MHz	Ohms	Ohms
800	0.8 + j3.7	1.4 + j2.3
836	0.9 + j4.0	1.3 + j2.4
870	1.0 + j4.4	1.25 + j2.6
900	1.0 + j4.7	1.2 + j2.7

Z_{OL}* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.

Figure 5. Series Equivalent Input/Output Impedance

PACKAGE DIMENSIONS



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