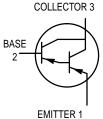
# **One Watt Darlington Transistors PNP Silicon**



#### **MAXIMUM RATINGS**

Rating	Symbol	MPSW63 MPSW64	Unit
Collector-Emitter Voltage	VCES	-30	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	-30	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	-10	Vdc
Collector Current — Continuous	IC	-500	mAdc
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	PD	1.0 8.0	Watt mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	2.5 20	Watts mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

# THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	125	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	50	°C/W

# **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = –100 μAdc, V <sub>BE</sub> = 0)	V(BR)CES	-30	_	Vdc
Collector Cutoff Current (VCB = -30 Vdc, IE = 0)	ICBO	_	-100	nAdc
Emitter Cutoff Current (VEB = -10 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	_	-100	nAdc

MPSW63 MPSW64 COLLECTOR 3

\*Motorola Preferred Device



Preferred devices are Motorola recommended choices for future use and best overall value.



# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Max	Unit
ON CHARACTERISTICS(1)					
DC Current Gain (I <sub>C</sub> = $-10$ mAdc, V <sub>CE</sub> = $-5.0$ Vdc) (I <sub>C</sub> = $-100$ mAdc, V <sub>CE</sub> = $-5.0$ Vdc)	MPSW63 MPSW64 MPSW63 MPSW64	hFE	5,000 10,000 10,000		-
Collector–Emitter Saturation Voltage (I <sub>C</sub> = -100 mAdc, I <sub>B</sub> = -0.1 mAdc)	WF3W04	VCE(sat)	20,000	-1.5	Vdc
Base–Emitter On Voltage (I <sub>C</sub> = -100 mAdc, V <sub>CE</sub> = -5.0 Vdc)		VBE(on)	_	-2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current–Gain — Bandwidth Product <sup>(2)</sup> ( $I_C = -10 \text{ mAdc}$ , $V_{CE} = -5.0 \text{ Vdc}$ , $f = 100 \text{ MHz}$ )		fΤ	125	_	MHz

<sup>1.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%.

## TYPICAL ELECTRICAL CHARACTERISTICS

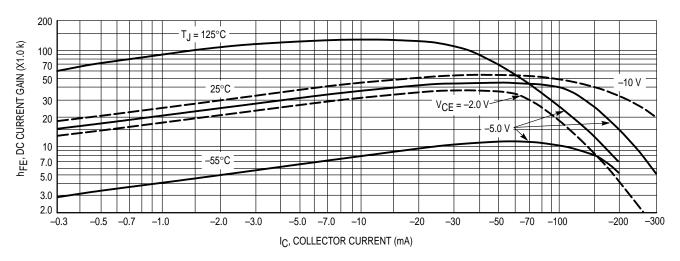


Figure 1. DC Current Gain

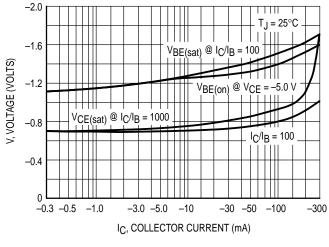


Figure 2. "ON" Voltage

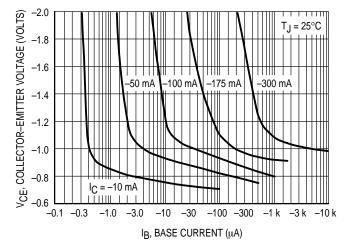
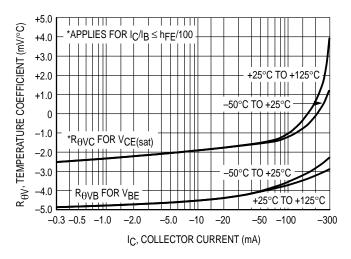


Figure 3. Collector Saturation Region

<sup>2.</sup>  $f_T = |h_{fe}| \cdot f_{test}$ .



**Figure 4. Temperature Coefficients** 

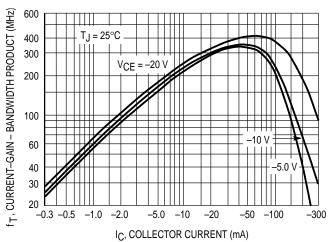


Figure 5. Current-Gain — Bandwidth Product

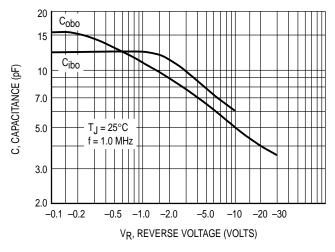


Figure 6. Capacitance

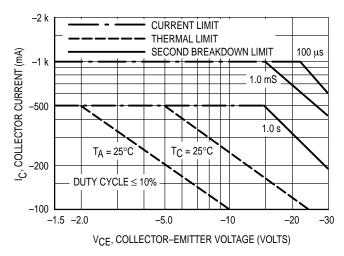
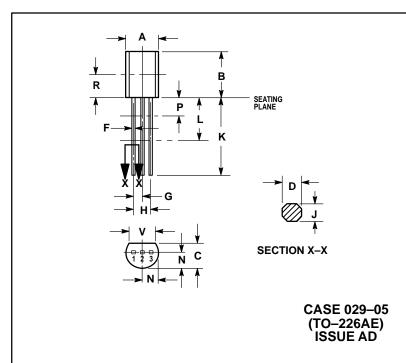


Figure 7. Active Region, Safe Operating Area

#### PACKAGE DIMENSIONS



- 1. DIMENSIONING AND TOLERANCING PER ANSI
- 714.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- 4. DIMENSION F APPLIES BETWEEN P AND L DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	ETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.44	5.21	
В	0.290	0.310	7.37	7.87	
С	0.125	0.165	3.18	4.19	
D	0.018	0.022	0.46	0.56	
F	0.016	0.019	0.41	0.48	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.018	0.024	0.46	0.61	
K	0.500		12.70		
L	0.250	_	6.35		
N	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.135		3.43		
V	0 135		3 43		

STYLE 1: PIN 1. EMITTER 2. BASE

3. COLLECTOR

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