

# UTC MPSA44/45 NPN EPITAXIAL SILICON TRANSISTOR

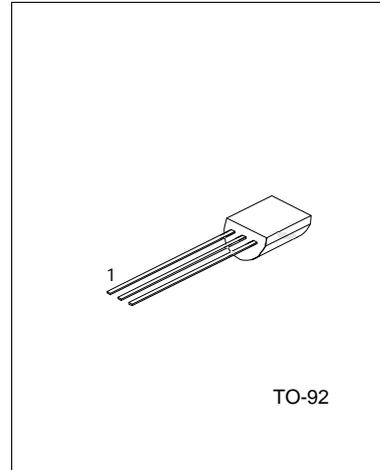
## HIGH VOLTAGE TRANSISTOR

### FEATURES

- \*Collector-Emitter voltage:  
 $V_{CE0}=400V$ (MPSA44)  
 $V_{CE0}=350V$ (MPSA45)
- \*Collector current up to 300mA
- \*Complement to MPSA94/93
- \*Collector Dissipation:  
 $P_c(\max)=625mW$

### APPLICATION

- \*Telephone switching
- \*High voltage switch



1:EMITTER 2:BASE 3:COLLECTOR

### ABSOLUTE MAXIMUM RATINGS ( Operating temperature range applies unless otherwise specified )

PARAMETER	SYMBOL	RATING	UNIT
Collector-base voltage MPSA44 MPSA45	$V_{CB0}$	500 400	V
Collector-emitter voltage MPSA44 MPSA45	$V_{CE0}$	400 350	V
Emitter-base voltage	$V_{EB0}$	6	V
Collector dissipation( $T_a=25^{\circ}C$ )	$P_c$	625	mW
Collector dissipation( $T_c=25^{\circ}C$ )	$P_c$	1.5	W
Collector current	$I_c$	300	mA
Junction Temperature	$T_j$	150	$^{\circ}C$
Storage Temperature	$T_{STG}$	-55 ~ +150	$^{\circ}C$

### ELECTRICAL CHARACTERISTICS( $T_j=25^{\circ}C$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-base breakdown voltage MPSA44 MPSA45	$BV_{CB0}$	$I_c=100\mu A, I_B=0$	500 400			V
Collector-emitter breakdown voltage MPSA44 MPSA45	$BV_{CE0}$	$I_c=1mA, I_B=0$	400 350			V
Emitter-base breakdown voltage	$BV_{EB0}$	$I_E=100\mu A, I_c=0$	6			V

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(continued)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector cut-off current MPSA44 MPSA45	ICBO	V <sub>CB</sub> =400V, I <sub>E</sub> =0 V <sub>CB</sub> =320V, I <sub>E</sub> =0			1 1	μA
Collector cut-off current MPSA44 MPSA45	ICES	V <sub>CB</sub> =400V, I <sub>E</sub> =0 V <sub>CB</sub> =320V, I <sub>E</sub> =0			1 1	μA
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> =4V, I <sub>C</sub> =0			0.1	μA
DC current gain(note)	h <sub>FE</sub>	V <sub>CE</sub> =10V, I <sub>C</sub> =1mA V <sub>CE</sub> =10V, I <sub>C</sub> =10mA V <sub>CE</sub> =10V, I <sub>C</sub> =50mA V <sub>CE</sub> =10V, I <sub>C</sub> =100mA	40 50 45 40		240	
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =1mA, I <sub>B</sub> =0.1mA I <sub>C</sub> =10mA, I <sub>B</sub> =1mA I <sub>C</sub> =50mA, I <sub>B</sub> =5mA			0.4 0.5 0.75	V
Base-emitter saturation voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =10mA, I <sub>B</sub> =1mA			0.75	V
Current gain bandwidth product	f <sub>T</sub>	V <sub>CE</sub> =20V, I <sub>C</sub> =10mA, f=100MHz	50			MHz
Output capacitance	C <sub>ob</sub>	V <sub>CB</sub> =20V, I <sub>E</sub> =0 f=1MHz			7	pF

Note: Pulse test: PW<300μs, Duty Cycle<2%

UTCMP5A44/45 NPN EPITAXIAL SILICON  
TRANSISTOR

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TYPICAL CHARACTERISTIC CURVES

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Fig.1 DC current gain

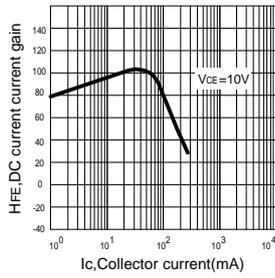


Fig.2 Turn-on switching times

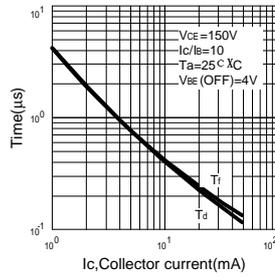


Fig.3 Turn-off switching times

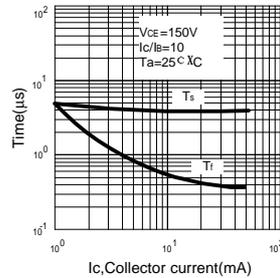


Fig.4 Capacitance

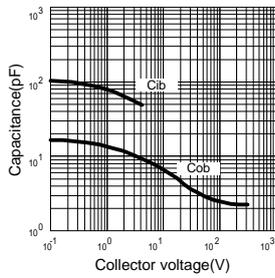


Fig.5 ON Voltage

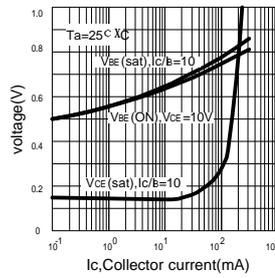


Fig.6 Collector saturation region

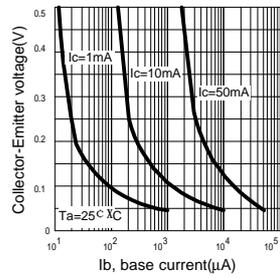


Fig.7 High Frequency current gain

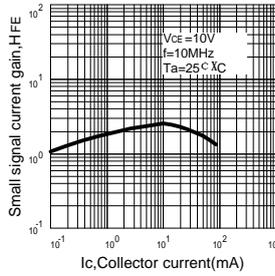


Fig.8 Safe operating area

