## MPM1A800A120C5

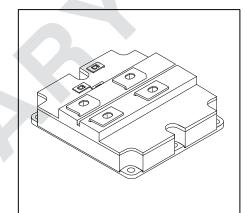
# Preliminary Data Sheet Hybrid Power Module

This module is designed for use in switching applications. Each module utilizes advanced insulated gate bipolar transistors (IGBT) in a single configuration with a reverse-connected free-wheeling diode. Applications include AC motor control, UPS, and motion/servo control.

- 800 Amp, 1200 Volt IGBT Switch
- Low Inductance Package
- Convenient Package Outline
- Isolated Baseplate for Easy Heat Sinking
- Low VCE(sat)

Motorola Preferred Device

800 AMP, 1200 VOLT HYBRID POWER MODULE



PLASTIC PACKAGE PRELIMINARY

#### MAXIMUM DEVICE RATINGS (TJ = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit	
MAXIMUM RATED VALUES			•	
IGBT Reverse Voltage	VCES	1200	V	
Gate-Emitter Voltage	VGES	± 20	V	
Continuous IGBT Collector Current	IC	800	A	
Peak IGBT Collector Current	lC(pk)	1600	A	
Continuous Free-Wheeling Diode Current	lF	800	A	
Peak Free-Wheeling Diode Current	lF(pk)	1600	A	
Total Power Dissipation	P <sub>tot</sub>	6300	W	
IGBT Junction Temperature Range	Тj	- 40 to +150	°C	
Free-Wheeling Diode Junction Temperature Range	Тj	- 40 to +125	°C	

This document contains information on a product under development. Motorola reserves the right to change or discontinue this product without notice.

Preferred devices are Motorola recommended choices for future use and best overall value.



REV 1

#### MAXIMUM DEVICE RATINGS (continued) (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted)

Rating		Symbol	Value	Unit
TOTAL MODULE				•
Isolation Voltage		VISO	3500	VAC
Maximum Junction Temperature		Tjmax	150	°C
Operating Temperature Range	Transistor Diode	T <sub>opr</sub> T <sub>opr</sub>	- 40 to + 150 - 40 to + 125	°C ℃
Storage Temperature Range		T <sub>stg</sub>	- 40 to + 125	°C
Mounting Torque		-	3.0	Nm
Terminal Mounting Torque		-	8 – 10	Nm

### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OUTPUT INVERTER				•	•
Gate-Emitter Leakage Current (V <sub>CE</sub> = 0 V, V <sub>GE</sub> = $\pm$ 20 V)	IGES	-	40	400	nA
Collector-Emitter Leakage Current (V <sub>CE</sub> = 1200 V, V <sub>GE</sub> = 0 V) T <sub>J</sub> = 25°C T <sub>J</sub> = 125°C	ICES	Ξ	5 20	25 -	mA mA
Gate-Emitter Threshold Voltage ( $V_{CE} = V_{GE}$ , $I_C = 80$ mA)	VGE(th)	4.5	5.5	6.5	V
Collector-Emitter Breakdown Voltage (I <sub>C</sub> = 100 $\mu$ A, V <sub>GE</sub> = 0)	V(BR)CES	1200	1300	-	V
Collector-Emitter Saturation Voltage (I <sub>C</sub> = 1200 A, V <sub>GE</sub> = 15 V) T <sub>J</sub> = 25°C T <sub>J</sub> = 125°C	VCE(SAT)		3.0 3.8		V V
Input Capacitance ( $V_{GE}$ = 0 V, $V_{CE}$ = 25 V, f = 1.0 MHz)	Cies	-	160	-	nF
Fall Time – Inductive Load ( $V_{CE} = 600 \text{ V}, \text{ I}_{C} = 1.2 \text{ kA}$ )	t <sub>fi</sub>	-	0.3	-	μs
Diode Forward Voltage (I <sub>F</sub> = 1200 A, V <sub>GE</sub> = 0 V)	VF	-	2.7	-	V
Thermal Resistance – IGBT (Each Die)	R <sub>θJC</sub>	-	-	0.02	°C/W
Thermal Resistance – Free-Wheeling Diode (Each Die)	R <sub>θJC</sub>	-	-	0.03	°C/W

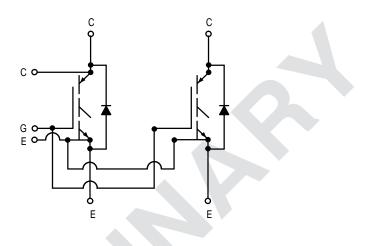
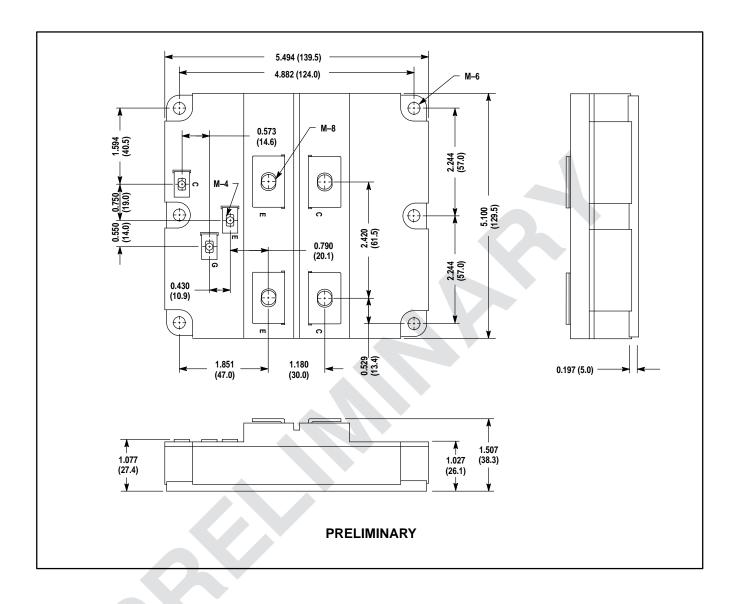


Figure 1.

#### PACKAGE DIMENSIONS



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