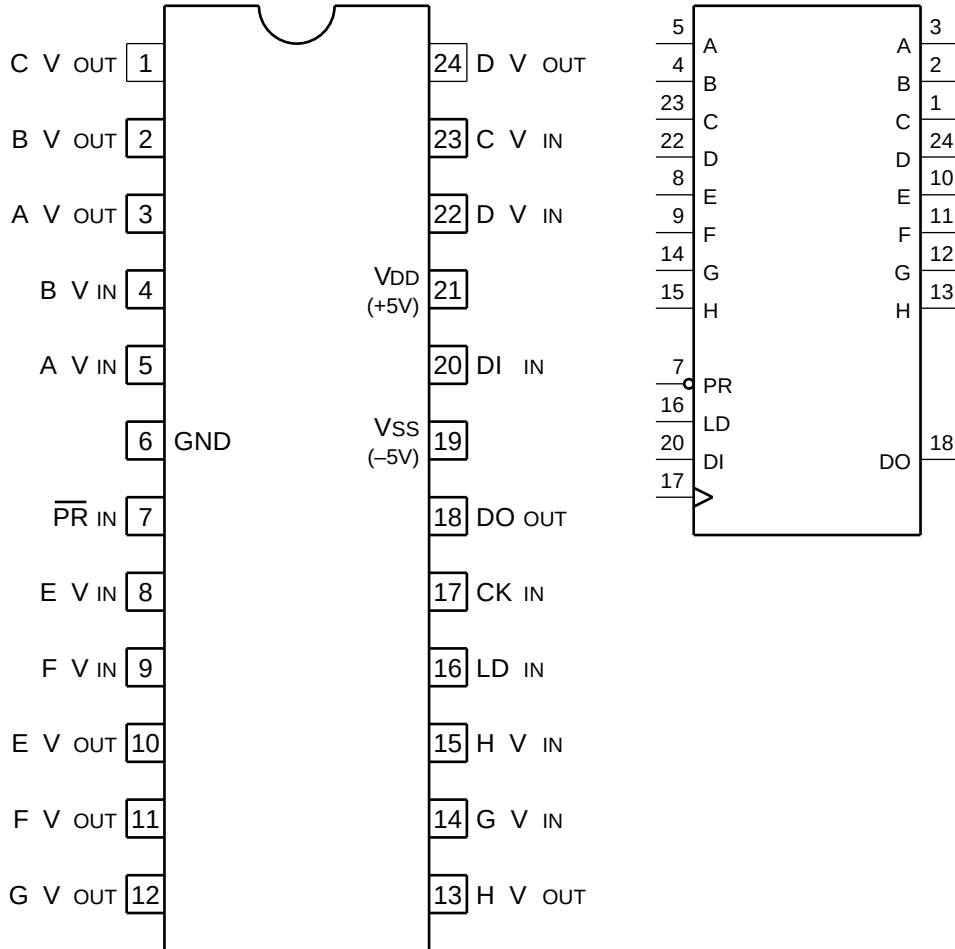

C-MOS 8-BIT 8CHANNEL D/A CONVERTER

— TOP VIEW —



CK ; CLOCK INPUT
 DI ; SERIAL DATA INPUT
 DO ; DATA OUTPUT
 LD ; DATA LOAD CONTROL INPUT
 $\overline{\text{PR}}$; PRESET INPUT

