

IC INFORMATION

		MN677061ZYUB		
Function	SERVO DSP			1/2 E
Type	CMOS	Model	AVX-7300/UC	

Pin Functions(MN677061ZY)

Pin No.	Pin Name	I/O	Function and Operation
1	AS	I	Full adder signal input
2	TE	I	Phase-contrast/Three beam tracking error input
3	FE	I	Focus error input
4	AVDD		Analog power supply (3.3V)
5	FODRV	O	Focus driver output
6	TRDRV	O	Tracking driver output
7	AVSS1		Analog GND
8	ARF	I	Equivalent RF+ input
9		I	Equivalent RF- input
10,11	IREF1,2	I	DBAL reference current input 1,2
12,13	DSLFI,2	I/O	DSL capacitor input/output 1,2
14	AVDD2		Analog power supply (3.3V)
15	VHALF		Reference voltage (1.65±0.1V)
16,17	NC		Not used
18	VREFH		Reference voltage (2.2±0.1V)
19	RVI	I/O	VERFH reference current resistor input/output
20	AVSS2		Analog GND
21,22	PLFLT1,2	O	PLL capacitor output 1,2
23	JITOUT	I/O	Jitter signal monitor input/output
24	N.C.1	I	Open
25	N.C.2	I/O	Pull up
26	NC		Not used
27	AVDD3		Analog power supply (3.3V)
28	N.C.3	I	Pull up
29	N.C.4		Not used
30	AVSS3		Analog GND
31	N.C.5		Pull up
32	N.C.6		Not used
33	N.C.7		Open
34	TRCRS	I	Track cross production signal input
35	VCOF	I/O	JFVCO control voltage input/output
36	DBALO	O	DSL balance adjustment output
37	JLIN	O	J-line set output
38	AVDD4		Analog power supply (3.3V)
39	LOUT	O	Analog audio left output
40	ROUT	O	Analog audio right output
41	AVSS4	I	Analog GND
42	GBAL	O	Tangential balance adjustment output
43	TBAL	O	Tracking balance adjustment output
44	FBAL	O	Focus balance adjustment output
45	VSS331		I/O GND
46	VDD331		I/O power supply (3.3V)
47	OFTR	I	Off track signal input
48	SYSCLK	I	System clock input
49	BDO	I	RF dropout signal input
50	TSTSG	O	Calibration signal output
51	TRSDRV	O	Traverse driver output
52	SPDRV	O	Spindle driver output
53	FG	I	FG signal input
54	N.C.8		Not used
55	CRGDRV	O	Carriage driver output
56	N.C.10		Not used
57	VSS251		GND
58	VDD251		Power supply
59	DTRD	I	Data read control signal output
60	LDGT	I	GND
61	LRCK	O	LR channel data strobe output
62	SBBCLK	O	CD sub code synchronism signal output

IC INFORMATION

Function		SERVO DSP	MN677061ZYUB		2/2	E
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Pin No.	Pin Name	I/O	Function and Operation
63	PLLOK	I	PLL drag OK signal input
64	IDHOLD		GND
65	DACLRCK	I	1bit DAC - LR channel data strobe input
66	DACDATA	I	CD 1bit DAC channel data input
67		O	Tracking ON output
68	DACCLK	O	1bit DAC channel data shift clock input
69	IPFLG	O	CIRG error flag output
70	SUBC	O	CD sub code output
71		O	CD sub code data frame clock output
72	MINTEST	I	GND
73	TEST	I	GND
74	VSS332		I/O GND
75	VDD332		I/O power supply (3.3V)
76	CHCK40	O	SRDATA clock output
77-80	DAT3-0	O	SRDATA output 3-0
81	VSS333		I/O GND
82	VDD333		I/O power supply (3.3V)
83	TX	O	Digital audio interface data output
84	XRESET	I	Reset input
85	ENS	I	Servo DSC serial I/F chip select input
86	ENC	I	CIRC serial I/F chip select input
87	CPUIRQ	O	Interrupt request output to system computer
88	CPUCLK	I	System computer serial I/F clock input
89	CPUDTIN	I	System computer serial I/F data input
90	CPUDTOUT	O	System computer serial I/F data output
91,92	MONA,B	O	Monitor terminal output A,B
93	CDPLLOK	O	CD PLL drag OK signal output
94	N.C.12		Not used
95	VSS252		GND
96	VDD252		Power supply
97,98	N.C.		Pull up
99	TG	I	Tangential phase-contrast input
100	REFNV	I	RF envelop input

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