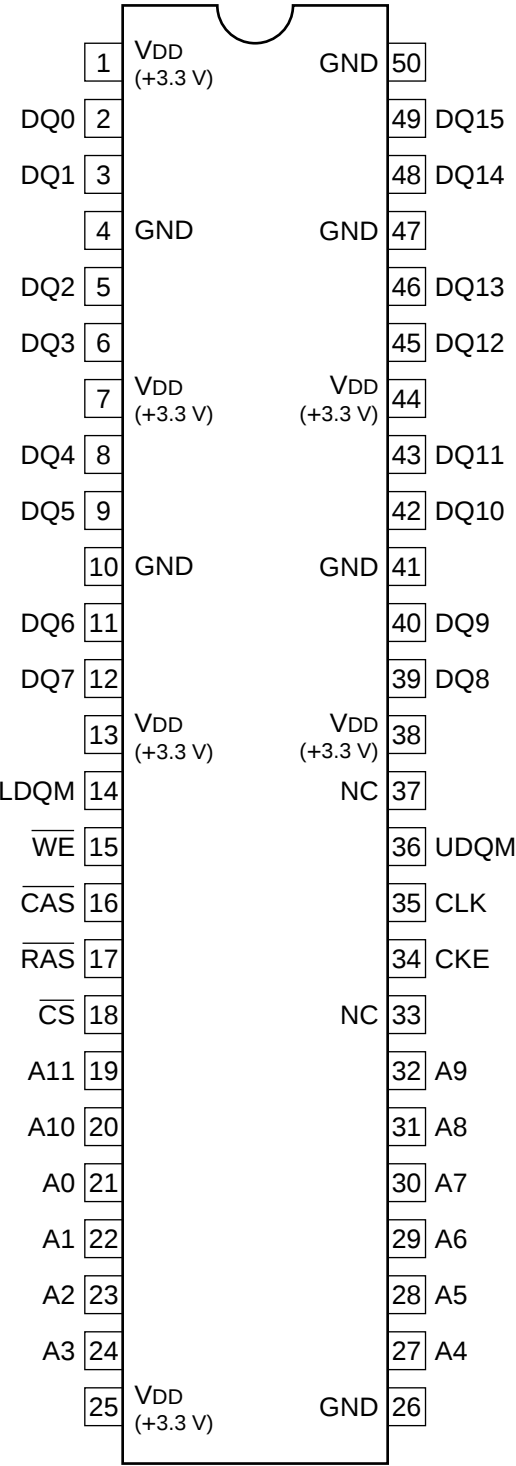

C-MOS 16 M (524,288-WORD × 16 × 2) -BIT SYNCHRONOUS DYNAMIC RAM

—TOP VIEW—



INPUT

- A0 - A11 ; ADDRESS
- CLK ; DATA
- CS ; CLOCK
- CKE ; CLOCK ENABLE
- CS ; CHIP SELECT
- RAS ; ROW ADDRESS STROBE
- CAS ; COLUMN ADDRESS STROBE
- WE ; WRITE ENABLE
- UDQM ; UPPER BYTE INPUT/OUTPUT DATA MASKING
- LDQM ; LOWER BYTE INPUT/OUTPUT DATA MASKING

INPUT/OUTPUT

- DQ0 - DQ15 ; DATA

