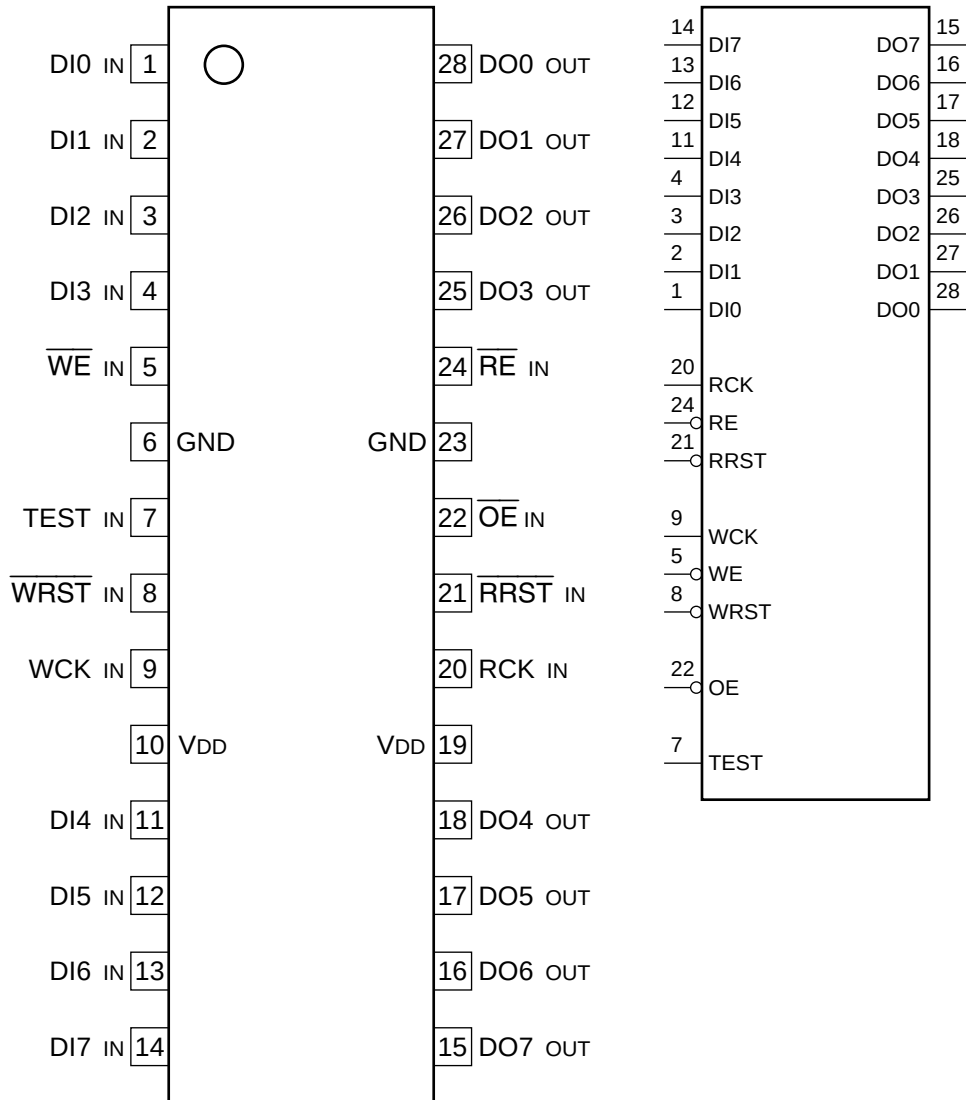


C-MOS 2 M (262,272-WORD × 8)-BIT FIFO (3.3 V) MEMORY

—TOP VIEW—

**INPUT**

DI0 - DI7 ; DATA

 $\overline{OE}$; OUTPUT ENABLE

RCK ; READ CLOCK

 $\overline{RE}$; READ ENABLE $\overline{RRST}$; READ RESET

TEST ; TEST

WCK ; WRITE CLOCK

 $\overline{WE}$; WRITE ENABLE $\overline{WRST}$; WRITE RESET**OUTPUT**

DO0 - DO7 ; DATA

