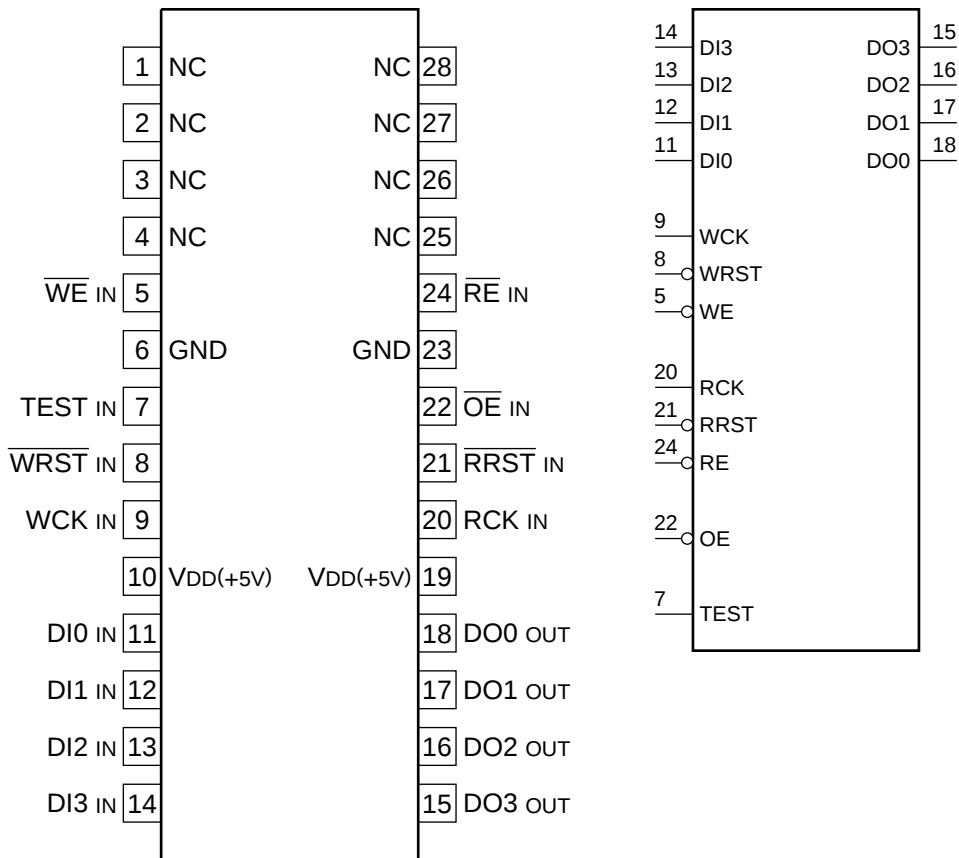

C-MOS 1M (262,144-WORD × 4)-BIT FIFO MEMORY
—TOP VIEW—



INPUT

DI0 - DI3 : DATA
 $\overline{\text{OE}}$: OUTPUT ENABLE
RCK : READ CLOCK
 $\overline{\text{RE}}$: READ ENABLE
 $\overline{\text{RRST}}$: READ RESET
TEST : TEST
WCK : WRITE CLOCK
 $\overline{\text{WE}}$: WRITE ENABLE
 $\overline{\text{WRST}}$: WRITE RESET

OUTPUT

DO0 - DO3 : DATA

