

MN1873284

Type			MN1873284
ROM (x8-bit)			32K
RAM (x8-bit)			640
Minimum Instruction Execution Time			0.5µs at 2/3 frequency dividing (at 4.5 to 5.5V, 12MHz)
Interrupts			• RESET • External 0 • External 1 • Timer 0 • Timer 1 • Timer 2 • I ² C • Remote Control • MOSD
Timer Counter			Timer Counter 0 : 8-bit x 1 Clock Source 1/1, 1/4, 1/16, 1/64 of System Clock Interrupt Source Overflow of Timer Counter 0 Timer Counter 1 : 8-bit x 1 Clock Source 1/2, 1/16, 1/64, 1/256, 1/512 of System Clock Interrupt Source Overflow of Timer Counter 1 Time Base Counter Clock Source 1/4096 of System Clock Interrupt Source 1/1, 1/2, 1/4, 1/8 of Timer Counter 2 Watchdog Timer
Serial Interface			I²C x 1 (Two bus line system)
I/O Pins	I/O	20	• Common use 12 • General use 8
	Input	1	• Common use 1
A/D Inputs			5-bit x 10ch (without S/H)
PWM			14-bit x 1ch, 8-bit x 5ch (All PWM are 5V , not connectable to 12V systems)
Special Ports			Hsync detection, Remote Control Reception
CRTC			Single OSD built-in (Caption Menu 12 x 18, 320 letters)
Notes			Remote Control Data Detection Circuit built-in, ROM Correction Circuit built-in
Package			SDIP042-P-0600

Electrical Characteristics

A/D Converter Characteristics

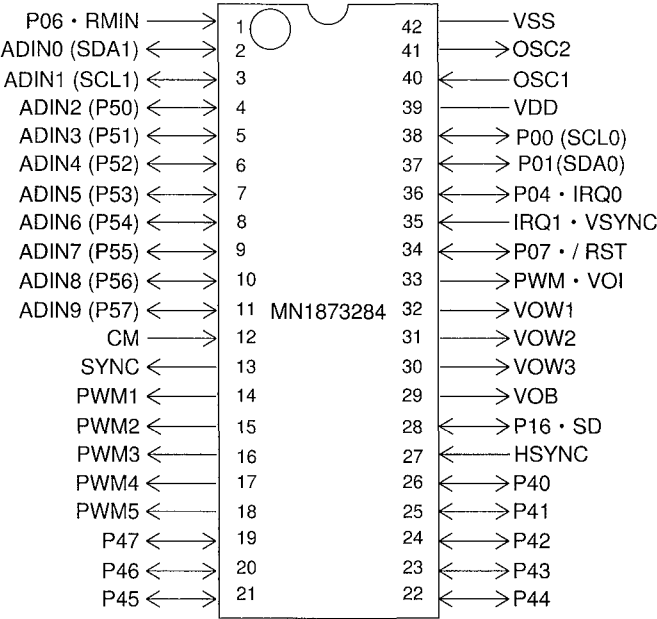
Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
A/D Conversion Time	TAD	fosc=12MHz	9			µs
Analog Input Voltage	VAD		VSS		VDD	V

(Ta= -20 to +70°C, VDD=5.0V, VSS=0V)

Support Tool

In-Circuit Emulator	PX-ICE1870 / 80+PX-PRB1876476		
EPROM built-in Type	Type	MN18P73284	
	ROM (x 8-bit)	32K	
	RAM (x 8-bit)	640	
	Minimum Instruction Execution Time	0.5μs (at 4.5 to 5.5V, 12MHz)	
	Package	SDIP042-P-0600	

Pin Assignment



SDIP042-P-0600