

MN151634

Type		MN151634	
ROM (x8-bit)		16K	
RAM (x4-bit)		512	
Number of Instructions		104	
Minimum Instruction Execution Time		1.0µs (at 4.5 to 5.5V)	
		26.6µs (at 2.2 to 5.5V, 150kHz)	
		53.3µs (at 2.2 to 5.5V, 75kHz)	
Interrupts		• Timer • RMIN/External • Time Base/External • IF/Serial	
Timer Counter		Timer Counter : 8-bit x 1	
		Clock Source	1/2, 1/8, 1/32, 1/128 of System Clock
		Interrupt Source	Overflow of Counter
		Time Base Counter	
		Clock Source	System Clock
		Interrupt Source	Overflow of Counter (4Hz, 1Hz)
		IF Counter	
		Clock Source	FMIF/AMIF Pin Input
		Interrupt Source	Overflow of Counter
Serial Interface		Serial : 8-bit x 1	
		Clock Source	System Clock, NSBT Pin Input
I/O Pins	I/O	30	• Common use 8 • Specified pull-up Resistor available 30 • Nch Open-drain available 30
	Input	12	• Common use 2 • Specified pull-up Resistor available 12
	Output	4	• Nch Open-drain for P92
PLL		for FM	Input Frequency 65 to 130MHz Input Level 200m Vpp min
		for AM/SW	Input Frequency 600kHz to 25MHz Input Level 300m Vpp min.
LCD		32SEG 2/3/4COM (Selectable)	
RMIN		6-bit Programmable (with 8 bit Data Register)	
PWM		6-bit x 1ch (Cycle Time 426 67 µs, fxi=150kHz 853 33µs, fxi=75kHz)	
A/D Inputs		5-bit x 4ch	
Special Ports		Alarm Output 3kHz	
		Buzzer Output 500/750/1250Hz	
Package		QFP100-P-1818A	

Electrical Characteristics

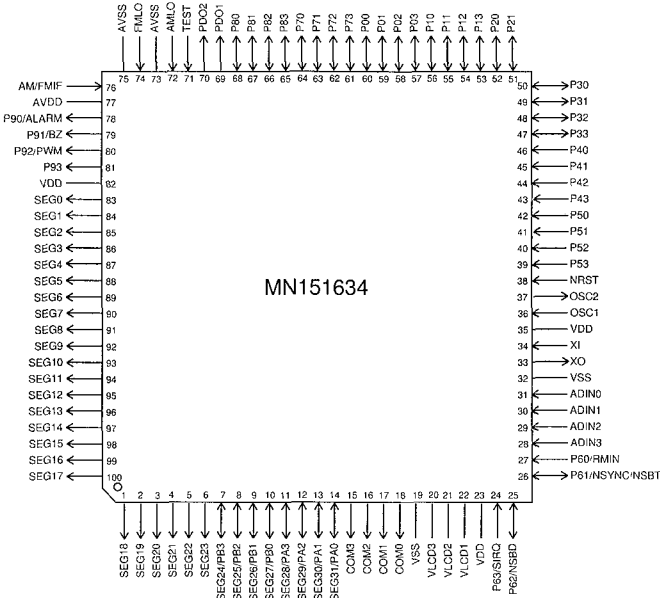
Supply Current

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating Supply Current	IDD1	fosc=8MHz VDD=5V		4.0		mA
	IDD2	fxl=75/150kHz fosc=Stop VDD=5V PLL Work		23	45	mA
	IDD6	fxl=75/150kHz fosc=Stop VDD=5V PLL Stop		0.5	1.0	mA
Supply Current at HALT	IDD3	fosc=8MHz VDD=5V		1.5	3.0	mA
	IDD4	fxl=75/150kHz fosc=Stop VDD=5V		75	150	μA
Supply Current at STOP	IDD5	VDD=5V		5.0	10	μA

(Ta= -30 to +85°C, VDD=5.0V, VSS=0V)

Support Tool

In-Circuit Emulator	PX-ICE1500 + PX-PRB151634
Piggy Back	EP151634 (MN15EV1634 mounted on a special board)
Pin Assignment	



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