

IC INFORMATION

Function		OPTICAL DISC CONTROLLER		MN103S25BDA		1/3	E
Type		CMOS		Model	AVX-7300/UC		

● Pin Functions(MN103S25BDA)

Pin No.	Pin Name	I/O	Function and Operation
1	HDD15	I/O	ATAPI HOST data input/output 15
2	HDD0	I/O	ATAPI HOST data input/output 0
3	HDD14	I/O	ATAPI HOST data input/output 14
4	VDD31		I/O power supply (3.3V)
5	HDD1	I/O	ATAPI HOST data input/output 1
6	HDD13	I/O	ATAPI HOST data input/output 13
7	HDD2	I/O	ATAPI HOST data input/output 2
8	VSS1		GND
9	HDD12	I/O	ATAPI HOST data input/output 12
10	VDD21		Digital power supply (2.7V)
11	HDD3	I/O	ATAPI HOST data input/output 3
12	HDD11	I/O	ATAPI HOST data input/output 11
13	HDD4	I/O	ATAPI HOST data input/output 4
14	HDD10	I/O	ATAPI HOST data input/output 10
15	VDD32		I/O power supply (3.3V)
16	HDD5	I/O	ATAPI HOST data input/output 5
17	HDD9	I/O	ATAPI HOST data input/output 9
18	VSS2		GND
19	HDD6	I/O	ATAPI HOST data input/output 6
20	HDD8	I/O	ATAPI HOST data input/output 8
21	HDD7	I/O	ATAPI HOST data input/output 7
22	VDDH		ATAPI reference power supply (5.0V)
23	RESET		Reset input
24	MASTER	I/O	ATAPI master/slave select input/output
25	INT0	O	System computer interrupt output 0
26	INT1	O	System computer interrupt output 1
27	WAITODC	O	System computer bus wait control output
28	MRST	O	System computer reset output
29	DASPST	I	DASP initialize value input
30	VDD33		I/O power supply (3.3V)
31	NC		Not used
32	P2	O	PORT output
33	UATASEL	I	Internal clock select input
34	VSS3		GND
35	PVSSDRAM1		GND
36	PVDDDRAM1		Digital power supply (2.7V)
37,38	CPUADR17,16	I	System computer address input 17,16
39	PVSSDRAM2		GND
40-43	CPUADR15-12	I	System computer address input 15-12
44	PVDDDRAM2		Digital power supply (2.7V)
45-55	CPUADR11-1	I	System computer address input 11-1
56	VSS4		GND
57	CPUADR0	I	System computer address input 0
58	CS	I	System computer chip select input
59	WR	I	System computer write signal input
60	RD	I	System computer read signal input
61	VDD34		I/O power supply (3.3V)
62,63	CPUDT7,6	I/O	System computer data input/output 7,6
64,65	PTESTDRAM0,1		GND
66	VDD22		Digital power supply (2.7V)
67	VSS5		GND
68-70	CPUDT5-3	I/O	System computer data input/output 5-3
71	VSS6		GND
72-74	CPUDT2-0	I/O	System computer data input/output 2-0
75	CLKOUT1	O	System computer clock output 1
76	VDD35		I/O power supply (3.3V)
77	NC		Not used
78	DTRD	O	CAPA read gate output
79	NC		Not used

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Pin No.	Pin Name	I/O	Function and Operation
80	BDO	I	RF dropout signal input
81	LRCK	I	LR identifier signal input
82	SBBCLK	I	CD sub code block clock input
83	VSS7		GND
84	MMOD		GND
85	RST	I	System reset input
86	VDD23		Digital power supply (2.7V)
87	CLKOUT2	O	System computer clock output 2
88	PLLOK	O	Frame mark detect signal output
89	NC		Not used
90	DACLCK	O	LR identifier signal output
91	DACDATA	O	Serial output
92	TRON	I	Tracking ON input
93,94	NC		Not used
95	DACCLK	I	1bit DAC Serial output clock input
96	IPFLG	I	interpolation flag input
97	TX	I	TX input
98	LRCK	I	VSS
99	VSS8		GND
100	OSCI1	I	Oscillator pin input
101	OSCO1	O	Oscillator pin output
102	VDD36		I/O power supply (3.3V)
103	PVSS		GND
104	PVDD		I/O power supply (3.3V)
105	P1		VDD3
106	P0		VDD3
107	VSS9		GND
108	NC		Not used
109	SUBC	I	Sub code serial input
110	XCLDCK	I	Sub code Frame clock input
111	CHCK4	I	Read clock input to DAT3-0
112-115	DAT3-0	I	Read data input from DISC 3-0
116	VDD37		I/O power supply (3.3V)
117	SCLOCK	I/O	Pull up
118	SDATA	I/O	Pull up
119-122	MONI3-0	O	Internal signal monitor output 3-0
123	VSS10		GND
124	EJECT	I	Eject detect input
125	VDD24		Digital power supply (2.7V)
126	TRYCL	I	Tray close detect input
127	DASP	I/O	ATAPI drive active/slave connect input/output
128	CS3FX	I	ATAPI HOST chip select input
129	CS1FX	I	ATAPI HOST chip select input
130	VDD38		I/O power supply (3.3V)
131	DA2	I/O	ATAPI HOST address input/output 2
132	DA0	I/O	ATAPI HOST address input/output 0
133	PDIAG	I/O	ATAPI slave/master diagnosis input/output
134	VSS11		GND
135	DA1	I/O	ATAPI HOST address input/output 1
136	IOCS16	O	ATAPI HOST data bus width select output
137	INTRQ	O	ATAPI HOST interrupt request output
138	VDD39		I/O power supply (3.3V)
139	DMACK	I	ATAPI HOST DMA response input
140	IORDY	O	ATAPI HOST ready output
141	IORD	I/O	ATAPI HOST data read input/output
142	VSS12		GND
143	IOWR	I/O	ATAPI HOST data write input/output
144	DMARQ	O	ATAPI HOST DMA request output

IC INFORMATION

		MN103S25BDA		
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Type	CMOS	Model	AVX-7300/UC	E

