

MN101C273

Type		MN101C273 (under development)	
ROM (x8-bit)		4K	
RAM (x8-bit)		512	
Minimum Instruction Execution Time		0.25 μs (at 2.7 to 5.5V, 8MHz) 1.00 μs (at 2.0 to 5.5V, 2MHz)* * The lower limit for operation guarantee for EPROM built-in version is 2.7V.	
Interrupts		• RESET • Watchdog • External 0 • External 1 • Timer 2 • Timer 3 • Serial 0 • A/D Conversion finish	
Timer Counter		Timer Counter 2 : 8-bit x 1 (Square-wave/8-bit PWM Output, Event Count, Synchronous Output Event) Clock Source 1/1, 1/4 of System Clock, External Clock Input Interrupt Source Coincidence with Compare Register 2 Timer Counter 3 : 8-bit x 1 (Square-wave Output, Event Count, Generation of Remote Control Carrier, Serial 0 Baud Rate Timer) Clock Source 1/4, 1/16 of System Clock, 1/1 of OSC Oscillation Clock, External Clock Input Interrupt Source .Coincidence with Compare Register 3 Timer Counter 2, 3 can be cascade-connected. Watchdog Timer Interrupt Source 1/1048576 of System Clock	
Serial Interface		Serial 0 : 8-bit x 1 (Synchronous Type/Simple UART[Half-duplex]) Clock Source 1/2, 1/4, 1/16 of System Clock 1/2 of Timer Counter 3	
I/O Pins	I/O	16	• Common use • Specified pull-up Resistor available • Input/Output selectable (bit unit)
	Input	6	• Common use • Specified pull-up Resistor available
A/D Inputs		10-bit x 4ch (with S/H)	
Special Ports		Remote Control Carrier Signal Output, High-current Drive Port	
Package		DIP028-P-0600 (under development), SDIP028-P-0600 (under planning)	
Electrical Characteristics			

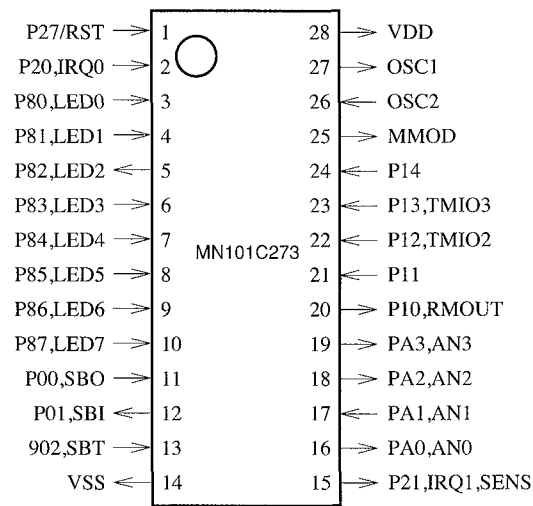
Supply Current

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating Supply Current	IDD1	fosc = 8 39MHz, VDD = 5V			25	mA
Supply Current at STOP	IDD2	VDD = 5V, Ta = 25°C			2	μ A
		VDD = 5V, Ta = -40 to +85°C			20	μ A

Support Tool

In-Circuit Emulator	PX-ICE101C / D + PX-PRB101C27-C / D	
EPROM built-in Type	Type	MN101CP273 (under development)
	ROM (x 8-bit)	4K
	RAM (x 8-bit)	512
	Minimum Instruction Execution Time	0.25μs (at 2.7 to 5.5V, 8MHz)
	Package	DIP028-P-0600 (under development), SDIP028-P-0600 (under planning)

Pin Assignment



DIP028-P-0600 (under development)
SDIP028-P-0600 (under planning)