

Preliminary Information

# Low rDS(on) Small-Signal MOSFETs TMOS Single N-Channel Field Effect Transistors

Part of the GreenLine<sup>™</sup> Portfolio of devices with energyconserving traits.

These miniature surface mount MOSFETs utilize Motorola's High Cell Density, HDTMOS process. Low  $r_{DS(on)}$  assures minimal power loss and conserves energy, making this device ideal for use in small power management circuitry. Typical applications are dc-dc converters, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r<sub>DS(on)</sub> Provides Higher Efficiency and Extends 30 Battery Life
- Miniature TSOP 6 Surface Mount Package Saves Board Space

MAXIMUM RATINGS (T = 25°C unless otherwise noted)

· Visit our web site at http://www.mot-sps.com/ospd

# OSFETs

CASE 318G-01, STYLE 1 TSOP 6 PLASTIC

MGSF3442V

Motorola Preffered Device

N-CHANNEL ENHANCEMENT-MODE

TMOS MOSFET r<sub>DS(0N)</sub> =58mΩ (TYP)



| Rating   | Symbol                             | Value      | Unit |
|--|------------------------------------|------------|------|
| Drain-to-Source Voltage  | V <sub>DSS</sub>                   | 20         | Vdc  |
| Gate-to-Source Voltage Continuous  | V <sub>GS</sub>                    | ±8         | Vdc  |
| Drain Current Continuous @ $T_A = 25^{\circ}C$<br>Pulsed Drain Currrent (t $_p \le 10\mu$ s) | I <sub>D</sub><br>И <sub>D</sub> М | 4.0<br>20  | A    |
| Total Power Dissipation @ $T_A = 25^{\circ}C$ Mounted on FR4 t $\leq$ 5 sec                  | PD                                 | 2.0        | W    |
| Operating and Storage Temperature Range  | T J, Tstg                          | -55 to 150 | °C   |
| Thermal Resistance Junction-to-Ambient   | R <sub>θJA</sub>                   | 62.5       | °C/W |
| Maximum Lead Temperature for Soldering Purposes, for 10 seconds                              | TL                                 | 260        | °C   |

### ORDERING INFORMATION

| Device       | Reel Size | Tape Width        | Quantity |
|--------------|-----------|-------------------|----------|
| MGSF3442VLT1 | 7"        | 8mm Embosed tape  | 3000     |
| MGSF3442VLT3 | 13"       | 8mm embossed tape | 10,000   |

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Thermal Clad is a trademark of the Bergquist Company.

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Preferred devices are Motorola recommended choices for future use and best overall value.

## MGSF3442X

### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless other noted)

| Characteristic  |  | Symbol               | Min | Тур            | Max            | Unit |
|---|--|----------------------|-----|----------------|----------------|------|
| OFF CHARACTERISTICS   |  | -                    |     |                |                |      |
| Drain-to-Source Breakdown Voltage<br>(V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 10μΑ)   |  | V <sub>(BR)DSS</sub> | 20  | -              | -              | Vdc  |
| Zero Gate Voltage Drain Current<br>$(V_{DS} = 20 \text{ Vdc}, V_{GS} = 0 \text{ Vdc})$<br>$(V_{DS} = 20 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_J = 70^{\circ}\text{C})$ |  | IDSS                 |     | -              | 1.0<br>5       | μAdc |
| Gate-Body Leakage Current ( $V_{GS} = \pm 8 \text{ Vdc}, V_{DS} = 0$ )  |  | I <sub>GSS</sub>     | -   |                | ±100           | nAdc |
| ON CHARACTERISTICS(1)   |  |                      |     |                | -              | -    |
| Gate Threaded Voltage<br>(V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μAdc)  |  | V <sub>GS(th)</sub>  | 0.6 | -              |                | Vdc  |
| Static Drain-to-Source On-Resistance<br>$(V_{GS} = 4.5 \text{ Vdc}, I_D = 4.0 \text{ A})$<br>$(V_{GS} = 2.5 \text{ Vdc}, I_D = 3.4 \text{ A})$                          |  | <sup>r</sup> DS(on)  |     | 0.058<br>0.072 | 0.070<br>0.095 | Ohms |
| YNAMIC CHARACTERIS  | TICS   |                      |     |                |                |      |
| Input Capacitance   | (V <sub>DS</sub> = 5.0 V)                                | C <sub>ISS</sub>     | I   | 90             | -              | pF   |
| Output Capacitance  | (V <sub>DS</sub> = 5.0V)                                 | C <sub>OSS</sub>     | I   | 50             | -              |      |
| Transfer Capacitance  | (V <sub>DG</sub> = 5.0V)                                 | C <sub>rss</sub>     | I   | 10             | -              |      |
| WITCHING CHARACTER  | STICS (2)  |                      |     |                |                |      |
| Turn-On Delay Time  |  | t <sub>d(on)</sub>   | -   | 8              | 20             | ns   |
| Rise Time   | (V <sub>DD</sub> = 10 Vdc, I <sub>D</sub> = 1.0 A,       | t <sub>r</sub>       |     | 24             | 40             |      |
| Turn-Off Delay Time   | $V_{\text{GEN}} = 10V \text{ R}_{\text{L}} = 10\Omega$ ) | t <sub>d(off)</sub>  |     | 36             | 60             |      |
| Fall Time   |  | t <sub>f</sub>       |     | 10             | 20             |      |
| Gate Charge   |  | QT                   |     | -              | -              | nC   |
| OURCE-DRAIN DIODE CI  | IARACTERISTICS   |                      |     |                |                |      |
| Continuous Current  |  | IS                   | -   | -              | 1.0            | А    |
| Pulsed Current  |  | I <sub>SM</sub>      | -   | _              | 5.0            | А    |
| Forward Voltage <sup>(2)</sup>  |  | V <sub>SD</sub>      | -   | -              | 1.2            | v    |

(1) Pulse Test: Pulse Width  $\leq$  300 µs, Duty cycle  $\leq$  2%.

(2) Switching characteristics are independent of operating junction temperature.

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