



## Ultrahigh-Speed Switching Applications

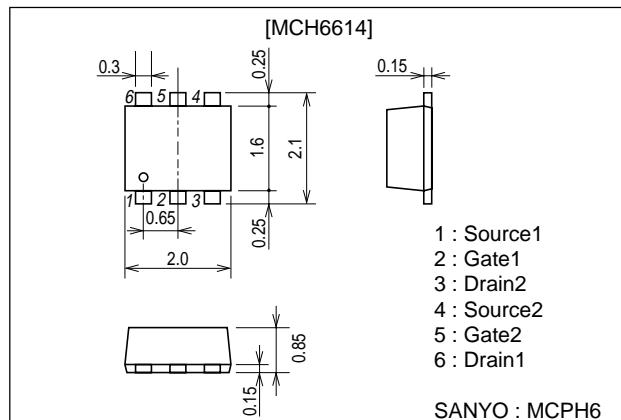
### Features

- The MCH6614 incorporates two elements that are an N-channel and a P-channel MOSFETs that feature low ON resistance and high-speed switching, thereby enabling high-density mounting.
- Low ON-resistance.
- 2.5V drive.

### Package Dimensions

unit : mm

2173



### Specifications

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	N-channel	P-channel	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		30	-30	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±10	±10	V
Drain Current (DC)	I <sub>D</sub>		0.35	-0.4	A
Drain Current (Pulse)	I <sub>DP</sub>	PW≤10μs, duty cycle≤1%	1.4	-1.6	A
Allowable Power Dissipation	P <sub>D</sub>	Mounted on a ceramic board (900mm <sup>2</sup> ×0.8mm)1unit	0.8		W
Channel Temperature	T <sub>ch</sub>		150		°C
Storage Temperature	T <sub>stg</sub>		-55 to +150		°C

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
<b>[N-channel]</b>						
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> =1mA, V <sub>GS</sub> =0	30			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0			10	μA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0			±10	μA
Cutoff Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =100μA	0.4		1.3	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =80mA	150	220		mS
Static Drain-to-Source On-State Resistance	R <sub>D(S(on)1</sub>	I <sub>D</sub> =80mA, V <sub>GS</sub> =4V		2.9	3.7	Ω
	R <sub>D(S(on)2</sub>	I <sub>D</sub> =40mA, V <sub>GS</sub> =2.5V		3.7	5.2	Ω
	R <sub>D(S(on)3</sub>	I <sub>D</sub> =10mA, V <sub>GS</sub> =1.5V		6.4	12.8	Ω

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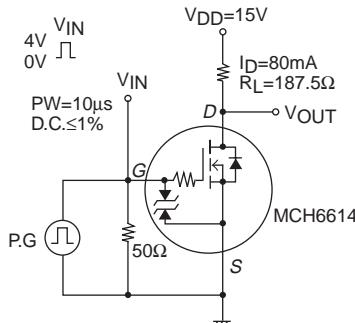
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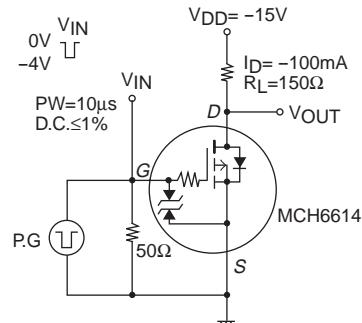
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	C <sub>iss</sub>	V <sub>D</sub> S=10V, f=1MHz		7.0		pF
Output Capacitance	C <sub>oss</sub>	V <sub>D</sub> S=10V, f=1MHz		5.9		pF
Reverse Transfer Capacitance	C <sub>rss</sub>	V <sub>D</sub> S=10V, f=1MHz		2.3		pF
Turn-ON Delay Time	t <sub>d(on)</sub>	See specified Test Circuit		19		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		65		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>	See specified Test Circuit		155		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit		120		ns
Total Gate Charge	Q <sub>g</sub>	V <sub>D</sub> S=10V, V <sub>G</sub> S=10V, I <sub>D</sub> =150mA		1.58		nC
Gate-to-Source Charge	Q <sub>gs</sub>	V <sub>D</sub> S=10V, V <sub>G</sub> S=10V, I <sub>D</sub> =150mA		0.26		nC
Gate-to-Drain "Miller" Charge	Q <sub>gd</sub>	V <sub>D</sub> S=10V, V <sub>G</sub> S=10V, I <sub>D</sub> =150mA		0.31		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =150mA, V <sub>G</sub> S=0		0.87	1.2	V
[P-channel]						
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> =-1mA, V <sub>G</sub> S=0	-30			V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>D</sub> S=-30V, V <sub>G</sub> S=0			-10	μA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>G</sub> S=±8V, V <sub>D</sub> S=0			±10	μA
Cutoff Voltage	V <sub>G</sub> S(off)	V <sub>D</sub> S=-10V, I <sub>D</sub> =-100μA	-0.4		-1.4	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>D</sub> S=-10V, I <sub>D</sub> =-100mA	210	300		mS
Static Drain-to-Source On-State Resistance	R <sub>D</sub> S(on)1	I <sub>D</sub> =-100mA, V <sub>G</sub> S=-4V		2.4	3.1	Ω
	R <sub>D</sub> S(on)2	I <sub>D</sub> =-50mA, V <sub>G</sub> S=-2.5V		3.5	4.9	Ω
	R <sub>D</sub> S(on)3	I <sub>D</sub> =-10mA, V <sub>G</sub> S=-1.5V		10	20	Ω
Input Capacitance	C <sub>iss</sub>	V <sub>D</sub> S=-10V, f=1MHz	28			pF
Output Capacitance	C <sub>oss</sub>	V <sub>D</sub> S=-10V, f=1MHz	15			pF
Reverse Transfer Capacitance	C <sub>rss</sub>	V <sub>D</sub> S=-10V, f=1MHz	5.2			pF
Turn-ON Delay Time	t <sub>d(on)</sub>	See specified Test Circuit	24			ns
Rise Time	t <sub>r</sub>	See specified Test Circuit	75			ns
Turn-OFF Delay Time	t <sub>d(off)</sub>	See specified Test Circuit	200			ns
Fall Time	t <sub>f</sub>	See specified Test Circuit	150			ns
Total Gate Charge	Q <sub>g</sub>	V <sub>D</sub> S=-10V, V <sub>G</sub> S=-10V, I <sub>D</sub> =-200mA	2			nC
Gate-to-Source Charge	Q <sub>gs</sub>	V <sub>D</sub> S=-10V, V <sub>G</sub> S=-10V, I <sub>D</sub> =-200mA	0.25			nC
Gate-to-Drain "Miller" Charge	Q <sub>gd</sub>	V <sub>D</sub> S=-10V, V <sub>G</sub> S=-10V, I <sub>D</sub> =-200mA	0.35			nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-200mA, V <sub>G</sub> S=0		-0.82	-1.2	V

## Switching Time Test Circuit

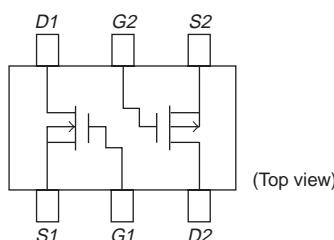
[N-channel]



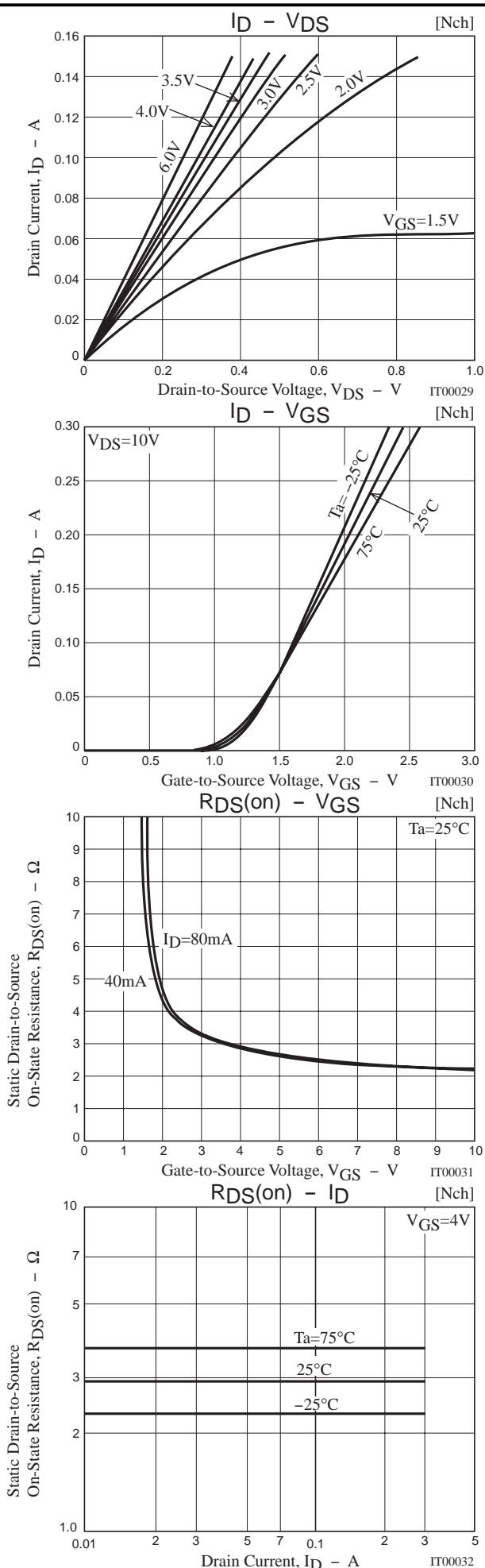
[P-channel]

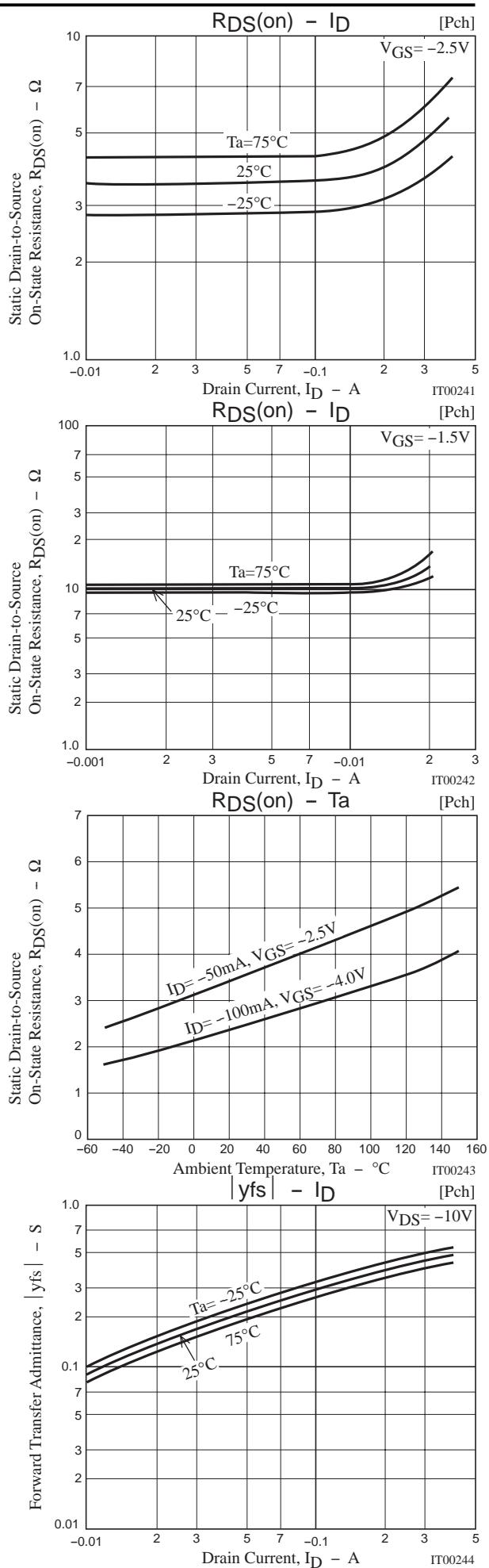
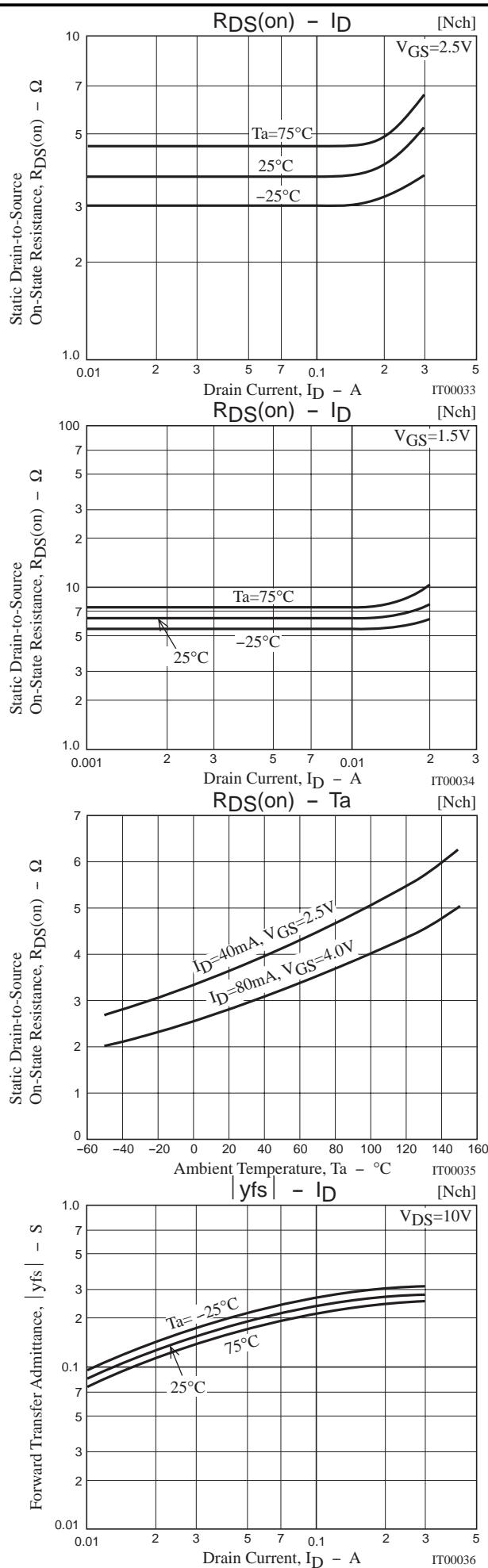


## Electrical Connection

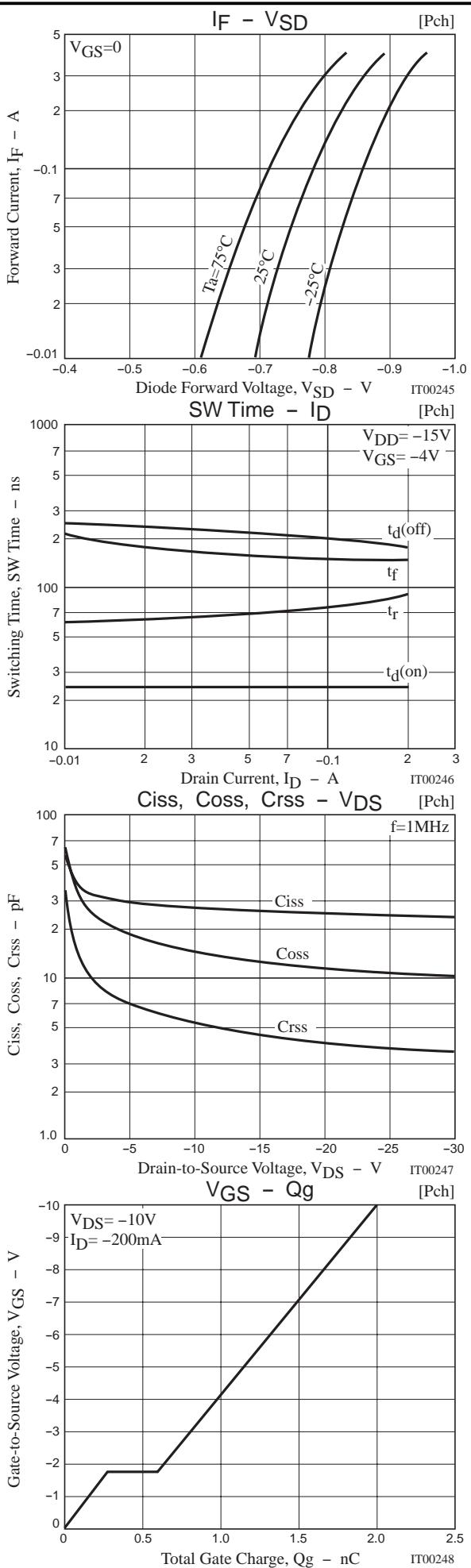
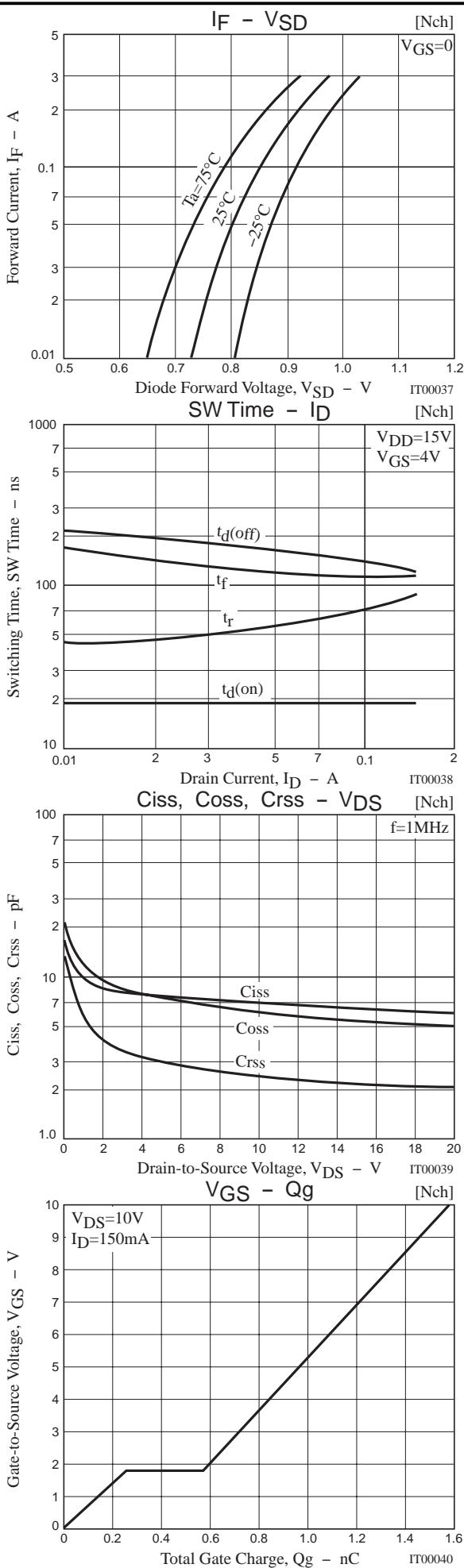


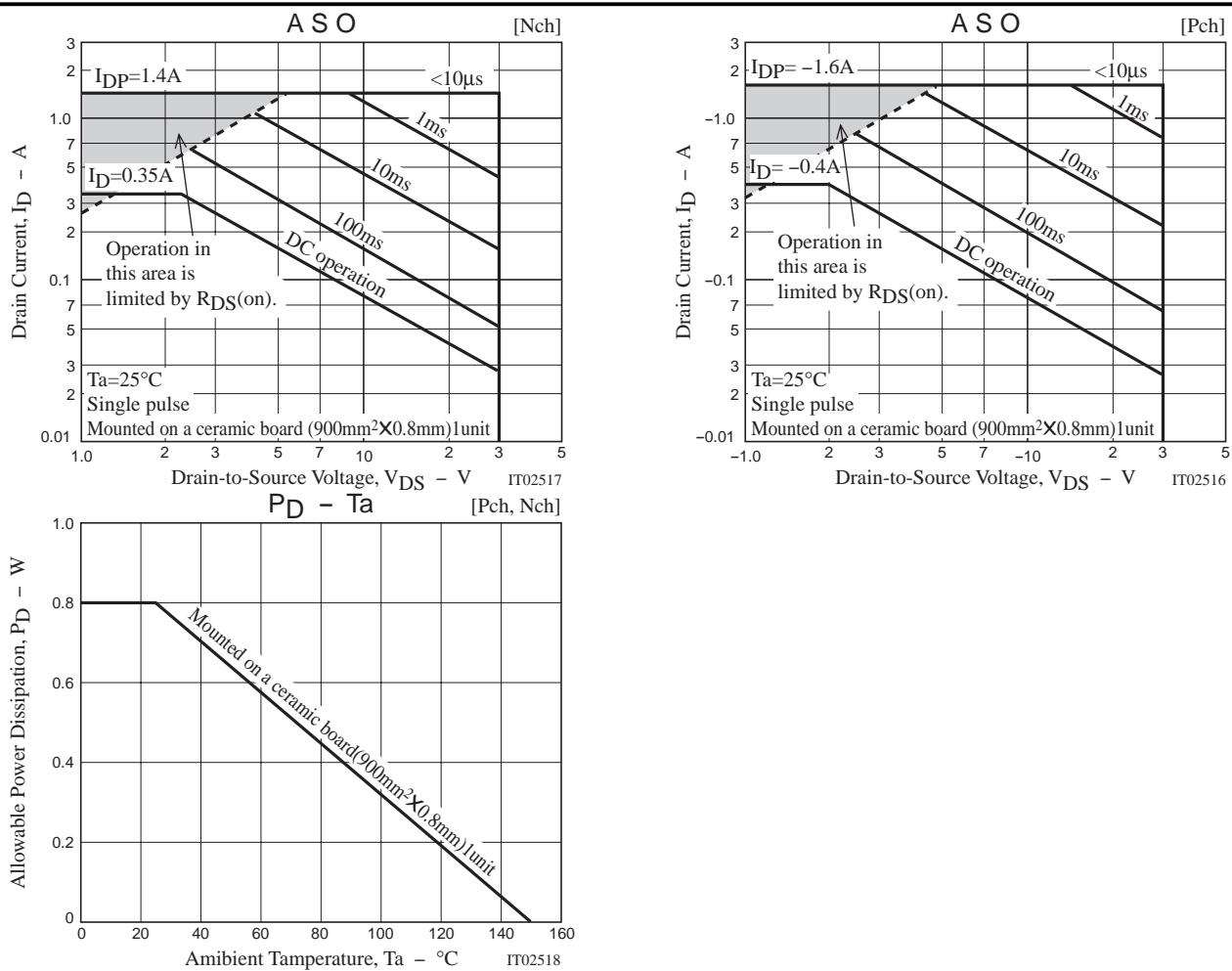
# MCH6614





# MCH6614





Note on usage : Since the MCH6614 is designed for high-speed switching applications, please avoid using this device in the vicinity of highly charged objects.

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