



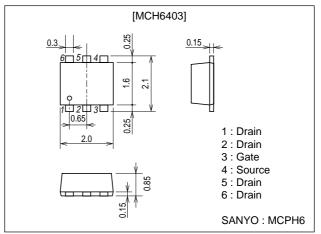
Ultrahigh-Speed Switching Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- 2.5V drive.

Package Dimensions

unit : mm 2193



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		20	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ΙD		3	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	12	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm ² X0.8mm)	1.4	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	20			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	0.4		1.3	٧
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =1.5A	3.3	4.8		S
Static Drain-to-Source On-State Resistance	RDS(on)1	I _D =1.5A, V _{GS} =4V		80	105	mΩ
	RDS(on)2	I _D =0.7A, V _{GS} =2.5V		110	155	mΩ
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		200		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		70		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		45		pF

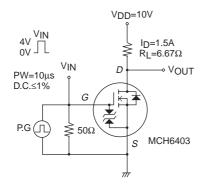
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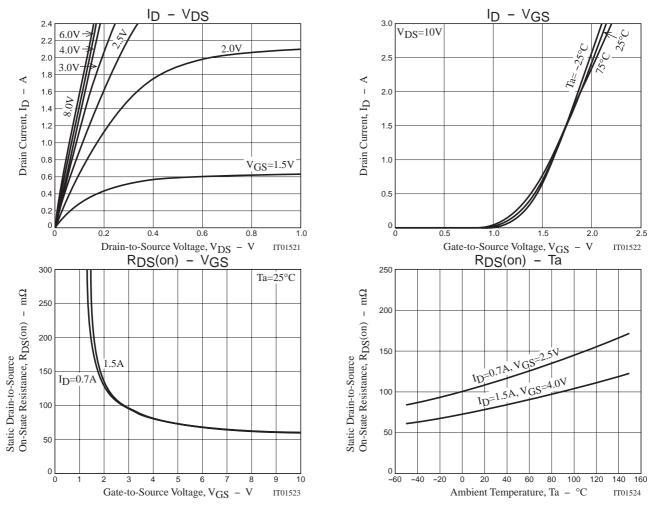
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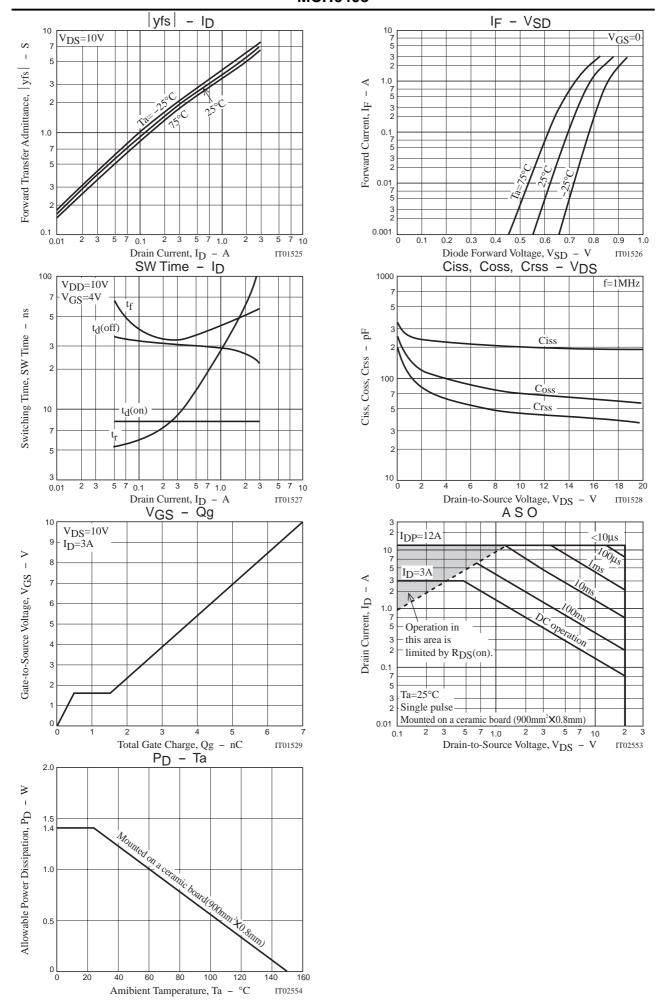
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	O IIII
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		8		ns
Rise Time	t _r	See specified Test Circuit		45		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit		27		ns
Fall Time	tf	See specified Test Circuit		45		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =3A		7		nC
Gate-to-Source Charge	Qgs	VDS=10V, VGS=10V, ID=3A		0.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =3A		1		nC
Diode Forward Voltage	V _{SD}	I _S =3A, V _{GS} =0		0.87	1.2	V

Switching Time Test Circuit







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