SBD: Schottky Barrier Diode



MCH5805

DC / DC Converter Applications

Features

 Composite type with a P-channel sillicon MOSFET (MCH3314) and a Schottky barrier diode (SB01-05) contained in one package facilitating high-density mounting.

[MOSFET]

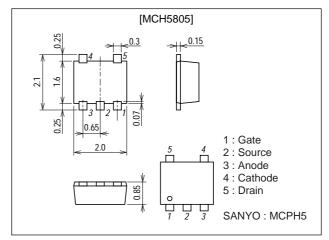
- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 4V drive.

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- Short reverse recovery time.
- · Low forward voltage.

Package Dimensions

unit : mm 2195



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit					
[MOSFET]									
Drain-to-Source Voltage	VDSS		-60	V					
Gate-to-Source Voltage	VGSS		±20	V					
Drain Current (DC)	ΙD		-0.6	Α					
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-2.4	Α					
Allowable Power Dissipation	P_{D}	Mounted on a ceramic board (900mm ² X0.8mm) 1unit	0.8	W					
Channel Temperature	Tch		150	°C					
Storage Temperature	Tstg		-55 to +125	°C					
[SBD]									
Repetitive Peak Reverse Voltage	VRRM		50	V					
Nonrepetitive Peak Reverse Surge Voltage	VRSM		50	V					
Average Output Current	lo		0.1	Α					
Surge Forward Current	IFSM	50Hz sine wave, 1 cycle	2	Α					
Junction Temperature	Tj		-55 to +125	°C					
Storage Temperature	Tstg		-55 to +125	°C					

Marking : QE

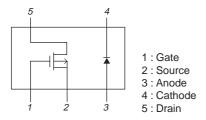
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Electrical Characteristics at Ta=25°C

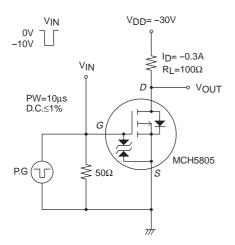
Parameter	Symbol	Conditions	Ratings			Unit			
			min	typ	max	Offic			
[MOSFET]									
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0	-60			V			
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-60V, V _{GS} =0			-1	μΑ			
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μΑ			
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	-1.2		-2.6	V			
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-0.3A	460	670		mS			
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =-0.3A, V _G S=-10V		1.3	1.7	Ω			
	R _{DS} (on)2	I _D =-0.2A, V _G S=-4V		1.6	2.3	Ω			
Input Capacitance	Ciss	V _{DS} =-20V, f=1MHz		73		pF			
Output Capacitance	Coss	V _{DS} =-20V, f=1MHz		7		pF			
Reverse Transfer Capacitance	Crss	V _{DS} =-20V, f=1MHz		4		pF			
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		6		ns			
Rise Time	t _r	See specified Test Circuit.		3.5		ns			
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		12.5		ns			
Fall Time	tf	See specified Test Circuit.		3		ns			
Total Gate Charge	Qg	V _{DS} =-10V, V _{GS} =-10V, I _D =-0.6A		2.4		nC			
Gate-to-Source Charge	Qgs	V _{DS} =-10V, V _{GS} =-10V, I _D =-0.6A		0.6		nC			
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =-10V, V _{GS} =-10V, I _D =-0.6A		0.2		nC			
Diode Forward Voltage	V _{SD}	I _S =-0.6A, V _G S=0		-0.88	-1.2	V			
[SBD]			'		'				
Reverse Voltage	٧R	I _R =50μA	50			V			
Forward Voltage	V _F 1	I _F =100mA			0.55	V			
Reverse Current	IR	V _R =25V			15	μА			
Interterminal Capacitance	С	V _R =10V, f=1MHz		4.4		pF			
Reverse Recovery Time	t _{rr}	I _F =I _R =100mA, See specified Test Circuit.			10	ns			

Electrical Connection (Top view)



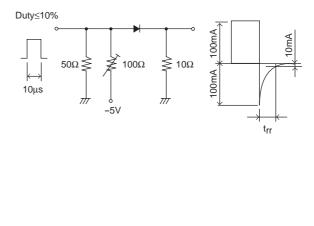
Switching Time Test Circuit

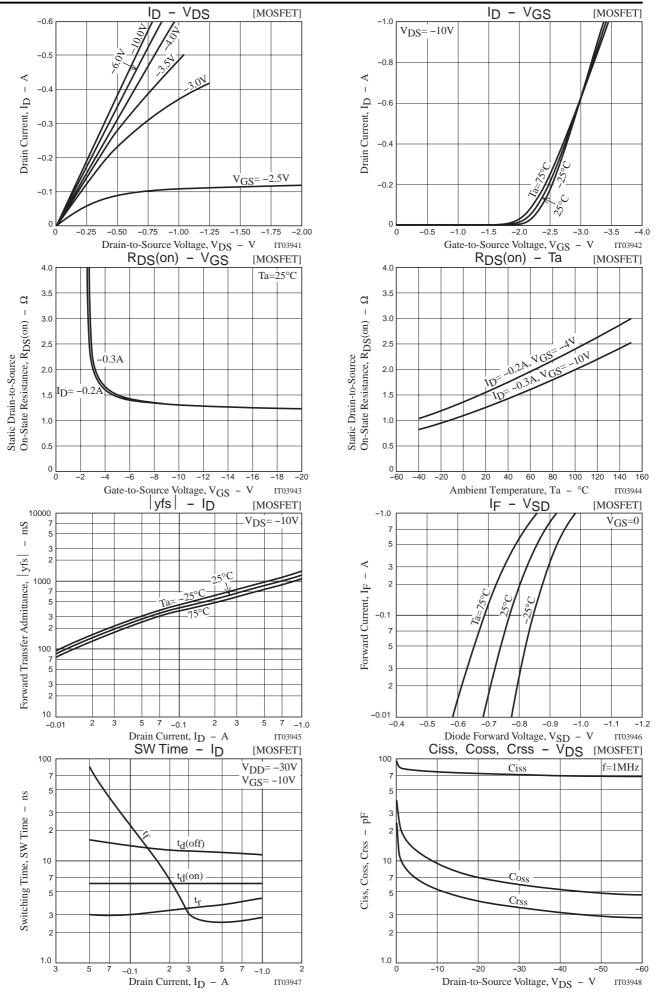
[MOSFET]



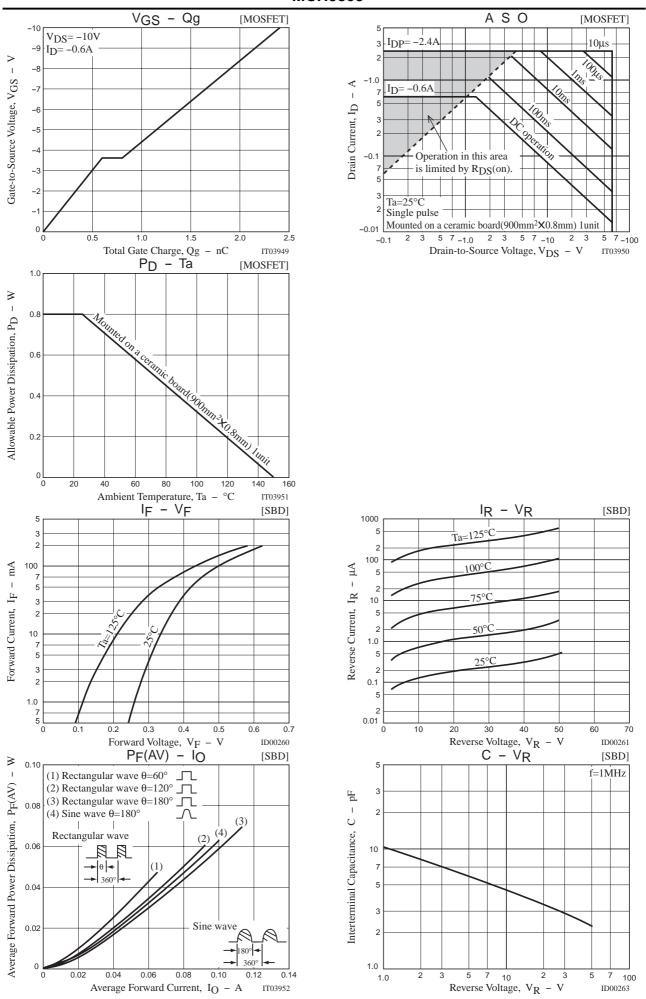
trr Test Circuit

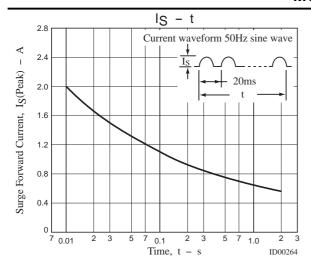
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