



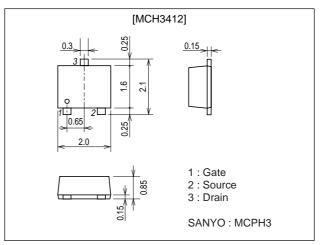
Ultrahigh-Speed Switching Applications

Features

- · Low ON-resinstance.
- · Ultrahigh-speed switching.
- 4V drive.

Package Dimensions

unit : mm 2167



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		3	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	12	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm ² X0.8mm)	1	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +125	°C

Electrical Characteristics at Ta=25°C

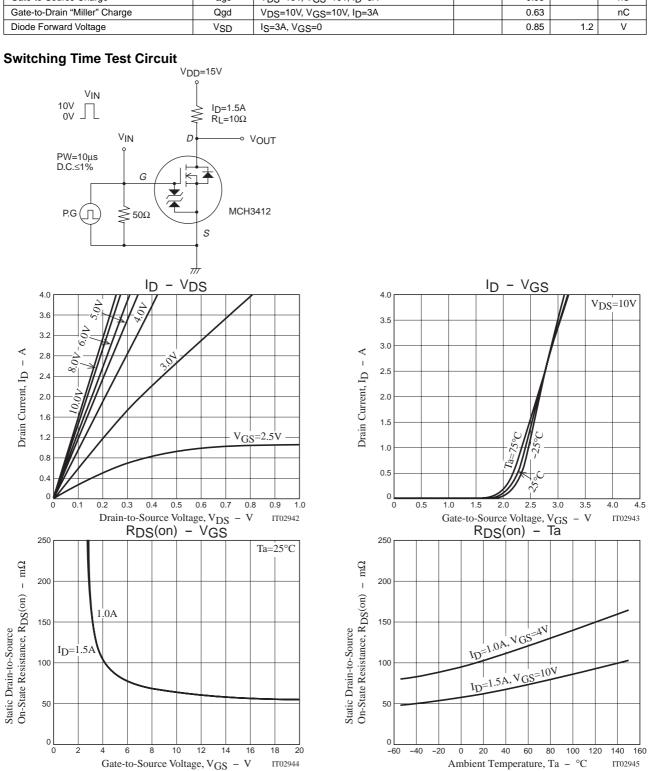
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	01111
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	30			٧
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.2		2.6	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =1.5A	2.1	3		S

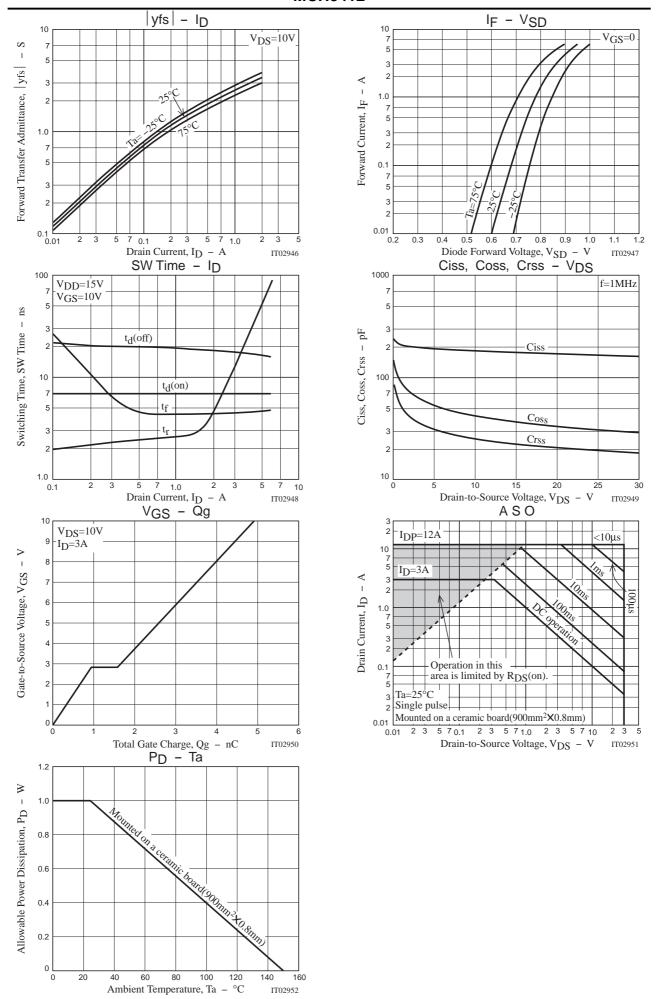
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =1.5A, V _{GS} =10V		64	84	$m\Omega$
	R _{DS} (on)2	I _D =1A, V _{GS} =4V		105	150	mΩ
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		180		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		42		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		25		pF
Turn-ON Delay Time	td(on)	See specified Test Circuit		7		ns
Rise Time	t _r	See specified Test Circuit		2.8		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		18.5		ns
Fall Time	tf	See specified Test Circuit		4.4		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =3A		4.9		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =3A		0.93		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =3A		0.63		nC
Diode Forward Voltage	V _{SD}	I _S =3A, V _G S=0		0.85	1.2	V





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