

UTC MC1458 LINEAR INTEGRATED CIRCUIT

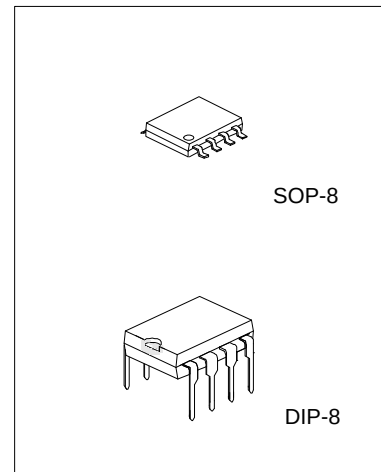
DUAL OPERATIONAL AMPLIFIERS

DESCRIPTION

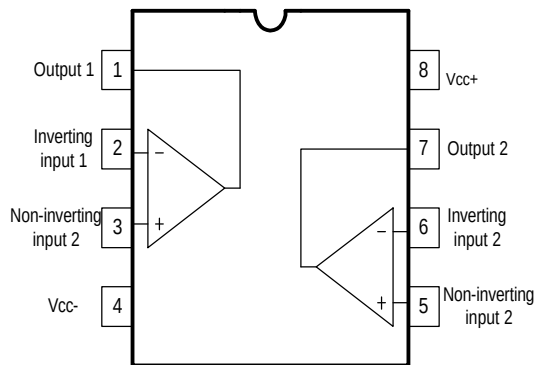
The MC1458 is a high performance monolithic dual operational amplifier constructed on a single silicon chip. It is intended for a wide range of analog applications. (Summing amplifier, Voltage follower, integrator, Active filter, function generator) The high gain and wide range of operating voltages provide superior performance in integrator, summing amplifier, and general feed back applications.

FEATURES

- *Low power consumption
- *Large input voltage range
- *No latch-up
- *High gain
- *Short-circuit protection
- *No frequency compensation required

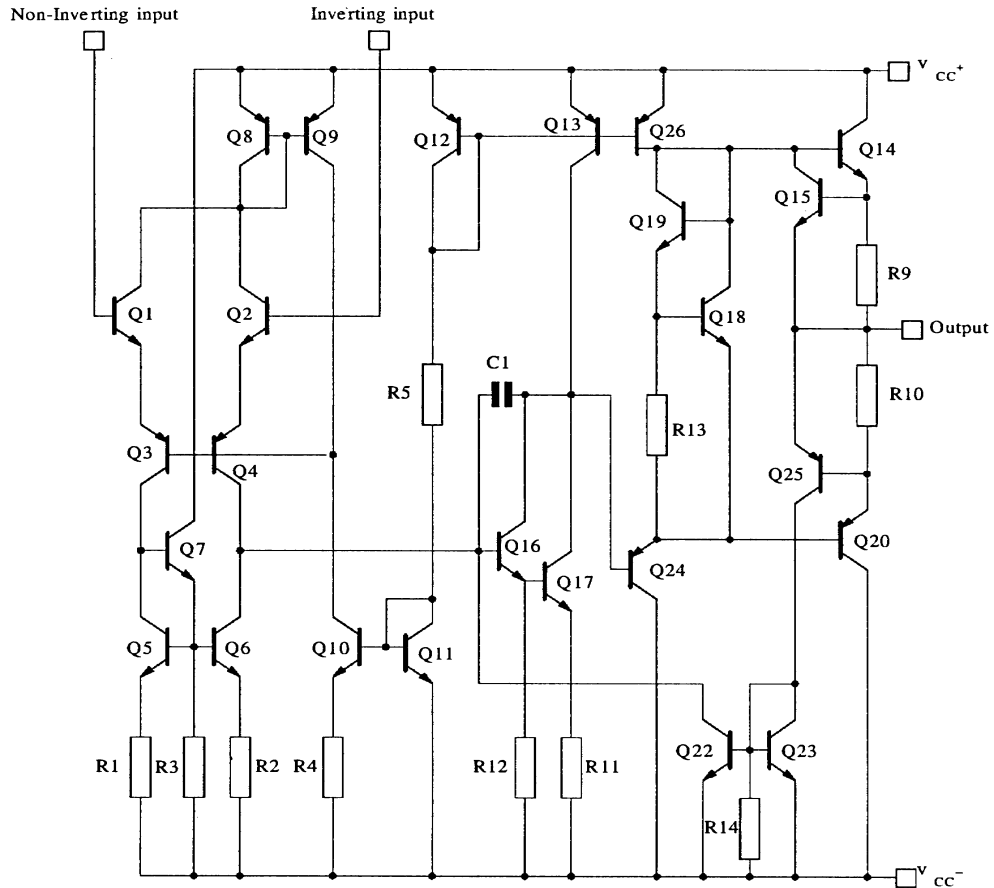


PING CONFIGURATIONS



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TEST CIRCUIT



ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	Vcc	-22 to +22	V
Differential Input Voltage	Vid	-30 to +30	V
Input Voltage	Vi	-15 to +15	V
Power Dissipation	Ptot	300 (SOP8) 500 (DIP8)	mW
Output Short Circuit Duration		Infinite	
Operating Free-air Temperature Range	Toper	0 to 70	°C
Storage Temperature Range	Tstg	-65 to +150	°C

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ELECTRICAL CHARACTERISTICS(VCC=+15V,Tamb=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Input Offset Voltage (RS<=10kΩ)	V _{IO}	T _{AMB} =+25°C		1	5	mV
		0°C <=T _{AMB} <=70°C			6	mV
Input Offset Current	I _{IO}	T _{AMB} =+25°C		2	200	nA
		0°C <=T _{AMB} <=70°C			300	nA
Input Bias Current	I _{IB}	T _{AMB} =+25°C		30	500	nA
		0°C <=T _{AMB} <=70°C			800	nA
Large Signal Voltage Gain (V _O =+10V,RL=2kΩ)	A _{VD}	T _{AMB} =+25°C	50	200		V/mV
		0°C <=T _{AMB} <=70°C	25			V/mV
Supply Voltage Rejection Ratio (RS<=10kΩ)	SVR	T _{AMB} =+25°C	77	90		dB
		0°C <=T _{AMB} <=70°C	77			dB
Supply Current, all Amp, no Load	I _{CC}	T _{AMB} =+25°C		2.3	5	mA
		0°C <=T _{AMB} <=70°C			6	mA
Input Common Mode Voltage Range	V _{ICM}	T _{AMB} =+25°C	+12			V
		0°C <=T _{AMB} <=70°C	+12			V
Common-mode Rejection Ratio (RS<=10kΩ)	CMR	T _{AMB} =+25°C	70	90		dB
		0°C <=T _{AMB} <=70°C	70			dB
Output Short-circuit Current	I _{OS}	T _{AMB} =+25°C	10	20	35	mA
Output Voltage Swing	+V _{OPP}	T _{AMB} =+25°C,RL=10kΩ	12	14		V
		T _{AMB} =+25°C,RL=2kΩ	10	13		V
		0°C <=T _{AMB} <=70°C, RL=10kΩ	12			V
		0°C <=T _{AMB} <=70°C, RL=2kΩ	10			V
Slew Rate	SR	V _I =+10V,RL=2kΩ,CL=100pF, T _{AMB} =+25°C, unity gain	0.2	0.8		V/μs
Rise Time	t _r	V _I =20mV,RL=2kΩ,CL=100pF, T _{AMB} =+25°C, unity gain		0.3		μs
Overshoot	K _{OV}	V _I =20mV,RL=2kΩ,CL=100pF, T _{AMB} =+25°C, unity gain		5		%
Input resistance	R _J		0.3	2		MΩ
Common-mode Input Impedance	Z _{IC}			200		MΩ
Input Capacitance	C _I			1.4		pF
Output Resistance	R _O			75		Ω
Full Power Bandwidth	FPB	RL=2kΩ, V _O >=+10V, A _{VD} =1, THD<=5%		14		KHz
Unity Gain bandwidth	B	V _I =10mV,RL=2kΩ,CL=100pF, T _{AMB} =+25°C		1		MHz
Gain bandwidth Product	GBP	V _I =10mV,RL=2kΩ,CL=100pF, t=100kHz, T _{AMB} =+25°C	0.4	1		MHz
Total harmonic distortion	THD	F=1kHz, A _v =20dB,RL=2kΩ, V _O =2Vpp,CL=100pF,Tamb=25°C		0.02		%
Equivalent Input Noise voltage	e _n	F=kHz,RS=100Ω		45		nV √ Hz
Phase Margin	φ _m			65		Degress
Gain Margin	A _m			11		dB
Channel Separation	V _{O1} /V _{O2}			120		dB