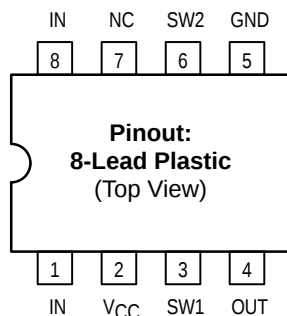


2.8GHz Prescaler

The MC12079 is a single modulus divide by 64, 128, 256 prescaler for low power frequency division of a 2.8GHz (typical) high frequency input signal. Divide ratio control inputs SW1 and SW2 select the required divide ratio of $\div 64$, $\div 128$, or $\div 256$.

An external load resistor is required to terminate the output. A 1.2k Ω resistor is recommended to achieve a 1.6V_{pp} output swing, when dividing a 1.1GHz input signal by the minimum divide ratio of 64, assuming a 12pF load. Output current can be minimized dependent on conditions such as output frequency, capacitive load being driven, and output voltage swing required. Typical values for load resistors are included in the V_{out} specification for various divide ratios at 2.8GHz input frequency.

- 2.8GHz Toggle Frequency
- Supply Voltage 4.5V to 5.5V
- Low Power 9mA Typical at V_{CC} = 5.0V
- Operating Temperature Range of -40°C to +85°C



MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC}	Power Supply Voltage, Pin 2	-0.5 to +7.0	VDC
T _A	Operating Temperature Range	-40 to +85	°C
T _{stg}	Storage Temperature Range	-65 to +150	°C
I _O	Maximum Output Current, Pin 4	4	mA

ELECTRICAL CHARACTERISTICS (V_{CC} = 4.5 to 5.5V; T_A = -40 to +85°C)

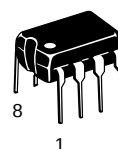
Symbol	Parameter	Min	Typ	Max	Unit
f _t	Toggle Frequency (Sine Wave)	0.25	3.4	2.8	GHz
I _{CC}	Supply Current Output (Pin 2)	—	9.0	11.5	mA
V _{in}	Input Voltage Sensitivity 250–500MHz 500–2800MHz	400 100	— —	1000 1000	mV _{pp}
V _{IH}	Divide Ratio Control Input High (SW)	V _{CC} - 0.5	V _{CC}	V _{CC} + 0.5	V
V _{IL}	Divide Ratio Control Input Low (SW)	Open	Open	Open	—
V _{out}	Output Voltage Swing (C _L = 12pF; R _L = 1.2k Ω ; I _O = 2.7mA) ¹ (C _L = 12pF; R _L = 2.2k Ω ; I _O = 1.5mA) ² (C _L = 12pF; R _L = 3.9k Ω ; I _O = 0.85mA) ³	1.0	1.6	—	V _{pp}

1. Divide ratio of $\div 64$ at 2.8GHz.
2. Divide ratio of $\div 128$ at 2.8GHz.
3. Divide ratio of $\div 256$ at 2.8GHz.

MC12079

MECL PLL COMPONENTS

$\div 64/128/256$
PRESCALER



P SUFFIX
PLASTIC PACKAGE
CASE 626-05



D SUFFIX
PLASTIC SOIC PACKAGE
CASE 751-05



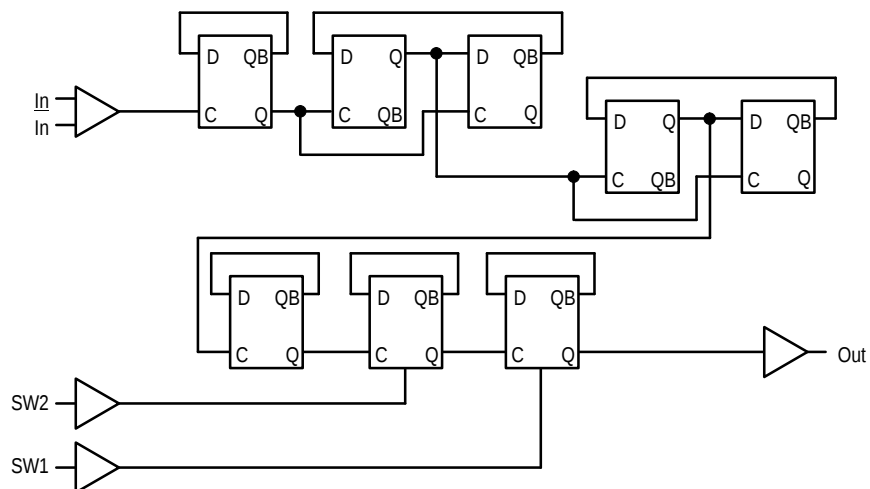


Figure 1. Logic Diagram (MC12079)

FUNCTION TABLE

SW1	SW2	Divide Ratio
H	H	64
H	L	128
L	H	128
L	L	256

Note: SW1 & SW2: H = V_{CC} ; L = Open

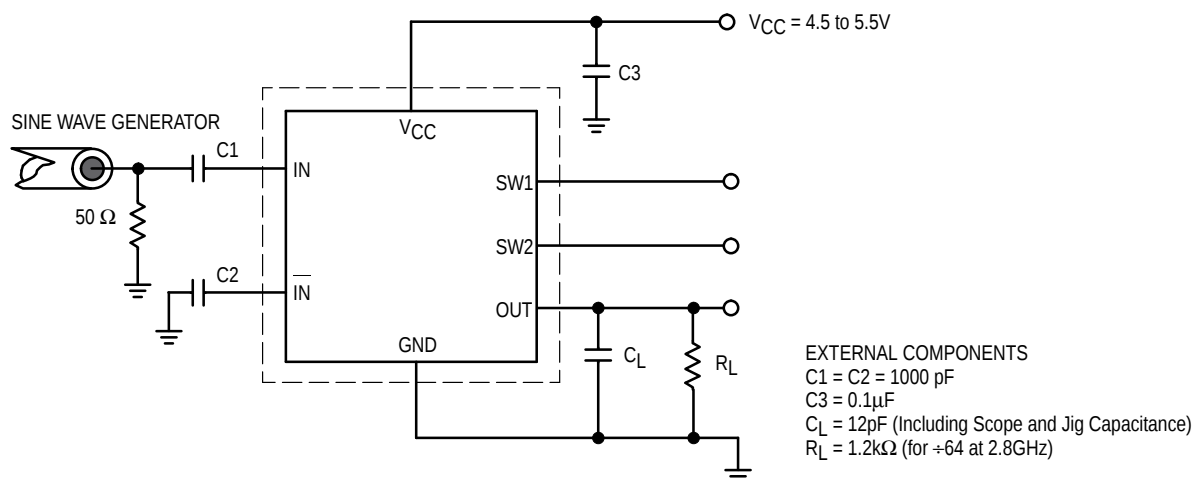


Figure 2. AC Test Circuit

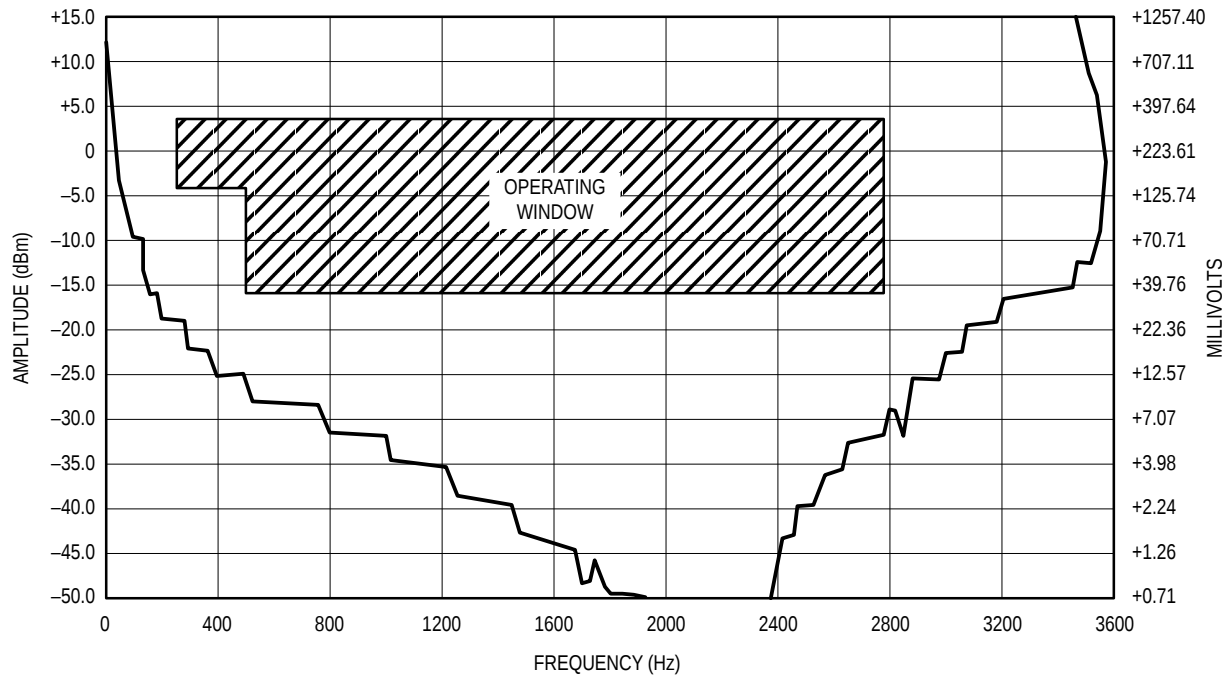


Figure 3. Input Signal Amplitude versus Input Frequency
Divide Ratio = 64; $V_{CC} = 5.0V$; $T_A = 25^{\circ}C$

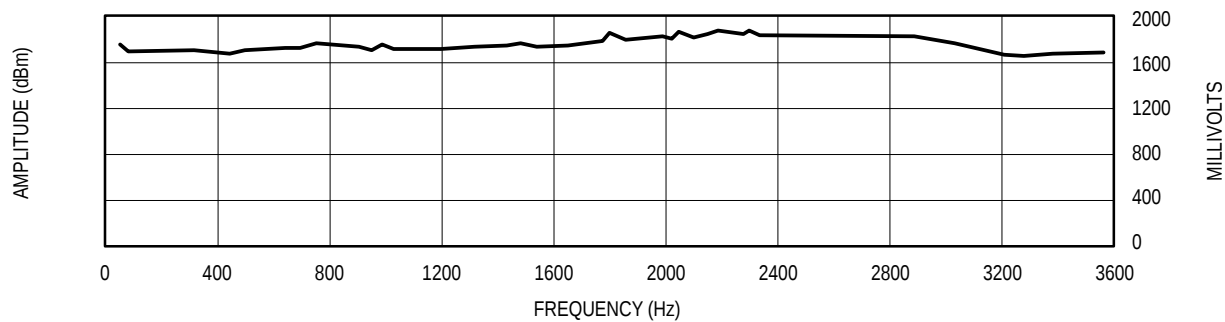
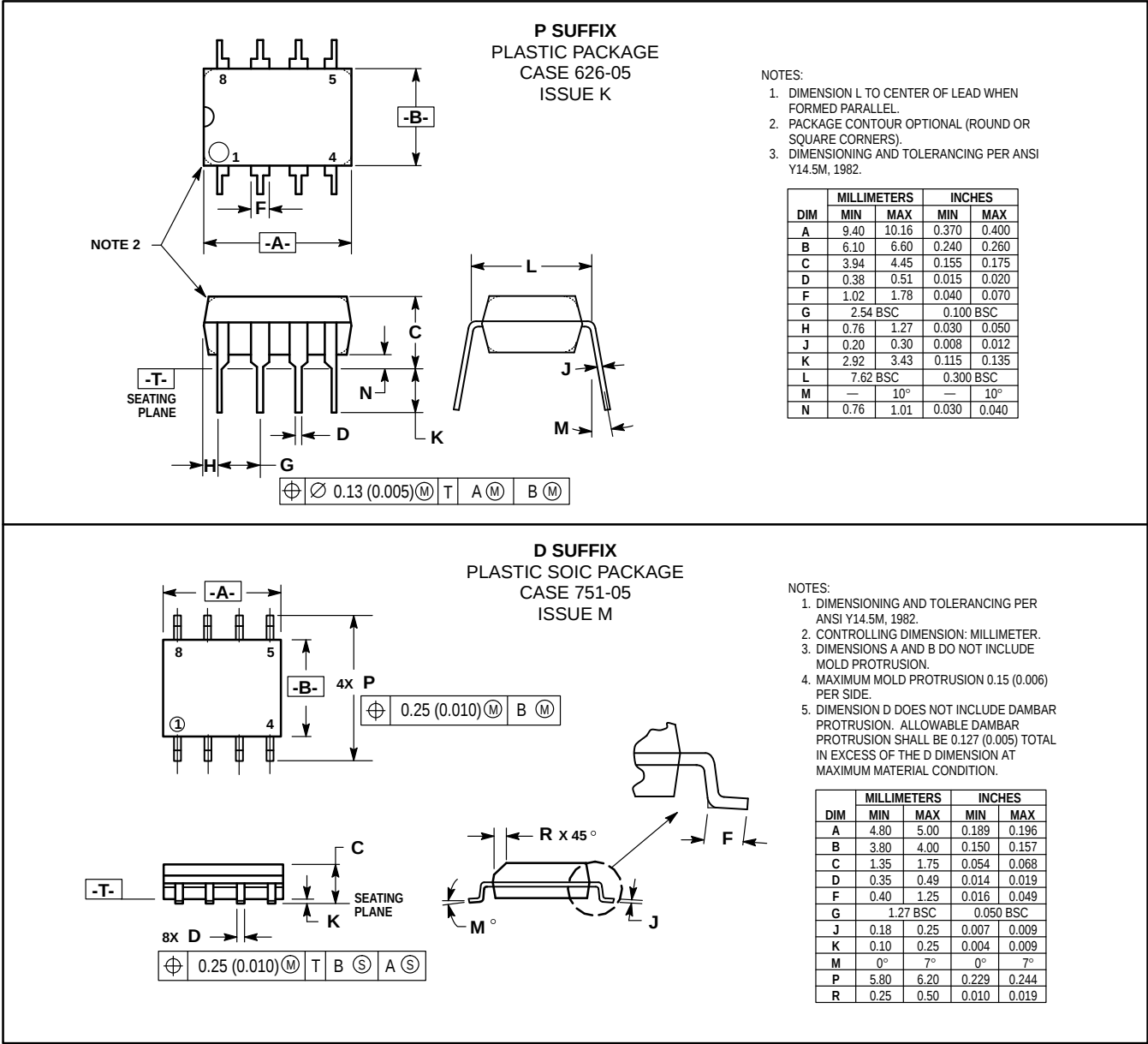


Figure 4. Output Amplitude versus Input Frequency

OUTLINE DIMENSIONS



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