1.1GHz Dual Modulus Prescaler

The MC12028A can be used with CMOS synthesizers requiring positive edges to trigger internal counters such as Motorola's MC145XXX series in a PLL to provide tuning signals up to 1.1GHz in programmable frequency steps.

The MC12028B can be used with CMOS synthesizers requiring negative edges to trigger internal counters.

A Divide Ratio Control (SW) permits selection of a 32/33 or 64/65 divide ratio as desired.

The Modulus Control (MC) selects the proper divide number after SW has been biased to select the desired divide ratio.

- 1.1 GHz Toggle Frequency
- MC12028A for Positive Edge Triggered Synthesizers
- MC12028B for Negative Edge Triggered Synthesizers
- 6.5mA Maximum, -40° to $+85^{\circ}$ C, $V_{CC} = 5.5$ Vdc
- Modulus Control Input Level Is Compatible With Standard CMOS and TTL
- Low–Power 4.0mA Typical

FUNCTIONAL TABLE

SW	MC	Divide Ratio
Н	Н	32
Н	L	33
L	н	64
L	L	65

Note: SW: $H = V_{CC}$, L = Open

MC: H = 2.0 V to V_{CC} , L = GND to 0.8 V

DESIGN GUIDE

Criteria	Value	Unit	
Internal Gate Count*	67	ea	
Internal Gate Propagation Delay	200	ps	
Internal Gate Power Dissipation	0.75	mW	
Speed Power Product	0.15	рJ	

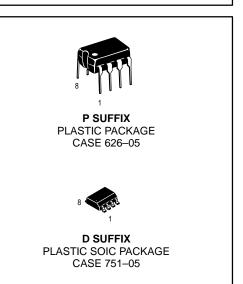
* Equivalent to a two-input NAND gate

MAXIMUM RATINGS

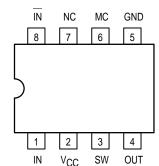
Symbol	Characteristic	Range	Unit
V _{CC}	Power Supply Voltage, Pin 2	-0.5 to + 7.0	Vdc
Т _А	Operating Temperature Range	-40 to + 85	°C
T _{stg}	Storage Temperature Range	-65 to + 150	°C
MC	Modulus Control Input, Pin 6	–0.5 to +6.5	Vdc

MECL PLL COMPONENTS

÷32/33, ÷64/65 DUAL MODULUS PRESCALER

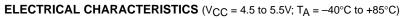


Pinout: 8-Lead Plastic (Top View)



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Symbol	Characteristic	Min	Тур	Max	Unit
ft	Toggle Frequency (Sine Wave Input)	0.1	1.4	1.1	GHz
ICC	Supply Current Output Unloaded (Pin 2)		4.0	6.5	mA
V _{IH1}	Modulus Control Input High (MC)	2.0		V _{CC} + 0.5V	V
V _{IL1}	Modulus Control Input Low (MC)			0.8	V
V _{IH2}	Divide Ratio Control Input High (SW)	$V_{CC} - 0.5V$	V _{CC}	V _{CC} + 0.5V	Vdc
V _{IL2}	Divide Ratio Control Input Low (SW)	Open	Open	Open	-
V _{out}	Output Voltage Swing (C _L = 12pF; R _L = $2.2k\Omega$)	1.0	1.6		V _{p-p}
t _{set}	Modulus Setup Time MC to Out		11	16	ns
V _{in}	Input Voltage Sensitivity 250–1100 MHz 100–250 MHz	100 400		1500 1500	mVpp
IO	Output Current (C _L = 12pF; R _L = $2.2k\Omega$)		1.5	4.0	mA



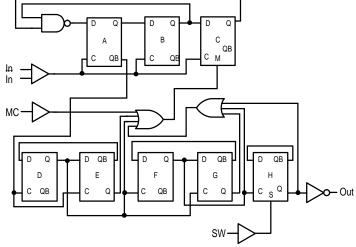
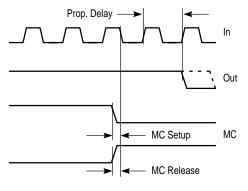


Figure 1. Logic Diagram (MC12028A)



Modulus setup time MC to out is the MC setup or MC release plus the prop delay.

Figure 2. Modulus Setup Time

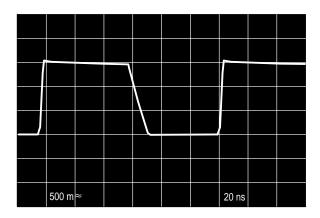


Figure 3. Typical Output Waveform

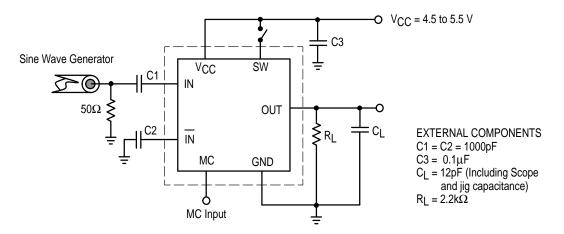
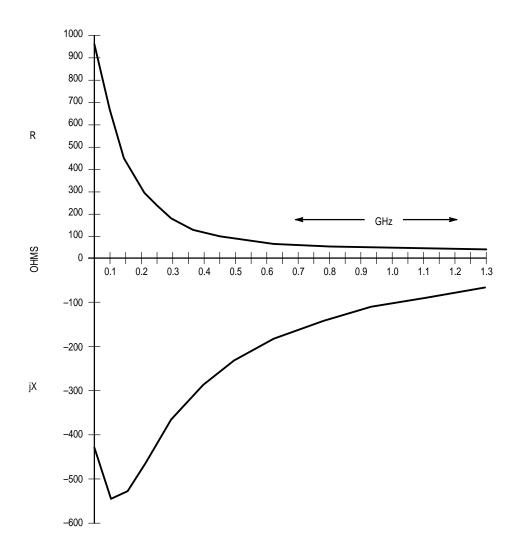
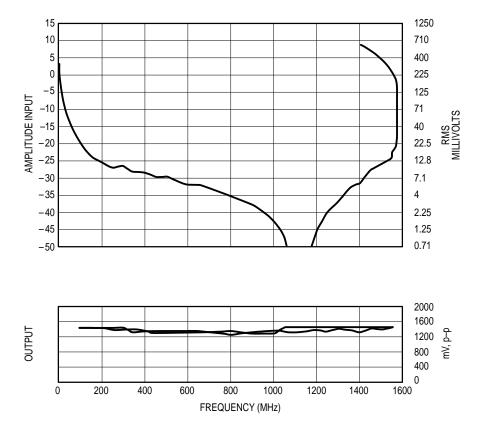
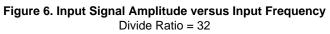


Figure 4. AC Test Circuit



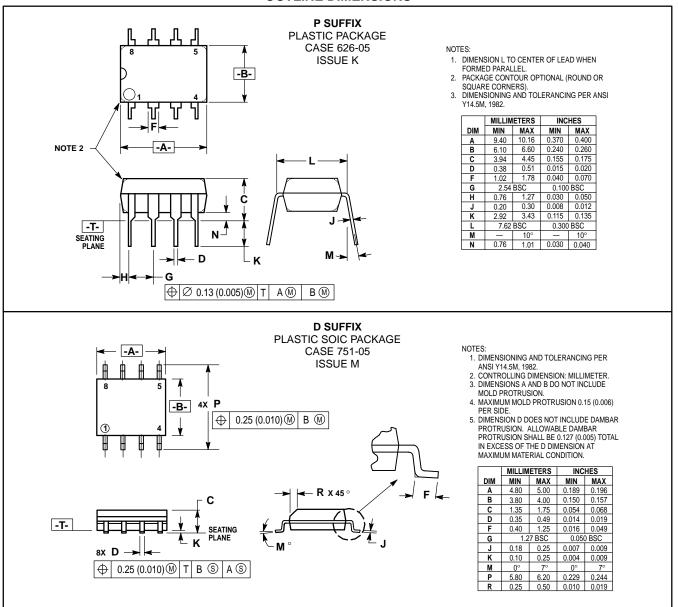






MC12028A/D

OUTLINE DIMENSIONS



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