



MAX749 Evaluation Kit

MAX749 EV Kit

General Description

The MAX749 LCD bias supply produces a variable negative output voltage when powered by a +3V to +5V supply. The MAX749 evaluation kit (EV kit) has a -8.0V to -23.6V adjustable output voltage. This is the typical range for driving the backplane of a liquid crystal display (LCD).

Two pushbutton switches, located on the EV kit's printed circuit board, control the output voltage. One switch connects to the CTRL pin and the other to the ADJ pin. Logic signals from an external controller may be connected to the board for optional output voltage control.

Component List

DESIGNATION	QTY	DESCRIPTION
C1	1	0.001 μ F surface-mount capacitor
C2	1	15 μ F, 35V surface-mount tantalum capacitor. Sprague 595D 156 X0 035 D 7.
C3	1	15 μ F, 10V surface-mount tantalum capacitor. Sprague 595D 156 X0 010 C 7.
C4	1	0.1 μ F, 50V surface-mount capacitor
C5, C6	2	0.01 μ F, 50V surface-mount capacitors
D1	1	1N5819 surface-mount diode. Nihon EC10QS04.
L1	1	47 μ H inductor. Sumida CD54-470N.
Q1	1	ZTX750 PNP switching transistor. Zetex ZTX750M1 (surface-mount).
R1, R2	2	100k Ω , 5% surface-mount resistors
R3	1	0.20 Ω , 5% surface-mount resistor. IRC LR2010-01-R200-J.
R4	1	470 Ω , 5% surface-mount resistor
R5	1	1.2M Ω , 5% surface-mount resistor
SW1, SW2	2	Pushbutton switches
U1	1	MAX749CSA
None	1	Printed circuit board
None	1	MAX749 data sheet

See Table 2 of the MAX749 data sheet for phone and fax numbers of component suppliers.

Features

- ◆ 2.7V to 6V Input Voltage
- ◆ -8.0V to -23.6V Adjustable Output Voltage
- ◆ 40mA Output Current with +5V Input
15mA Output Current with +3V Input
- ◆ Easy Interface to μ P
- ◆ Pushbutton Control of Output Voltage

Ordering Information

PART	TEMP. RANGE	BOARD TYPE
MAX749EVKIT-SO	0°C to +70°C	Surface-Mount

EV Kit



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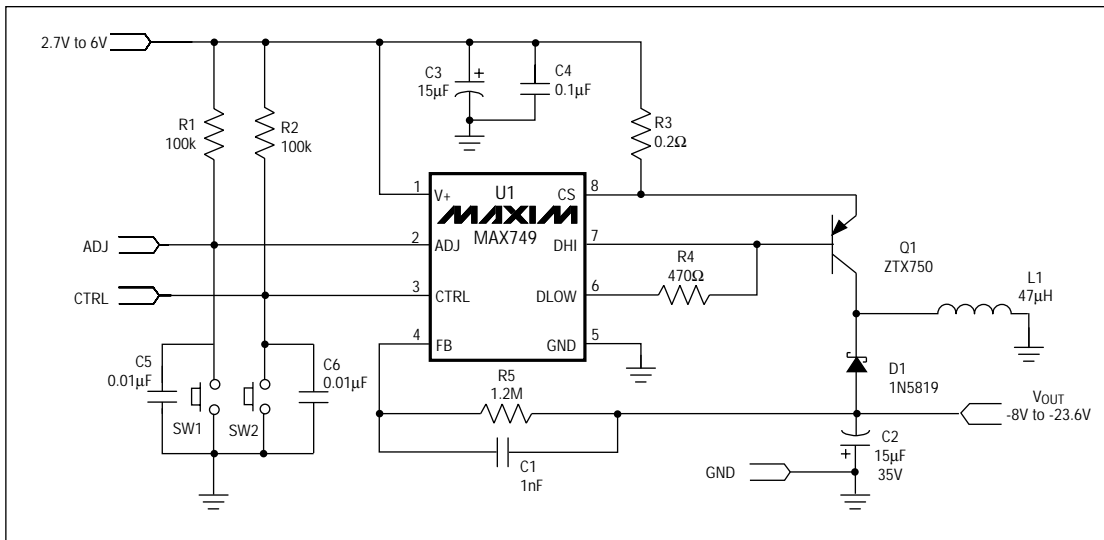


Figure 1. MAX749 EV Kit Schematic

Quick Start

The MAX749 EV kit is easy to connect and use. Follow these steps for making connections to the printed circuit board and changing the output voltage.

Do not turn on the power supply until all connections are completed.

- 1) Connect a +3V to +6V supply to the pad marked V_{CC} on the printed circuit board. The ground connects to a pad marked GND.
- 2) Connect a voltmeter and load (if any) to the V_{OUT} pad.
- 3) Turn on the power and verify that the output voltage is -16V.
- 4) Press the ADJ button several times to observe the output voltage changing.
- 5) Press the CTRL button and verify that the output returns to -16V.

Detailed Description

Input Source

The MAX749 EV kit works with supply voltages of 3V to 5V. It delivers at least 40mA at -23.6V with a 5V input. With a 3V input, the maximum output current drops to 15mA. Under full load (40mA at -23.6V), the kit will draw approximately 240mA from a 5V input supply. The following equations show how this figure was determined:

$$\begin{aligned}
 1) \text{ Input Power} &= \frac{\text{Output Power}}{\text{Efficiency}} \\
 \text{Input Power} &= \frac{23.6V \times 40mA}{80\%} \\
 \text{Input Power} &= 1.18W \\
 2) \text{ Input Current} &= \frac{\text{Input Power}}{\text{Input Voltage}} \\
 \text{Input Current} &= \frac{1.18W}{5V} \\
 \text{Input Current} &= 236mA
 \end{aligned}$$

The input supply should be capable of greater than 300mA to cover the range of normal operating conditions.

Output Voltage Setting

The value of the feedback resistor ($R5$) and the input current of the feedback pin (FB) determine the output voltage range of the MAX749 EV kit. The MAX749 sets the feedback current to a mid-scale value of 13.3μA whenever the CTRL pushbutton switch is pressed and released. Since $R5$ is 1.2MΩ, the mid-scale output voltage is set at -16.0V ($13.33\mu A \times 1.2M\Omega$). The output voltage changes by -0.24V for the next 31 pushes on the ADJ button. The 32nd push on the ADJ button causes the output voltage to change to its minimum value of

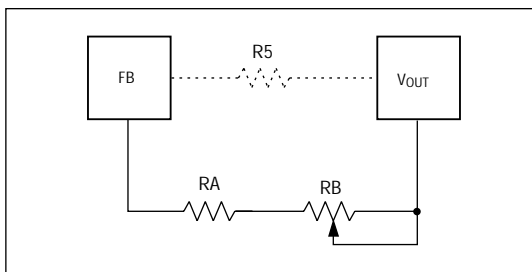


Figure 2. Using a Potentiometer to Vary the Output Voltage

-8V. See the MAX749 data sheet for a full description of CTRL pin and ADJ pin operation.

Using pushbutton switches with only a capacitor for debouncing is economical, but is not recommended. The device may occasionally false step because of switch bounce.

The External Transistor

The standard MAX749 application circuit uses an external P-channel, logic-level MOSFET or a PNP switching transistor. The transistor switches the high inductor peak currents and isolates the MAX749 from the negative output voltage. The ZTX750 PNP used in the EV kit has a typical beta of 200 at 700mA (the peak inductor current). This beta is higher than average for PNP transistors when operating with high collector currents. The high gain improves the circuit's efficiency. A more costly logic-level MOSFET (such as the Siliconix SMD10P05L) will improve efficiency, but it will also require a 5V power supply. Graphs in the MAX749 data sheet illustrate the conversion efficiency with both types of external switch.

The peak current allowed through the transistor and inductor is determined by the current-sense resistor in series with the transistor's emitter. The MAX749 turns off the PNP whenever the voltage across the resistor exceeds the current-limit threshold (140mV).

The following equation yields the value of the peak

$$I_{PEAK} = \frac{V_{CURRENT LIMIT}}{R_{SENSE}}$$

$$I_{PEAK} = \frac{140mV}{0.2\Omega} = 700mA$$

inductor current:

The MAX749 EV kit is configured for a 40mA output current with a 5V supply. The output current drops to 15mA with a 3V supply. For higher output current at 3V,

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R3 and R4 should be decreased in value to increase the peak inductor current. See the *Current-Sense Resistor* section of the MAX749 data sheet.

Output Filter Capacitors

The output filter capacitor (C2) is a low-value, high-voltage tantalum capacitor. The lower value does allow higher ripple voltage (200mV) when operating with a load; however, LCD displays are not affected by the ripple. The output capacitor value may be increased to reduce ripple for other applications.

Interfacing to Microprocessors

The CTRL and ADJ pins are compatible with the logic levels found in digital systems. They can be driven directly by logic gates or the programmable data pins generally available on microprocessors. Ensure that the logic levels applied to CTRL and ADJ do not exceed V+. To use external CTRL and ADJ signals, connect the source to the indicated pads on the printed circuit board. Capacitors used to debounce the pushbutton switches (C5 and C6) should be removed. The switches do not have to be removed, their presence will not affect circuit operation.

Modifying the Output Voltage Range

The MAX749 output voltage can also be controlled with an external potentiometer. To connect the potentiometer, remove the feedback resistor (R5) and connect the circuit of Figure 2 to the pads. See the *Potentiometer Adjustment* section of the MAX749 data sheet to determine values for resistors RA and RB. Be sure the maximum output voltage does not exceed the voltage rating of the output filter capacitor.

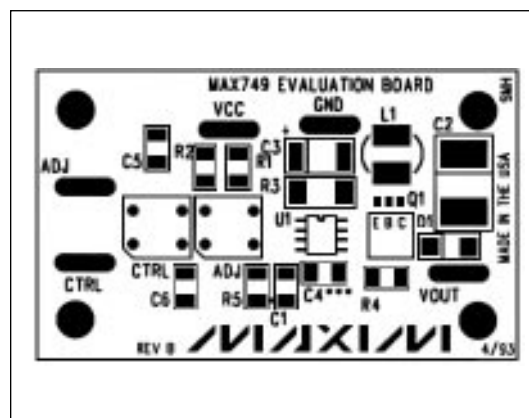


Figure 3. MAX749 EV Kit Component Placement Guide

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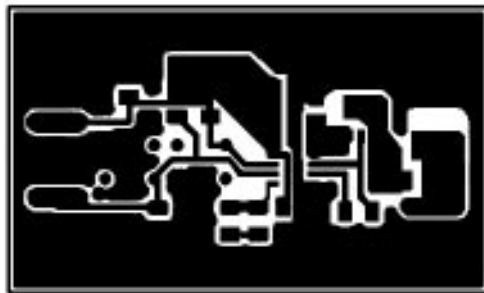


Figure 4. MAX749 EV Kit Component-Side Layout

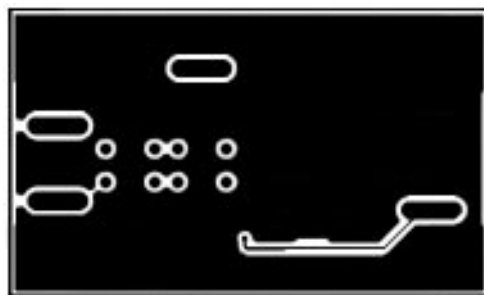


Figure 5. MAX749 EV Kit Solder-Side Layout

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