Features



Low-Cost, Remote SOT Temperature Switches

General Description

The MAX6511/MAX6512/MAX6513 are fully integrated. remote temperature switches that use an external P-N iunction (typically a diode-connected transistor) as the sensing element to measure the remote temperature. These devices assert a logic signal when the temperature crosses a factory-programmed threshold. Available trip thresholds are from +45°C to +125°C in 10°C increments. Accuracy is within $\pm 3^{\circ}$ C (T_A = -5°C to $\pm 55^{\circ}$ C) or $\pm 5^{\circ}$ C (TA = -40°C to $\pm 85^{\circ}$ C). Hysteresis is pin selectable to 5°C or 10°C.

The MAX6511 has an active-low CMOS output and the MAX6513 has an active-high CMOS output. The MAX6512 has an open-drain output. The output is asserted when the temperature exceeds the threshold value. The active-low open-drain output is intended to interface with a microprocessor (µP) reset or interrupt input. The active-high CMOS output can directly drive a power FET to control a cooling fan.

The MAX6511/MAX6512/MAX6513 operate from a +3.0V to +5.5V supply and typically consume 400µA of supply current. They are available in a small 6-pin SOT23 package.

- **♦** Factory-Programmed Temperature Threshold from +45°C to +125°C in 10°C Increments
- Insensitive to Series Parasitic Resistance

♦ Continuously Measure External Junction

♦ Active-Low CMOS Output (MAX6511) or Open-**Drain Output for Overtemperature Alarm** (MAX6512) or Active-High Output (MAX6513) for **Direct Fan Control**

 $\pm 5^{\circ}$ C (TREMOTE = +45°C to +125°C, T_A = -40°C to +85°C)

♦ <100ms Response Time

Temperature

- **♦** Accuracy $\pm 3^{\circ}$ C (Tremote = +45°C to +125°C, TA = -5°C to +55°C)
- ♦ Pin-Selectable 5°C or 10°C Hysteresis
- ♦ 400µA Average Current Consumption
- ♦ +3.0V to +5.5V Supply Range
- ♦ 6-Pin SOT23 Package

Applications

CPU Temperature Monitoring in High-Speed Computers

Multichip Modules

Battery Packs

Temperature Control

Temperature Alarms

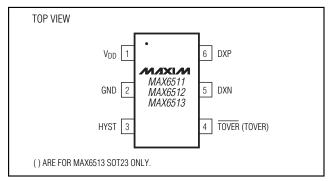
Fan Control

Ordering Information

PART*	TEMP. RANGE	PIN- PACKAGE	ОИТРИТ
MAX6511 EUTT	-40°C to +85°C	6 SOT23-6	CMOS (Active-Low)
MAX6512 EUTT	-40°C to +85°C	6 SOT23-6	Open-Drain
MAX6513 EUTT	-40°C to +85°C	6 SOT23-6	CMOS (Active-High)

*These parts are offered in nine standard temperature versions with a minimum order of 2500 pieces. To complete the suffix information, select an available trip point in degrees centigrade from the device marking codes table. For example, the MAX6511UT065-T describes a MAX6511 in a SOT23-6 package with a +65°C threshold.

Pin Configuration



Typical Operating Circuit appears at end of data sheet.

MIXIM

Maxim Integrated Products 1

ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V _{DD})	0.3V to +6V
DXP, DXN, HYST, TOVER (MAX6513),	
TOVER (MAX6511/MAX6512)0.3	V to $(V_{DD} + 0.3V)$
TOVER (MAX6513), TOVER (MAX6511)	
Output Current	1mA/+50mA
DXN Input Current	1mA/+50mA
Current (all other pins)	±20mA
Continuous Power Dissipation ($T_A = +70^{\circ}C$)	
6-Pin SOT23-6 (derate 9.1mW/°C above +70	0°C)727mW

Operating Temperature Range	40°C to +85°C
Storage Temperature Range	65°C to +150°C
Junction Temperature	+150°C
Lead Temperature	
Vapor Phase (60s)	+215°C
Infrared (15s)	+220°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{DD} = +3.0 V \text{ to } +5.5 V, C_S = 2200 pF, T_A = -40 ^{\circ} C \text{ to } +85 ^{\circ} C, T_{REMOTE} = +45 ^{\circ} C \text{ to } +125 ^{\circ} C \text{ (Note 1), unless otherwise noted.}$ Typical values are at $T_A = +25 ^{\circ} C.$) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Supply Voltage Range	V_{DD}		3.0		5.5	V	
Supply Current	I _{DD}			400	600	μΑ	
Temperature Threshold	ΔT_TH	$T_A = -5^{\circ}C \text{ to } +55^{\circ}C$	-3.0		+3.0	°C	
Accuracy (Note 3)	ΔΠΗ	$T_A = -40$ °C to $+85$ °C	-5.0		+5.0	<u>] </u>	
Power-Supply Sensitivity for Temperature Trip Point				-0.6		°C/V	
Temperature Threshold	Tuncor	HYST = V _{IL}		5		°C	
Hysteresis THYST		HYST = V _{IH}		10			
Response Time				70	120	ms	
Input Voltage High	VIH		V _{DD} - 0.2			V	
Input Voltage Low	V _{IL}				0.2	V	
Output Voltage High	V _{OH}	MAX6511/MAX6513, I _{OUT} = 1mA	V _{DD} - 0.2			V	
Output Voltage Low	V _{OL}	I _{OUT} = 1mA			0.2	V	
Maximum DXP Source Current		$0.4V \le V_{DXP} \le 2V$, DXN = GND		270		μΑ	
Minimum DXP Source Current		$0.4V \le V_{DXP} \le 2V$, DXN = GND		9		μΑ	

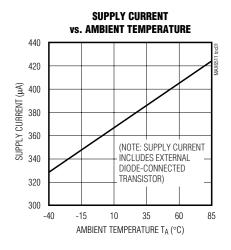
Note 1: T_{REMOTE} refers to the temperature of the remote-sensing junction. T_A refers to the temperature of the MAX6511/MAX6512/ MAX6513 package.

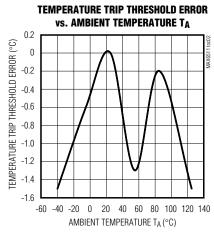
Note 2: All parameters are 100% production tested at TA = +25°C. Specifications over temperature limits are guaranteed by design.

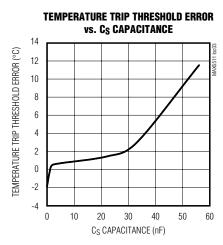
Note 3: This parameter is guaranteed by design to ±3.5 sigma.

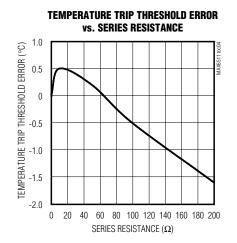
Typical Operating Characteristics

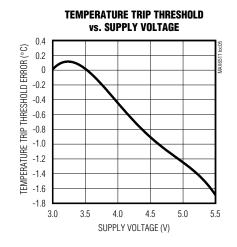
 $(V_{DD} = +3.3V, C_S = 2200pF, T_A = +25^{\circ}C, unless otherwise noted.)$











Pin Description

Р	PIN		
MAX6511 MAX6512	MAX6513	NAME	FUNCTION
1	1	V _{DD}	Power-Supply Input, +3.0V to +5.5V. Bypass V _{DD} to GND with a 0.1µF capacitor.
2	2	GND	Ground
3	3	HYST	Hysteresis Selection. Hysteresis is 10°C for HYST = V _{DD} , 5°C for HYST = GND.
4	_	TOVER	CMOS Active-Low Output (MAX6511) or Open-Drain Active-Low Output (MAX6512). TOVER goes low when the temperature exceeds the factory-programmed temperature threshold. This pin can only sink current in the MAX6512.
_	4	TOVER	CMOS Active-High Output (MAX6513). TOVER goes high when the temperature exceeds the factory-programmed temperature threshold.
5	5	DXN	This pin connects to the negative (cathode) terminal of the external P-N sense junction. DXN must be connected to GND.
6	6	DXP	This pin connects to the positive (anode) terminal of the external P-N sense junction.

Detailed Description

The MAX6511/MAX6512/MAX6513 fully integrated temperature switches incorporate a precision bandgap reference, a conversion block, a current source, and a comparator (Figure 1). These devices use an external P-N junction as the temperature-sensing element. They steer bias currents through the external diode, measure the forward voltages, and compute the temperature using a precision chopper stabilized amplifier.

Resistance values of less than 100Ω in series with the external sense junction will result in trip-point errors <1°C. The MAX6511/MAX6512/MAX6513 provide noise immunity by integration and oversampling of the diode voltage, but good design practice includes routing the DXP and DXN lines away from noise sources, such as high-speed digital lines, switching regulators, inductors, and transformers. The DXP and DXN traces should be paired together and surrounded by ground plane whenever possible.

In applications where the temperature changes rapidly, the measured temperature will be approximately equal to the average value of the temperature during the measurement period. The MAX6512 has an active-low, open-drain output structure that can only sink current. The MAX6511 has an active-low CMOS output structure, and the MAX6513 has an active-high CMOS output.

The MAX6511/MAX6512/MAX6513 are available with preset temperature thresholds from +45°C to +125°C in 10°C increments.

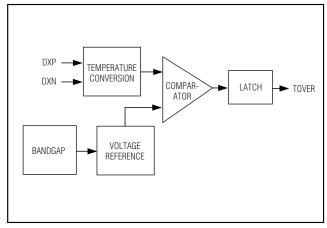


Figure 1. Functional Block Diagram

Table 1. Sensor Transistor Manufacturers

MANUFACTURER	MODEL NUMBER
Central Semiconductor (USA)	CMPT3904
ON (USA)	MMBT3904
Rohm Semiconductor (Japan)	SST3904
Samsung (Korea)	KST3904-TF
Siemens (Germany)	SMBT3904
Zetex (England)	FMMT3904CT-ND

Note: Transistors must be diode connected (base shorted to collector).

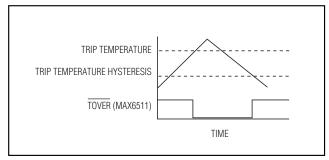


Figure 2. Temperature Trip Threshold Hysteresis

Hysteresis Input

The HYST pin is a CMOS-compatible input that selects hysteresis at either a high level (10°C for HYST = V_{DD}) or a low level (5°C for HYST = GND). Hysteresis prevents the output from chattering when the temperature is near the trip point. The HYST pin must not float.

The output asserts when the temperature exceeds the trip point and deasserts when the temperature falls back below the trip point minus the hysteresis. For example, if the trip point is 105°C, the output will assert at 105°C and will not deassert until temperature falls below 105°C minus the hysteresis (e.g., 95°C if 10°C hysteresis is chosen) (Figure 2).

Applications Information

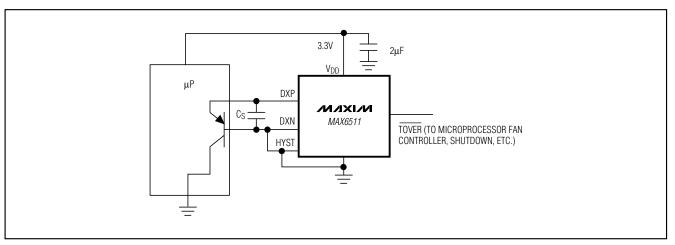
Remote-Diode Selection

To ensure best accuracy, use a good-quality diodeconnected transistor. Suggested devices are listed in Table 1. Large power transistors are not recommended. Tight specifications for forward current gain indicate the manufacturer has good process controls and that the devices have consistent V_{be} characteristics. The MAX6511/MAX6512/MAX6513 can also measure the die temperature of CPUs and other integrated circuits having on-board temperature-sensing diodes. Use the monitor's output to reset the μP , assert an interrupt, activate a cooling fan, or trigger an external alarm.

Noise Filtering Capacitors

A quality ceramic capacitor must be connected across the DXP/DXN inputs to maintain temperature threshold accuracy by filtering out noise. The capacitor should be located physically close to the DXP/DXN pins and should typically have a value of 2200pF. Larger capacitor values can cause temperature measurement errors. A 50% variation from the recommended capacitor value can cause up to ±1°C error.

Typical Operating Circuit



Device Marking Codes for SOT23-6 Package

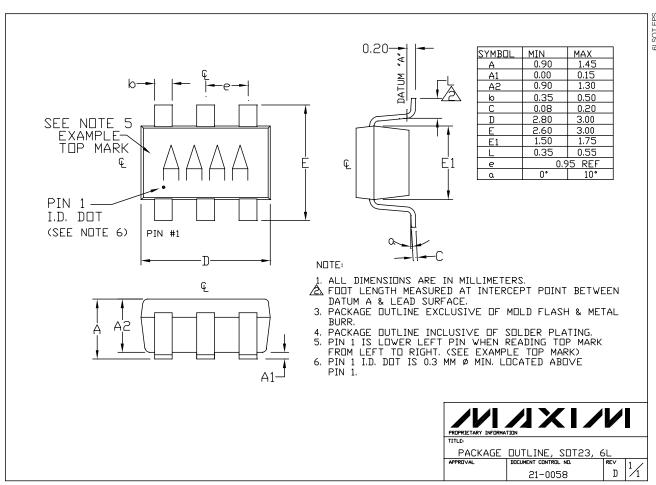
DEVICE	CODE	TEMPERATURE TRIP THRESHOLD (°C)
MAX6511UT045	AAOA	45
MAX6511UT055	AAOB	55
MAX6511UT065	AAOC	65
MAX6511UT075	AAOD	75
MAX6511UT085	AAOE	85
MAX6511UT095	AAOF	95
MAX6511UT105	AAOG	105
MAX6511UT115	AAOH	115
MAX6511UT125	AAOI	125
MAX6512UT045	AAOJ	45
MAX6512UT055	AAOK	55
MAX6512UT065	AAOL	65
MAX6512UT075	AAOM	75
MAX6512UT085	AAON	85

DEVICE	CODE	TEMPERATURE TRIP THRESHOLD (°C)
MAX6512UT095	AAOO	95
MAX6512UT105	AAOP	105
MAX6512UT115	AAOQ	115
MAX6512UT125	AAOR	125
MAX6513UT045	AAPD	45
MAX6513UT055	AAPE	55
MAX6513UT065	AAPF	65
MAX6513UT075	AAPG	75
MAX6513UT085	AAPH	85
MAX6513UT095	AAPI	95
MAX6513UT105	AAPJ	105
MAX6513UT115	AAPK	115
MAX6513UT125	AAPL	125

Chip Topography

TRANSISTOR COUNT: 3300

Package Information



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