19-1306; Rev 0; 3/98

MAXIM

MAXSMBus 2-Wire Interface Board

Evaluates: MAXSMBus

General Description

MAXSMBus is an interface between an IBM-compatible PC and System Management Bus (SMBus™)-compatible serial-interface devices such as temperature monitors, voltage regulators, or A/D converters (ADCs). The MAXSMBus interface board is connected between the PC parallel port and the device under test, converting parallel data into 2-wire, open-drain serial data. The board is provided for use with selected Maxim products and is not intended to replace commercially available SMBus hardware. MAXSMBus is shipped with a companion EV kit board, including all relevant software.

Features

- ♦ **SMBus-Compatible 2-Wire Interface**
- ♦ **SMBus Suspend Output**
- ♦ **Two SMBus Alert Inputs**
- ♦ **Overvoltage Fault Protection**
- ♦ **PC Parallel Port Interface**

Ordering Information

PART	BOARD TYPE
MAXSMBus	Companion Board for SMBus EV Kits

Maxim SMBus Interface Board Component List

DESIGNATION	QTY	DESCRIPTION
C1, C2, C3	3	0.1µF ceramic capacitors
C4–C9	6	3.3µF, 25V tantalum capacitors
D1	1	1N5235B zener diode, 6.8V
D2	1	1N5229B zener diode, 4.3V
D3	1	1N4148 small-signal diode
J1	1	DB25 right-angle plug
J2	1	Not installed
P1	1	2x10 right-angle male header
R1, R2, R3, R10, R11	5	47kΩ ±5% resistors
R4–R7	4	4.7kΩ ±5% resistors
R8, R9	2	1kΩ ±5% resistors
U1	1	74HC05 hex open-collector inverter
U2	1	74HC04 hex inverter
U3	1	74HC08 quad AND gate
U4	1	74HC74 dual D flip-flop
U5	1	+5V, 100mA regulator LM78L05ACM
U6	1	MAX865EUA (8-pin µMAX)
U7	1	MAX367CWN (18-pin SO)
NONE	1	PC board

MAXSMBus Functionality Check

Follow these steps to verify that the MAXSMBus interface board is functioning properly. All necessary software is supplied on a disk with the companion EV kit. Instructions for operating the software are included in the EV kit manual.

1) Connect a +9VDC supply (+7V minimum, +20V maximum) to the MAXSMBus interface board at the terminals labeled POS9 and GND in the lower left corner of the board.
2) Use a digital voltmeter to verify that the oval pad labeled POS5 is +5V (+4.75V minimum, +5.25V maximum). Also verify that the pads labeled SBDAT1, SBCLK1, SBSUS1, ALERT1, and ALERT2 are above +4V.
3) If these DC voltages are correct, MAXSMBus passes the functionality test.

Detailed Description

The MAXSMBus interface board provides all of the interface signals necessary to interface an IBM PC-compatible computer with an SMBus-compliant device. A DB25 right-angle plug connects to the computer (Table 1). The companion board plugs into a 20-pin dual-row right-angle header at the edge of the board (Table 2). Alternatively, connection can be made by soldering wires to the oval pads as appropriate. This allows the companion board to be placed in an environmental chamber for evaluation over temperature.

Refer to the documentation of the companion Maxim EV kit for quick start and operating instructions.

SMBus is a trademark of Intel Corp.

MAXSMBus 2-Wire Interface Board

Power Supply

The interface board is powered by a 78L05 linear regulator. The companion board can draw about 20mA of +5V power through the circuit protector. Companion boards that require more power must provide their own regulator. The unregulated input to the 78L05 is available on the right-angle header.

Fault-Protection Circuitry

Overvoltage fault protection is provided by a MAX367 fault protector (U7). If any of the SMBus interface signals exceed the MAX367's power-supply rails, the MAX367 increases its resistance to prevent damage to the user's computer. A MAX865 dual charge pump (U6) and two zener diodes (D1 and D2) provide +7V and -3V supplies to the MAX367, thus allowing 0V and +5V signals to pass with a nominal resistance of 100Ω.

Bus Driving Circuitry

A 74HC05 open-drain inverter (U1) is used to pull down the SMBus interface signals. The 74HC08 (U3), 74HC74 (U4), and 74HC04 (U2) buffer the signal to the IBM PC and provide the capability to mask the ALERT interrupts, detect an externally generated start condition, and capture data sent by an external bus master. Interface connections are listed in Table 1 and Table 2.

Bus Monitoring Circuitry

Flip-flop U4A detects the start condition (falling edge of SMBDATA when SMBCLK is high). Flip-flop U4B detects the falling SMBCLK edge when enabled, and U1F holds SMBCLK low until the software releases it. To advance to the next data bit, the software uses U1A to assert and then release SMBCLK. A logic high at the input of U1A also resets flip-flop U4B. Because the IBM PC parallel port has a limited number of inputs, the start-detect circuit and the two alert inputs share a single interrupt input. The source of the interrupt is distinguished using U3A, U3B, and U3C.

Troubleshooting Guide

SYMPTOM	CAUSE	SOLUTION
Can't Find the Interface Board	Board not connected to parallel printer port	Verify that the cable is a 25-pin parallel port I/O extension cable with a plug on one end and a socket on the other end. Verify that the cable is connected to a printer port, not a floppy disk, SCSI, or serial communications port.
Clock or Data Stuck Low	Board is connected to correct port, but SMBus is not functioning	Check power connections on the interface board. Check clock and data signal connections. Try operating the interface board without the companion Maxim evaluation kit—this should cause the address-not-acknowledged symptom described below.
Address Not Acknowledged	SMBus is OK, but no response at expected SMBus address	Verify that the companion board is connected to the MAXSMBus interface board. Verify that the companion board is powered. If the companion offers a choice of addresses, confirm that the software and hardware addresses match. Some devices only read the address select pins at device power-up.
Erratic Operation	Conflict with local printer driver	Disable print manager in Windows printer control panel. Disable printer driver.
	Operating system conflict	1) Use computer with commercially available BIOS. 2) Make a bootable floppy disk, remove unnecessary device drivers from A:config.sys, and boot system from floppy.

Table 1. DB25 Connector Signals

PIN	NAME	FUNCTION
1	SPARE OUTPUT A	Spare output
2	$\overline{\text{SMBCLK\_OUT}}$	When high, drives SMBCLK signal low
3	$\overline{\text{SMBDATA\_OUT}}$	When high, drives SMBDATA signal low
4	SMBUS_OUT	When high, drives SMBSUS signal low
5	LOOPBACK	Loopback connection for port verification
6	$\overline{\text{MASK\_ALERT1}}$	When high, allows $\overline{\text{ALERT1}}$ to trigger $\overline{\text{INT}}$ low
7	$\overline{\text{MASK\_ALERT2}}$	When high, allows $\overline{\text{ALERT2}}$ to trigger $\overline{\text{INT}}$ low
8	$\overline{\text{MASK\_START}}$	When high, allows a start condition to trigger $\overline{\text{INT}}$ low
9	CAPTURE_ENABLE	When high, enables slave / bus monitor circuitry. This circuit waits until SMBCLK is pulled low, and then it holds SMBCLK until the software resets it.
10	INT	Active low interrupt input
11	$\overline{\text{SMBDATA\_IN}}$	When high, indicates that SMBDATA is low
12	$\overline{\text{SMBCLK\_IN}}$	When high, indicates that SMBCLK is low
13	LOOPBACK	Loopback connection for port verification
14	SPARE OUTPUT B	Spare output
15	$\overline{\text{HOLDING\_CLOCK}}$	When low, indicates that interface board is holding SMBCLK low
16	UNUSED	Not used
17	UNUSED	Not used
18–25	GND	Signal ground return

Table 2. Right-Angle Header P1 Signals

PIN	NAME	FUNCTION
1	DUT +5V	+5V at 20mA power supply to Maxim companion board
2	GND	Signal ground return
3	DUT SDA	SMBDATA interface signal
4	GND	Signal ground return
5	GND	Signal ground return
6	GND	Signal ground return
7	DUT SCL	SMBCLK interface signal
8	GND	Signal ground return
9	$\overline{\text{DUTSMBSUS}}$	$\overline{\text{SMBSUS}}$ interface signal
10	GND	Signal ground return
11	$\overline{\text{DUTSMBALERT}}$	Primary $\overline{\text{ALERT}}$ interface signal
12	GND	Signal ground return
13	$\overline{\text{DUTALERT2}}$	Secondary $\overline{\text{ALERT}}$ interface signal
14	GND	Signal ground return
15	SPARE OUTPUT A	Spare output from pin 1 of the DB25 connector
16	GND	Signal ground return
17	SPARE OUTPUT B	Spare output from pin 14 of the DB25 connector
18	GND	Signal ground return
19	GND	Signal ground return
20	RAW POWER	Unregulated, unprotected power-supply input to MAXSMBus interface board

Note: *Odd-numbered pins are on the outer row. Even-numbered pins are on the inner row. All right-angle header signals pass through the MAX367 circuit protector, except 20.*

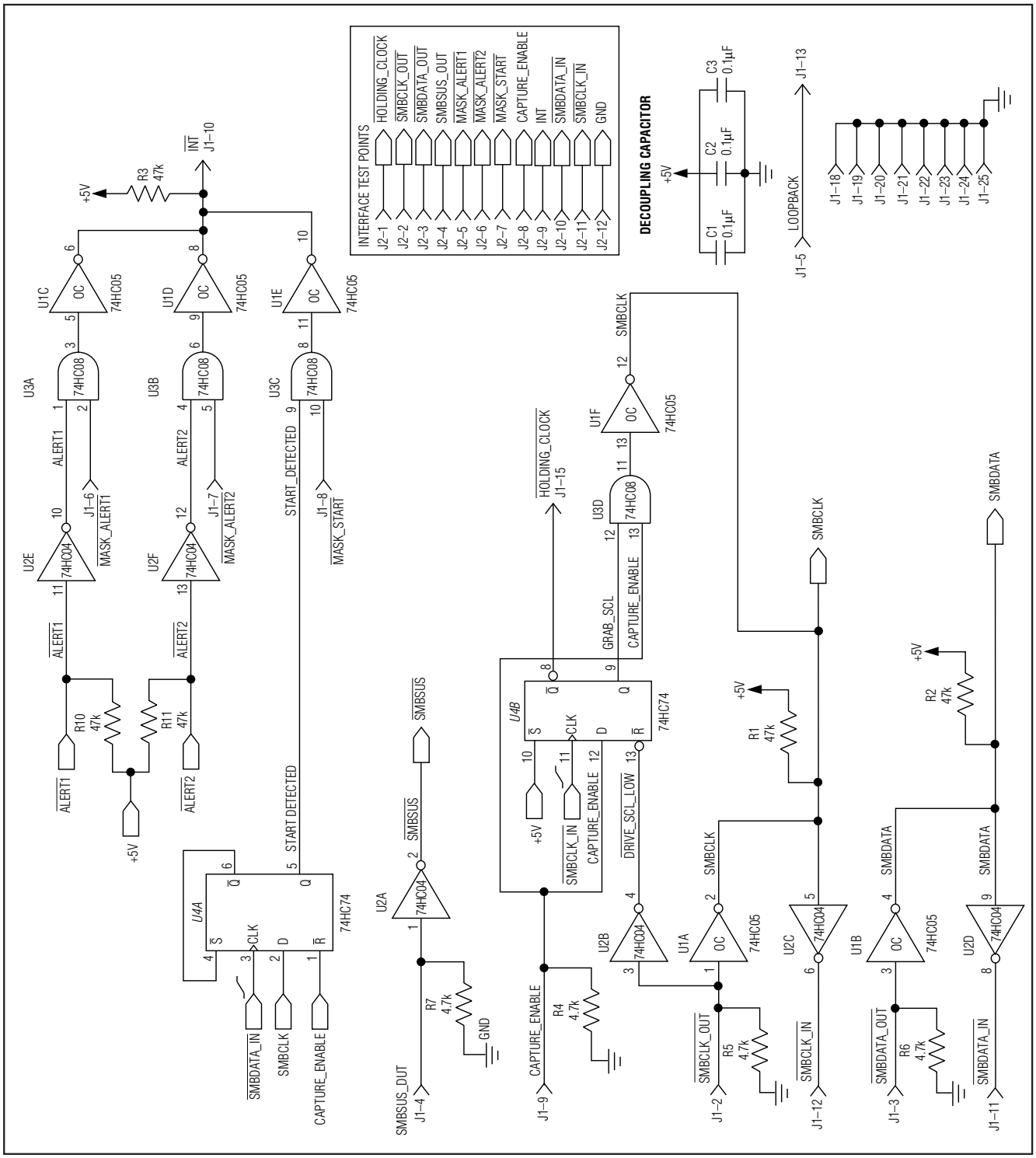

Figure 1. MAXSMBus Schematic

MAXSMBus
2-Wire Interface Board

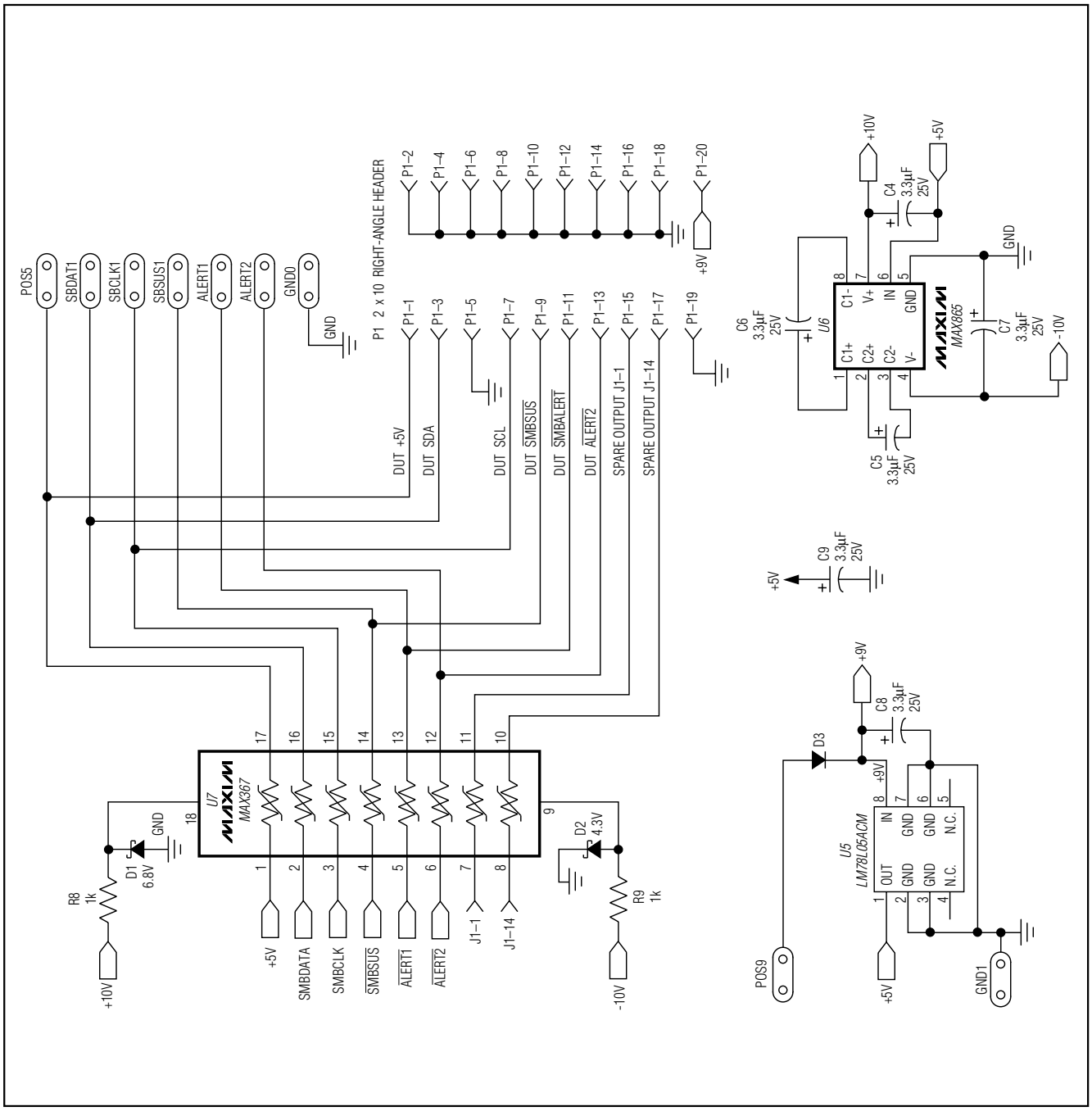

Figure 1. MAXSMBus Schematic (continued)

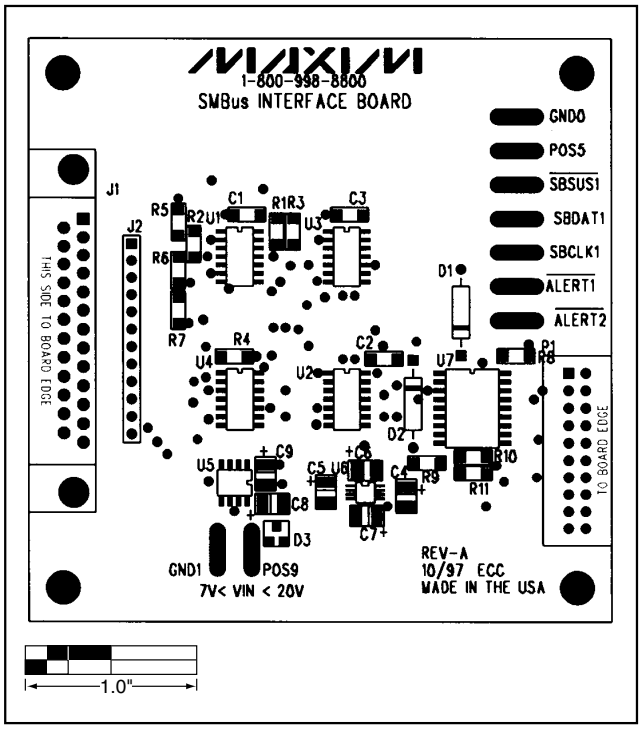

Figure 2. MAXSMBus Component Placement Guide

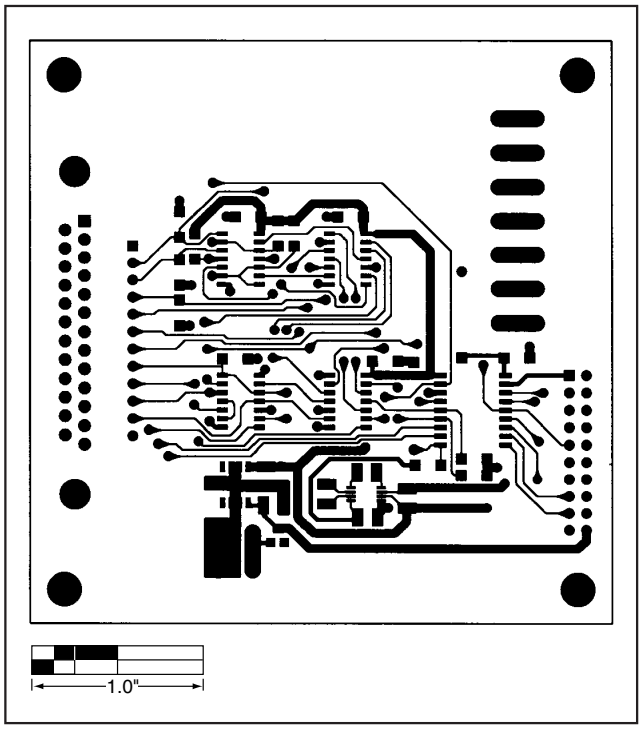

Figure 3. MAXSMBus PC Board Layout—Component Side

MAXSMBus
2-Wire Interface Board

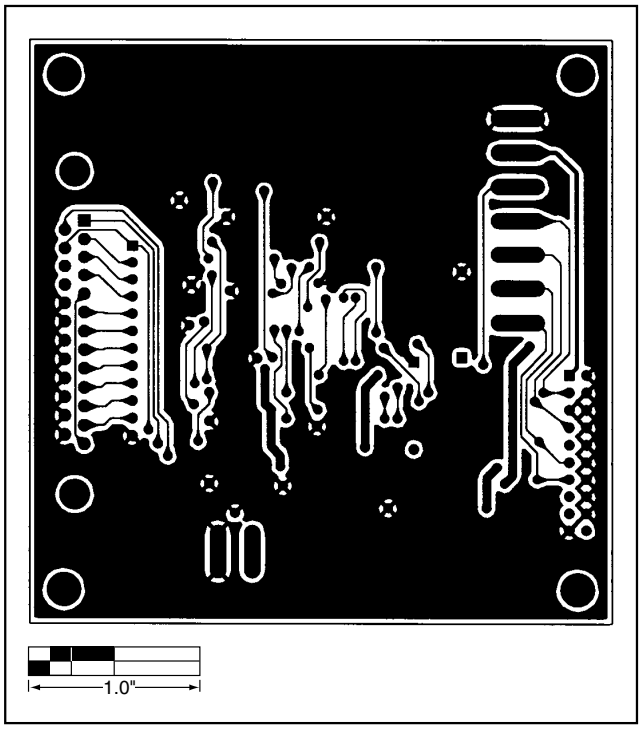

Figure 4. MAXSMBus PC Board Layout—Solder Side