

# 1.1MHz, 0.4V/ $\mu$ s Over-The-Top Micropower, Rail-To-Rail Input and Output Op Amp

## FEATURES

- Operates with Inputs Above  $V^+$
- Rail-to-Rail Input and Output
- Micropower: 250 $\mu$ A Supply Current Max
- Gain-Bandwidth Product: 1.1MHz
- Slew Rate: 0.4V/ $\mu$ s
- Low Input Offset Voltage: 350 $\mu$ V Max
- Single Supply Input Range:  $-0.4V$  to 44V
- High Output Current: 25mA Min
- Specified on 3V, 5V and  $\pm 15V$  Supplies
- Output Shutdown
- Output Drives 4700pF with Output Compensation
- Reverse Battery Protection to 25V
- High Voltage Gain: 800V/mV
- High CMRR: 110dB

## APPLICATIONS

- Battery or Solar Powered Systems:  
Portable Instrumentation  
Sensor Conditioning
- Supply Current Sensing
- Battery Monitoring
- MUX Amplifiers
- 4mA to 25mA Transmitters

## DESCRIPTION

July 1999

The LT<sup>®</sup>1637 is a rugged op amp that operates on all single and split supplies with a total voltage of 2.7V to 44V. The LT1637 has a gain-bandwidth product of 1.1MHz while drawing less than 250 $\mu$ A of quiescent current. The LT1637 can be shut down, making the output high impedance and reducing the quiescent current to only 3 $\mu$ A. The LT1637 is reverse supply protected: it draws no current for reverse supply up to 25V. The input range of the LT1637 includes both supplies and the output swings to both supplies. Unlike most micropower op amps, the LT1637 can drive heavy loads; its rail-to-rail output drives 25mA. The LT1637 is unity-gain stable into all capacitive loads up to 4700pF when optional 0.22 $\mu$ F and 150 $\Omega$  compensation is used.

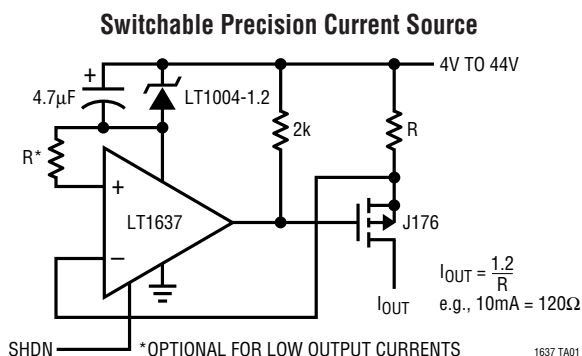
The LT1637 has a unique input stage that operates and remains high impedance when above the positive supply. The inputs take 44V both differential and common mode, even when operating on a 3V supply. Built-in resistors protect the inputs for faults below the negative supply up to 22V. There is no phase reversal of the output for inputs 5V below  $V_{EE}$  or 44V above  $V_{EE}$ , independent of  $V_{CC}$ .

The LT1637 op amp is available in the 8-pin MSOP, PDIP and SO packages.

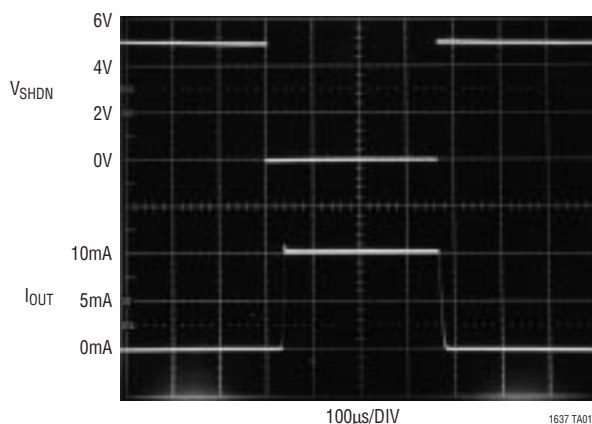
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Over-The-Top is a trademark of Linear Technology Corporation.

## TYPICAL APPLICATION

### Over-The-Top<sup>™</sup> Current Source with Shutdown



### Current Source Timing



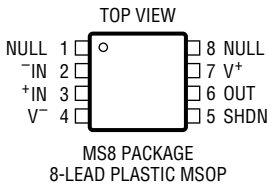
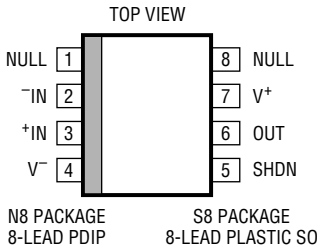
## ABSOLUTE MAXIMUM RATINGS

(Note 1)

Total Supply Voltage ( $V^+$  to  $V^-$ ) ..... 44V  
 Input Differential Voltage ..... 44V  
 Input Current .....  $\pm 25\text{mA}$   
 Shutdown Pin Voltage Above  $V^-$  ..... 32V  
 Shutdown Pin Current .....  $\pm 10\text{mA}$   
 Output Short-Circuit Duration (Note 2) ..... Continuous

Operating Temperature Range .....  $-40^\circ\text{C}$  to  $85^\circ\text{C}$   
 Specified Temperature Range (Note 3) ...  $-40^\circ\text{C}$  to  $85^\circ\text{C}$   
 Junction Temperature .....  $150^\circ\text{C}$   
 Storage Temperature Range .....  $-65^\circ\text{C}$  to  $150^\circ\text{C}$   
 Lead Temperature (Soldering, 10 sec) .....  $300^\circ\text{C}$

## PACKAGE/ORDER INFORMATION

 <p>MS8 PACKAGE 8-LEAD PLASTIC MSOP</p> <p><math>T_{JMAX} = 150^\circ\text{C}</math>, <math>\theta_{JA} = 250^\circ\text{C/W}</math></p>	ORDER PART NUMBER	 <p>N8 PACKAGE 8-LEAD PDIP</p> <p>S8 PACKAGE 8-LEAD PLASTIC SO</p> <p><math>T_{JMAX} = 150^\circ\text{C}</math>, <math>\theta_{JA} = 130^\circ\text{C/W}</math> (N8)  <math>T_{JMAX} = 150^\circ\text{C}</math>, <math>\theta_{JA} = 190^\circ\text{C/W}</math> (S8)</p>	ORDER PART NUMBER
	LT1637CMS8		LT1637CN8 LT1637CS8 LT1637IN8 LT1637IS8
	MS8 PART MARKING		S8 PART MARKING
	LTIE		1637 1637I

Consult factory for Military grade parts.

## 3V AND 5V ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ .  
 $V_S = 3\text{V}$ ,  $0\text{V}$ ;  $V_S = 5\text{V}$ ,  $0\text{V}$ ;  $V_{SHDN} = V^-$ ,  $V_{CM} = V_{OUT} = \text{Half Supply}$  unless otherwise specified. (Note 3)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
$V_{OS}$	Input Offset Voltage	N8, S8 Packages $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	●	100	350	$\mu\text{V}$
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●		550	$\mu\text{V}$
					700	$\mu\text{V}$
		MS8 Package $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	●	100	350	$\mu\text{V}$
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●		750	$\mu\text{V}$
			●		900	$\mu\text{V}$
	Input Offset Voltage Drift (Note 8)	N8, S8 Packages, $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	1	3	$\mu\text{V}/^\circ\text{C}$
		MS8 Package, $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	2		$\mu\text{V}/^\circ\text{C}$
$I_{OS}$	Input Offset Current	$V_{CM} = 44\text{V}$ (Note 4)	●	0.4	6.0	nA
			●		2.5	$\mu\text{A}$
$I_B$	Input Bias Current	$V_{CM} = 44\text{V}$ (Note 4)	●	20	50	nA
		$V_S = 0\text{V}$	●	30	60	$\mu\text{A}$
				0.1		nA
	Input Noise Voltage	0.1Hz to 10Hz		1		$\mu\text{V}_{P-P}$
$e_n$	Input Noise Voltage Density	$f = 1\text{kHz}$		27		$\text{nV}/\sqrt{\text{Hz}}$
$i_n$	Input Noise Current Density	$f = 1\text{kHz}$		0.17		$\text{pA}/\sqrt{\text{Hz}}$

## 3V AND 5V ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ .  
 $V_S = 3\text{V}$ , 0V;  $V_S = 5\text{V}$ , 0V;  $V_{\text{SHDN}} = V^-$ ,  $V_{\text{CM}} = V_{\text{OUT}} = \text{Half Supply}$  unless otherwise specified. (Note 3)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
$R_{\text{IN}}$	Input Resistance	Differential Common Mode, $V_{\text{CM}} = 0\text{V}$ to $44\text{V}$	1 0.7	2.6 1.4		$\text{M}\Omega$ $\text{M}\Omega$
$C_{\text{IN}}$	Input Capacitance			4		pF
	Input Voltage Range	●	0		44	V
$\text{CMRR}$	Common Mode Rejection Ratio (Note 4)	$V_{\text{CM}} = 0\text{V}$ to $(V_{\text{CC}} - 1\text{V})$ $V_{\text{CM}} = 0\text{V}$ to $44\text{V}$ (Note 7)	● 88 ● 80	110 98		dB dB
$A_{\text{VOL}}$	Large-Signal Voltage Gain	$V_S = 3\text{V}$ , $V_0 = 500\text{mV}$ to $2.5\text{V}$ , $R_L = 10\text{k}$ $V_S = 3\text{V}$ , $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $V_S = 3\text{V}$ , $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	● 150 ● 100 ● 75	400		V/mV V/mV V/mV
		$V_S = 5\text{V}$ , $V_0 = 500\text{mV}$ to $4.5\text{V}$ , $R_L = 10\text{k}$ $V_S = 5\text{V}$ , $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $V_S = 5\text{V}$ , $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	● 300 ● 200 ● 150	800		V/mV V/mV V/mV
$V_{\text{OL}}$	Output Voltage Swing LOW	No Load $I_{\text{SINK}} = 5\text{mA}$ $V_S = 5\text{V}$ , $I_{\text{SINK}} = 10\text{mA}$	● ● ●	3 325 580	8 700 1300	mV mV mV
$V_{\text{OH}}$	Output Voltage Swing HIGH	$V_S = 3\text{V}$ , No Load $V_S = 3\text{V}$ , $I_{\text{SOURCE}} = 5\text{mA}$	● 2.94 ● 2.25	2.975 2.67		V V
		$V_S = 5\text{V}$ , No Load $V_S = 5\text{V}$ , $I_{\text{SOURCE}} = 10\text{mA}$	● 4.94 ● 3.80	4.975 4.45		V V
$I_{\text{SC}}$	Short-Circuit Current (Note 2)	$V_S = 3\text{V}$ , Short to Ground $V_S = 3\text{V}$ , Short to $V_{\text{CC}}$	10 15	14 45		mA mA
		$V_S = 5\text{V}$ , Short to Ground $V_S = 5\text{V}$ , Short to $V_{\text{CC}}$	15 15	22 60		mA mA
$\text{PSRR}$	Power Supply Rejection Ratio	$V_S = 3\text{V}$ to $12.5\text{V}$ , $V_{\text{CM}} = V_0 = 1\text{V}$	● 90	98		dB
	Minimum Supply Voltage		●		2.7	V
	Reverse Supply Voltage	$I_S = -100\mu\text{A}$	● 25	40		V
$I_S$	Supply Current (Note 5)		●	190 295	250 295	$\mu\text{A}$ $\mu\text{A}$
	Supply Current, SHDN	$V_{\text{PIN5}} = 2\text{V}$ , No Load (Note 5)	●	3	12	$\mu\text{A}$
$I_{\text{SHDN}}$	Shutdown Pin Current	$V_{\text{PIN5}} = 0.3\text{V}$ , No Load (Note 5) $V_{\text{PIN5}} = 2\text{V}$ , No Load (Note 4)	● ●	0.2 1.0	15 5	nA $\mu\text{A}$
	Output Leakage Current	$V_{\text{PIN5}} = 2\text{V}$ , No Load (Note 5)	●	0.02	1	$\mu\text{A}$
	Maximum Shutdown Pin Current	$V_{\text{PIN5}} = 32\text{V}$ , No Load (Note 4)	●	20	150	$\mu\text{A}$
$t_{\text{ON}}$	Turn-On Time	$V_{\text{PIN5}} = 5\text{V}$ to $0\text{V}$ , $R_L = 10\text{k}$		45		$\mu\text{s}$
$t_{\text{OFF}}$	Turn-Off Time	$V_{\text{PIN5}} = 0\text{V}$ to $5\text{V}$ , $R_L = 10\text{k}$		3		$\mu\text{s}$
$\text{GBW}$	Gain-Bandwidth Product (Note 4)	$f = 10\text{kHz}$ $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	● 650 ● 550 ● 500	1000		kHz kHz kHz
$\text{SR}$	Slew Rate (Note 6)	$A_V = -1$ , $R_L = \infty$ $0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$ $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	● 0.210 ● 0.185 ● 0.170	0.35		V/ $\mu\text{s}$ V/ $\mu\text{s}$ V/ $\mu\text{s}$

## ±15V ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ .  
 $V_S = \pm 15\text{V}$ ,  $V_{CM} = 0\text{V}$ ,  $V_{OUT} = 0\text{V}$ ,  $V_{SHDN} = V^-$  unless otherwise specified. (Note 3)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
$V_{OS}$	Input Offset Voltage	N8, S8 Packages		100	450	$\mu\text{V}$
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	●		650	$\mu\text{V}$
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●		800	$\mu\text{V}$
		MS8 Package		100	450	$\mu\text{V}$
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	●		800	$\mu\text{V}$
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●		950	$\mu\text{V}$
	Input Offset Voltage Drift (Note 8)	N8, S8 Packages, $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	1	3	$\mu\text{V}/^\circ\text{C}$
		MS8 Package, $-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	2		$\mu\text{V}/^\circ\text{C}$
$I_{OS}$	Input Offset Current		●	1	6	nA
$I_B$	Input Bias Current		●	17	50	nA
	Input Noise Voltage	0.1Hz to 10Hz		1		$\mu\text{V}_{P-P}$
$e_n$	Input Noise Voltage Density	$f = 1\text{kHz}$		23		$\text{nV}/\sqrt{\text{Hz}}$
$i_n$	Input Noise Current Density	$f = 1\text{kHz}$		0.3		$\text{pA}/\sqrt{\text{Hz}}$
$R_{IN}$	Input Resistance	Differential		1	3	M $\Omega$
		Common Mode, $V_{CM} = -15\text{V}$ to $14\text{V}$			2200	M $\Omega$
$C_{IN}$	Input Capacitance			4		pF
	Input Voltage Range		●	-15	29	V
CMRR	Common Mode Rejection Ratio	$V_{CM} = -15\text{V}$ to $29\text{V}$	●	80	110	dB
$A_{VOL}$	Large-Signal Voltage Gain	$V_O = \pm 14\text{V}$ , $R_L = 10\text{k}$		100	400	V/mV
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	●	75		V/mV
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	50		V/mV
$V_{OL}$	Output Voltage Swing LOW	No Load	●	-14.997	-14.95	V
		$I_{SINK} = 5\text{mA}$	●	-14.680	-14.25	V
		$I_{SINK} = 10\text{mA}$	●	-14.420	-13.65	V
$V_{OH}$	Output Voltage Swing HIGH	No Load	●	14.9	14.967	V
		$I_{SOURCE} = 5\text{mA}$	●	14.2	14.667	V
		$I_{SOURCE} = 10\text{mA}$	●	13.7	14.440	V
$I_{SC}$	Short-Circuit Current (Note 2)	Short to GND		$\pm 25$	$\pm 31.7$	mA
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	●	$\pm 20$		mA
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	●	$\pm 15$		mA
PSRR	Power Supply Rejection Ratio	$V_S = \pm 1.5\text{V}$ to $\pm 22\text{V}$	●	90	115	dB
	Minimum Supply Voltage		●		$\pm 1.35$	V
$I_S$	Supply Current			230	300	$\mu\text{A}$
			●		370	$\mu\text{A}$
	Positive Supply Current, SHDN	$V_{PIN5} = -20\text{V}$ , $V_S = \pm 22\text{V}$ , No Load	●	6	40	$\mu\text{A}$
$I_{SHDN}$	Shutdown Pin Current	$V_{PIN5} = -21.7\text{V}$ , $V_S = \pm 22\text{V}$ , No Load	●	0.3	15	nA
		$V_{PIN5} = -20\text{V}$ , $V_S = \pm 22\text{V}$ , No Load	●	0.9	8	$\mu\text{A}$
	Maximum Shutdown Pin Current	$V_{PIN5} = 32\text{V}$ , $V_S = \pm 22\text{V}$	●	20	150	$\mu\text{A}$
	Output Leakage Current	$V_{PIN5} = -20\text{V}$ , $V_S = \pm 22\text{V}$ , No Load	●	0.02	2	$\mu\text{A}$
$V_L$	Shutdown Pin Input Low Voltage	$V_S = \pm 22\text{V}$	●	-21.6	-21.7	V
$V_H$	Shutdown Pin Input High Voltage	$V_S = \pm 22\text{V}$	●	-20.0	-20.8	V
$t_{ON}$	Turn-On Time	$V_{PIN5} = -10\text{V}$ to $-15\text{V}$ , $R_L = 10\text{k}$		35		$\mu\text{s}$
$t_{OFF}$	Turn-Off Time	$V_{PIN5} = -15\text{V}$ to $-10\text{V}$ , $R_L = 10\text{k}$		3		$\mu\text{s}$

# ±15V ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ .  $V_S = \pm 15\text{V}$ ,  $V_{CM} = 0\text{V}$ ,  $V_{OUT} = 0\text{V}$ ,  $V_{SHDN} = V^-$  unless otherwise specified. (Note 3)

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
GBW	Gain-Bandwidth Product	$f = 10\text{kHz}$	750	1100		kHz
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	● 650			kHz
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	● 600			kHz
SR	Slew Rate	$A_V = -1$ , $R_L = \infty$ , $V_O = \pm 10\text{V}$ , Measure at $V_O = \pm 5\text{V}$	0.225	0.4		$\text{V}/\mu\text{s}$
		$0^\circ\text{C} \leq T_A \leq 70^\circ\text{C}$	● 0.200			$\text{V}/\mu\text{s}$
		$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$	● 0.180			$\text{V}/\mu\text{s}$

**Note 1:** Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

**Note 2:** A heat sink may be required to keep the junction temperature below absolute maximum.

**Note 3:** The LT1637C is guaranteed to meet specified performance from  $0^\circ\text{C}$  to  $70^\circ\text{C}$  and is designed, characterized and expected to meet these extended temperature limits, but is not tested at  $-40^\circ\text{C}$  and  $85^\circ\text{C}$ . The LT1637I is guaranteed to meet the extended temperature limits.

**Note 4:**  $V_S = 5\text{V}$  limits are guaranteed by correlation to  $V_S = 3\text{V}$  and  $V_S = \pm 15\text{V}$  or  $V_S = \pm 22\text{V}$  tests.

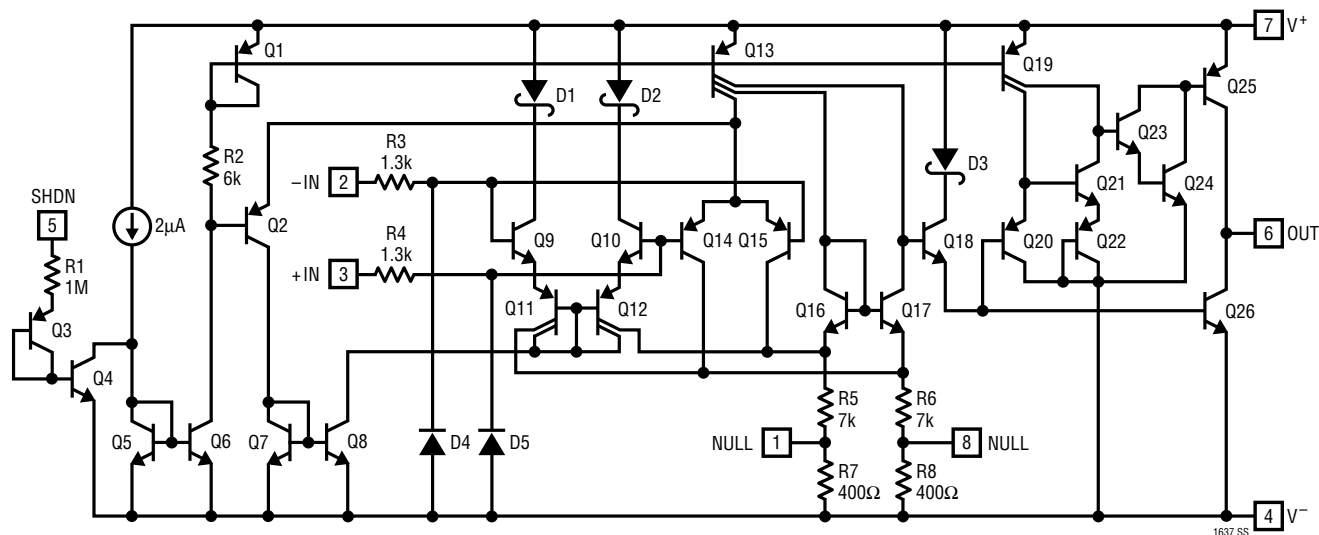
**Note 5:**  $V_S = 3\text{V}$  limits are guaranteed by correlation to  $V_S = 5\text{V}$  and  $V_S = \pm 15\text{V}$  or  $V_S = \pm 22\text{V}$  tests.

**Note 6:** Guaranteed by correlation to slew rate at  $V_S = \pm 15\text{V}$  and GBW at  $V_S = 3\text{V}$  and  $V_S = \pm 15\text{V}$  tests.

**Note 7:** This specification implies a typical input offset voltage of  $600\mu\text{V}$  at  $V_{CM} = 44\text{V}$  and a maximum input offset voltage of  $4.4\text{mV}$  at  $V_{CM} = 44\text{V}$ .

**Note 8:** This parameter is not 100% tested.

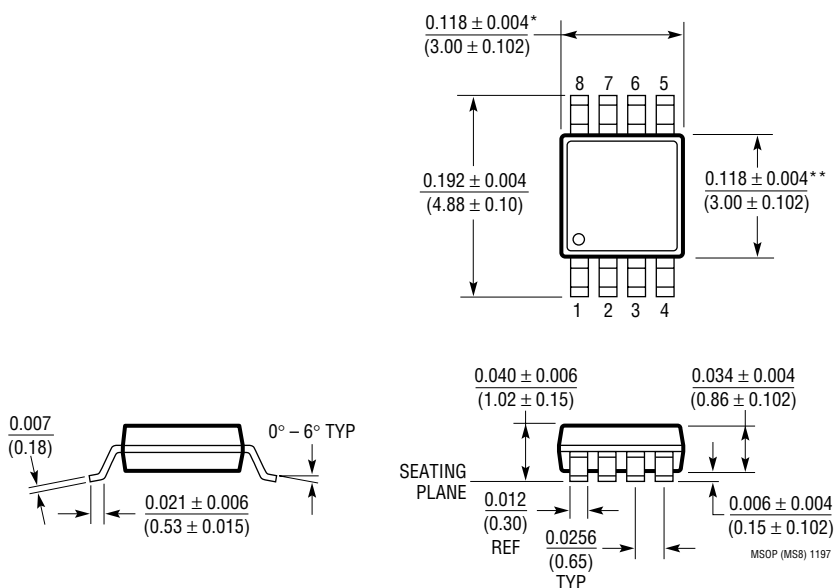
## SIMPLIFIED SCHEMATIC



# **PACKAGE DESCRIPTION**

Dimensions in inches (millimeters) unless otherwise noted.

## **MS8 Package** **8-Lead Plastic MSOP** (LTC DWG # 05-08-1660)



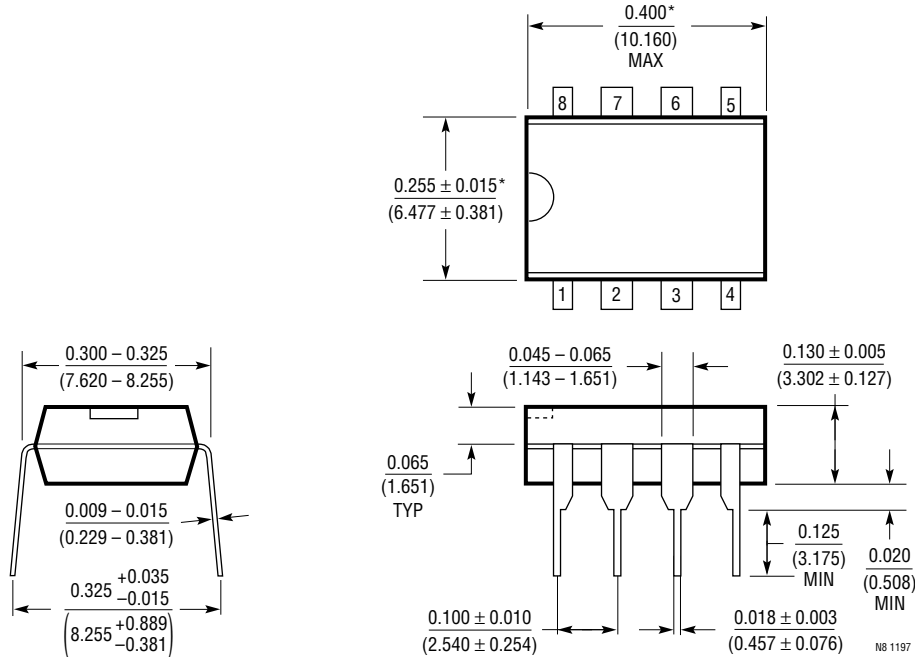
\* DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

\*\* DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS. INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

# PACKAGE DESCRIPTION

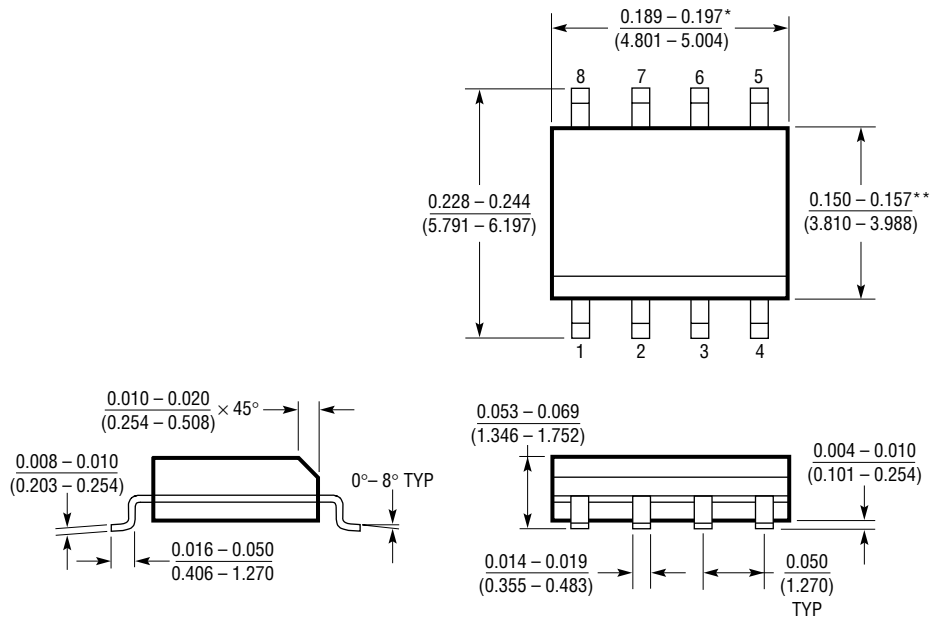
Dimensions in inches (millimeters) unless otherwise noted.

## N8 Package 8-Lead PDIP (Narrow 0.300) (LTC DWG # 05-08-1510)



\*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.  
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.010 INCH (0.254mm)

## S8 Package 8-Lead Plastic Small Outline (Narrow 0.150) (LTC DWG # 05-08-1610)



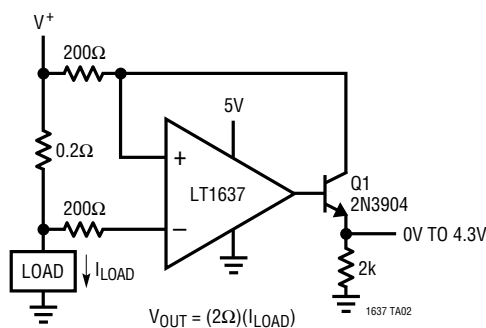
\*DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH  
SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

\*\*DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD  
FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

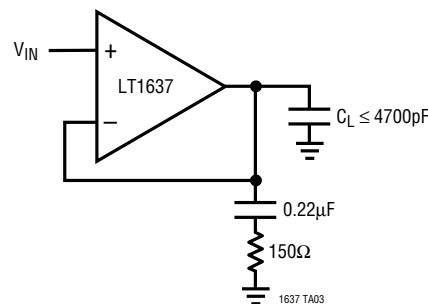
S08 0996

TYPICAL APPLICATIONS

Positive Supply Rail Current Sense



Optional Output Compensation for Capacitive Loads Greater Than 200pF



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1078/LT1079 LT2078/LT2079	Dual/Quad 55μA Max, Single Supply, Precision Op Amps	Input/Output Common Mode Includes Ground, 70μV $V_{OS(MAX)}$ and 2.5μV/°C Drift (Max), 200kHz GBW, 0.07V/μs Slew Rate
LT1178/LT1179 LT2178/LT2179	Dual/Quad 17μA Max, Single Supply, Precision Op Amps	Input/Output Common Mode Includes Ground, 70μV $V_{OS(MAX)}$ and 4μV/°C Drift (Max), 85kHz GBW, 0.04V/μs Slew Rate
LT1366/LT1367	Dual/Quad Precision, Rail-to-Rail Input and Output Op Amps	475μV $V_{OS(MAX)}$ , 500V/mV $A_{VOL(MIN)}$ , 400kHz GBW
LT1490/LT1491	Dual/Quad Over-The-Top Micropower, Rail-to-Rail Input and Output Op Amps	Single Supply Input Range: -0.4V to 44V, Micropower 50μA per Amplifier, Rail-to-Rail Input and Output, 200kHz GBW
LT1636	Single Over-The-Top Micropower Rail-to-Rail Input and Output Op Amp	55μA Supply Current, $V_{CM}$ Extends 44V above $V_{EE}$ , Independent of $V_{CC}$ ; MSOP Package, Shutdown Function
LT1638/LT1639	Dual/Quad 1.2MHz Over-The-Top Micropower, Rail-to-Rail Input and Output Op Amps	0.4V/μs Slew Rate, 230μA Supply Current per Amplifier