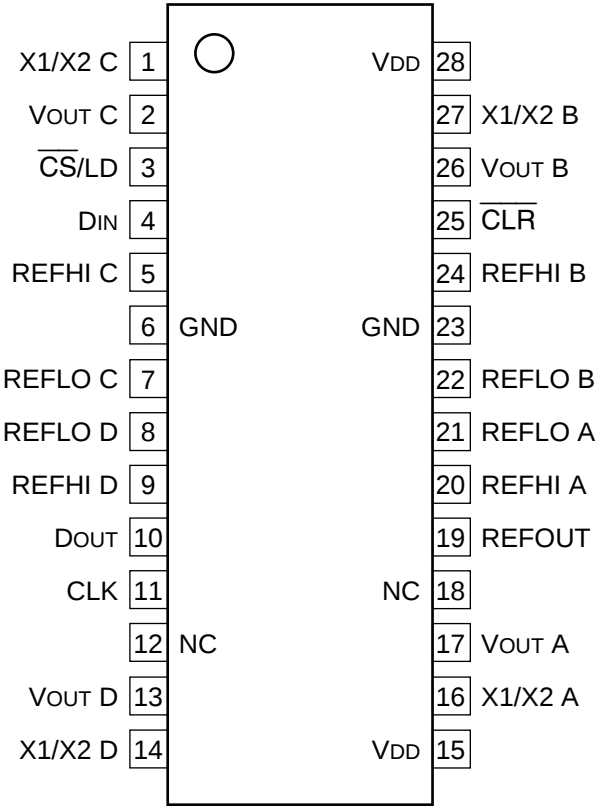


C-MOS QUAD 12-BIT RAIL-TO-RAIL MICROPOWER DAC

—TOP VIEW—



- INPUT**
CLK : SERIAL INTERFACE CLOCK
CLR : CLEAR
CS/LD : SERIAL INTERFACE ENABLE/
LOAD CONTROL
DIN : SERIAL DATA
REFHI : TOP REFERENCE
REFLO : BOTTOM REFERENCE
X1/X2 : GAIN CONTROL
- OUTPUT**
DOUT : SERIAL DATA VIA SHIFT REGISTER
REFOUT : INTERNAL REFERENCE VOLTAGE
VOUT : D-to-A CONVERTED VOLTAGE

