

# **UTC LP2950/2951    LINEAR INTEGRATED CIRCUIT**

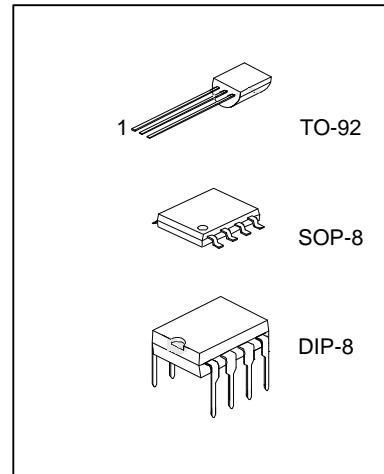
## **100 mA LOW-DROPOUT VOLTAGE REGULATOR**

### **DESCRIPTION**

The UTC LP2950/2951 is a monolithic integrated voltage regulator with low dropout voltage, and low quiescent current. It includes many features that suitable for different applications. Available in 3-pin TO-92, DIP-8 and SOP-8 packages.

### **FEATURES**

- \*High accuracy 3.0, 3.3, or 5V fixed output for TO-92 package.
- \*Extremely low quiescent current and dropout voltage.
- \*Extremely tight load and line regulation.
- \*Current and thermal limiting.
- \*Very low temperature coefficient.
- \*Logic controlled shutdown and error flag available for DIP and SOP package.
- \*Output voltage programmable for DIP and SOP package.



### **APPLICATIONS**

- \*Battery powered equipment.
- \*High efficient linear regulator down to 1.24V.
- \*Cellular phones.

### **ORDERING INFORMATION**

PART NUMBER	TEMPERATURE RANGE	PACKAGE	ACCURACY
UTC LP2950-3.0	-40 ~ +125°C	3-Pin TO-92 plastic	1.0%
UTC LP2950-3.3	-40 ~ +125°C	3-Pin TO-92 plastic	2.0%
UTC LP2950	-40 ~ +125°C	3-Pin TO-92 plastic	1.0%
UTC LP2951F	-40 ~ +125°C	8-Pin SOP-8 plastic	2.0%
UTC LP2951P	-40 ~ +125°C	8-Pin DIP-8 plastic	2.0%

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## PIN CONFIGURATIONS

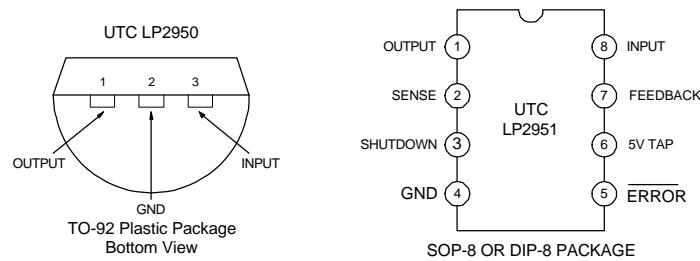
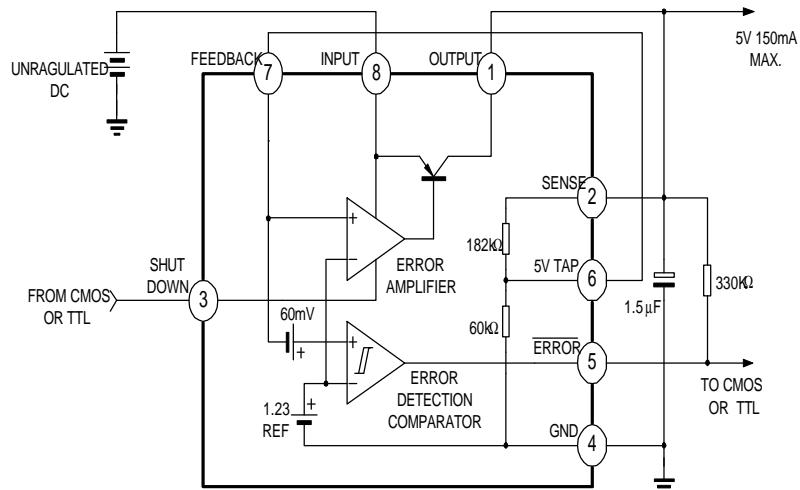


Fig. 1

## BLOCK DIAGRAM



## ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Supply Voltage	V <sub>cc</sub>	-0.3~+30	V
Feedback Voltage	V <sub>feedback</sub>	-1.5~+30	V
Shutdown Voltage	V <sub>shutdown</sub>	-0.3~+30	V
Comparator Output Voltage	V <sub>co</sub>	-0.3~+30	V
Storage Temperature	T <sub>str</sub>	-65~+150	°C
Operating Junction Temperature	T <sub>j</sub>	-40~+125	°C

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## ELECTRICAL CHARACTERISTICS

( Tested at  $T_j=25^\circ\text{C}$ ,  $V_{IN}=6\text{V}$ ,  $I_L=100\text{A}$  and  $C_L=1\text{F}$ .unless otherwise specified)

PARAMETER	PART NUMBER	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage	UTC LP2950-3.0	$T_j=25^\circ\text{C}$  (note 1)	2.97	3.0	3.03	V
	UTC LP2950-3.3		3.26	3.3	3.37	
	UTC LP2950		4.90	5.0	5.10	
	UTC LP2951					
	UTC LP2950-3.0	$-25^\circ\text{C} \leq T_j \leq +85^\circ\text{C}$  (note 1)	2.97	3.0	3.03	V
	UTC LP2950-3.3		3.26	3.3	3.37	
	UTC LP2950		4.90	5.0	5.10	
	UTC LP2951					
Output Voltage	UTC LP2950-3.0 UTC LP2950-3.3 UTC LP2950 UTC LP2951	$100\mu\text{A} \leq I_L \leq 100\text{ mA}$ $T_j \leq T_j(\text{max})$ (note 1)	2.97 3.26 4.90	3.0 3.3 5.0	3.03 3.37 5.10	V
Output Voltage Temperature Coefficient			20		100	$\text{ppm}/^\circ\text{C}$
Line Regulation		$6\text{V} \leq V_{IN} \leq 30\text{V}$	0.03	0.1	0.2	%
Load Regulation		$100\text{A} \leq I_L \leq 100\text{ mA}$	0.04	0.1	0.2	%
Dropout Voltage		$I_L=100\mu\text{A}$ $I_L=100\text{mA}$ (note 2)	50 380	80 450	150 600	mV
Ground Current		$I_L=100\mu\text{A}$ $I_L=100\text{mA}$	75 8	120 12	140 14	$\mu\text{A}$ mA
Dropout Ground Current		$V_{IN}=4.5\text{V}, I_L=100\mu\text{A}$	110	170	200	$\mu\text{A}$
Current Limit		$V_{OUT}=0$	160	200	220	mA
Output Noise 10Hz to 100KHz		$C_L=1\mu\text{F}$ $C_L=200\mu\text{F}$ $C_L=3.3\mu\text{F}$ (Bypass=0.01 $\mu\text{F}$ pins 7 to (utc2951))			430 160 100	$\mu\text{V}$
For 8-Pin version only						
Reference Voltage			1.22	1.235	1.25	V
Reference Voltage		(Note 7)	1.19		1.27	V
Feedback pin Bias Current				20	40	nA
Reference Voltage Temperature Coefficient				50		$\text{ppm}/^\circ\text{C}$
Feedback Bias Current temperature Coefficient				0.1		$\text{nA}/^\circ\text{C}$
Error Comparator						
Output Leakage Current		$V_{OH}=30\text{V}$			1	$\mu\text{A}$
Output Low Voltage		$V_{IN}=4.5\text{V}$ $I_{OL}=400\mu\text{A}$			250	mV
Upper Threshold Voltage		(Note 3)	3.2			$\%V_O$

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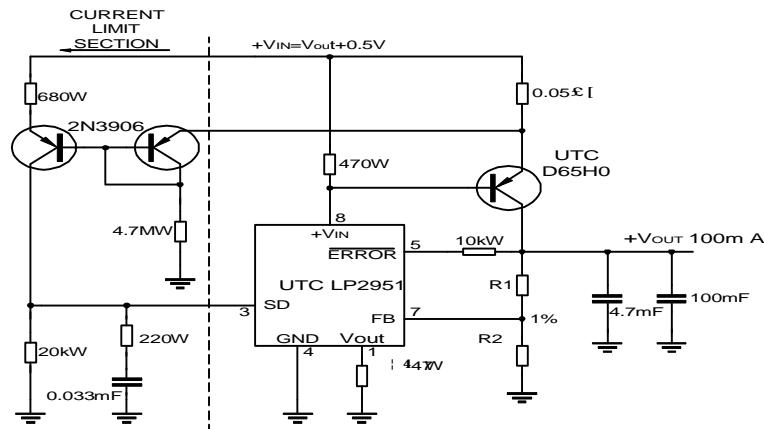
PARAMETER	PART NUMBER	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Lower Threshold Voltage		(Note 3)			7.6	% $V_o$
Hysteresis		(Note 3)		15		mV
Shutdown Input						
Input Logic Voltage		Low(Regulator ON) High(Regulator OFF)	2.0	1.3	0.70	V
Shutdown Pin Input Current		$V_{shutdown}=2.4V$		30	50	$\mu A$
		$V_{shutdown}=30V$		450	600	$\mu A$
Regulator Output Current Shutdown		$V_{shutdown}>=2V, V_{IN}<=30V, V_{out}=0,$ Feedback pin tied to 5V Tap.		3	10	$\mu A$

Note 1: Additional conditions for 8-pin versions are feedback tied to 5V Tap an Output tied to Output Sense ( $V_{out}=5V$ ) and  $V_{shutdown}<=0.8V$ .

Note 2: Dropout Voltage is defined as the input to output differential at which the output voltage drops 100mV below its nominal value measured at 1V differential.

Note 3: Comparator thresholds are expressed in terms of percentage value of voltage output.

## APPLICATION CIRCUIT (10 Ampere Low Dropout Regulator)



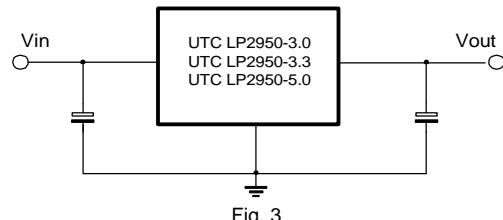
$$V_{out} = 1.23V \times (1 + R_1/R_2)$$

For 5V output use internal resistors. Wire pin 6 to 7 and wire pin 2 to +Vout

Fig.2

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## TYPICAL PERFORMANCE CHARACTERISTICS

