

APPLICATION INFORMATION

Demoboard 900 MHz LNA with the BGA2003

Application Note

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SUMMARY

- **Description of products**

Monolithic Microwave Integrated Circuit (MMIC): RF transistor with internal bias circuit. The benefit is lower component count, low production spread and enabling function by I_{ctrl} with high isolation when shutoff.

- **Application Area**

Low noise amplifier for systems like GSM, DECT, DCS with low component count.

- **Presented Application**

The applications present a low noise amplifier at 900 MHz at 3 V supply voltage and 4 mA supply current with matching components.

- **Main results**

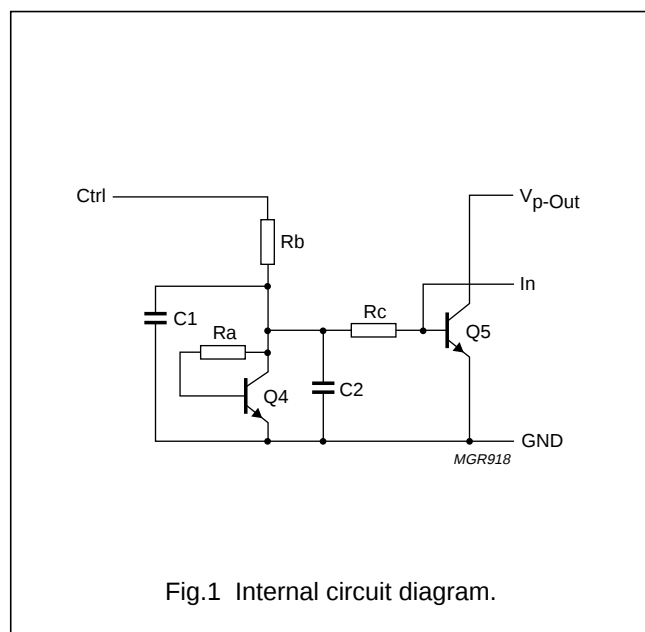
An amplifier has been designed and tested with minimum component count with 15.4 dB gain, $IIP3 = -6.7$ dBm, $VSWR_{in} = 1.5$, $VSWR_{out} = 1.8$, 1.8 dB Noise Figure at 900 MHz, 3 V and 4 mA supply.

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THE INTERNAL BGA2003 CIRCUIT

For understanding the behaviour of the BGA2003 MMIC the internal circuit diagram is given in Fig.1.



Q5 is the main RF transistor. Q4 forms a current mirror with Q5. The input current of this current mirror is determined by the current into pin Ctrl. Rb limits the current when a control voltage is applied to bias this circuit. Rc, and C2 in parallel with C1 decouple the bias circuit from the RF input signal.

SIMULATION OF THE BGA2003 DEMOBOARD

S-parameters of the BGA2003 MMIC were measured at $V_{p-Out} = 2.5$ V, $I_C = 4$ mA.

Table 1 Measured S-parameters

FREQ. MHz	MAG S_{11}	PHASE S_{11}	MAG S_{21}	PHASE S_{21}	MAG S_{12}	PHASE S_{12}	MAG S_{22}	PHASE S_{22}
800	0.5039	-45.05	7.2572	132.84	0.03984	73.68	0.7980	-26.81
900	0.4774	-47.83	6.7996	128.60	0.04393	72.61	0.7751	-28.52
1000	0.4523	-50.23	6.3946	125.36	0.04564	72.24	0.7520	-30.12

Table 2 Measured NF-parameters

FREQ. MHz	FMIN dB	GAMMA-OPT MAGNITUDE	GAMMA-OPT PHASE [DEG]	RN [Ω]
800	1.685	0.160	26.57	0.276
900	1.703	0.182	28.86	0.275
1000	1.700	0.153	31.56	0.261

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An additional emitter inductance of 2.0 nH was added in the MDS simulation. The emitter inductance on the Printed Circuit Board (PCB) is beneficial for achieving good noise match, for stability and easier impedance match.

Optimization of the matching components was done for the parameters $VSWR_{in}$ and $VSWR_{out}$ ($<1 : 1.5$), resulting in component values, which were rounded up to nearby practical values. These values have been used on the PCB (see Table 1).

Important remarks

Available samples were made with 'worst case' processing. Optimal noise matching and use of typical products are expected to result in an improvement of the noise figure.

APPLICATION CIRCUIT

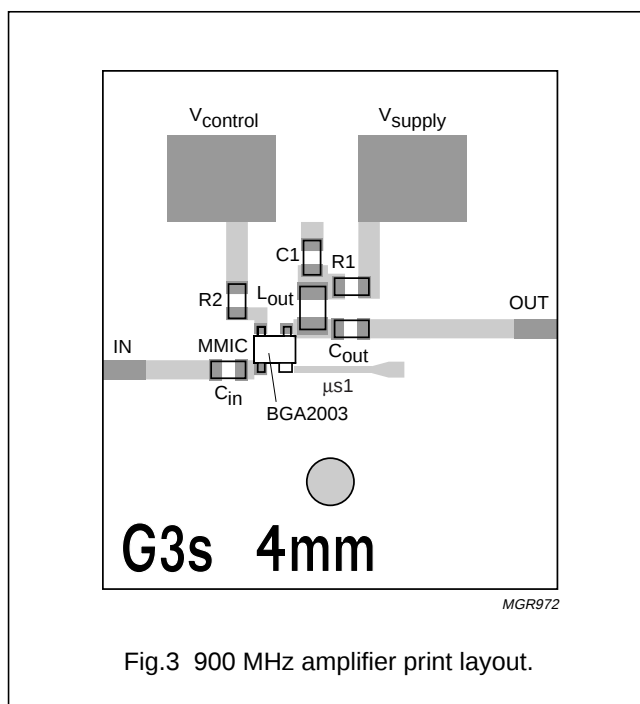
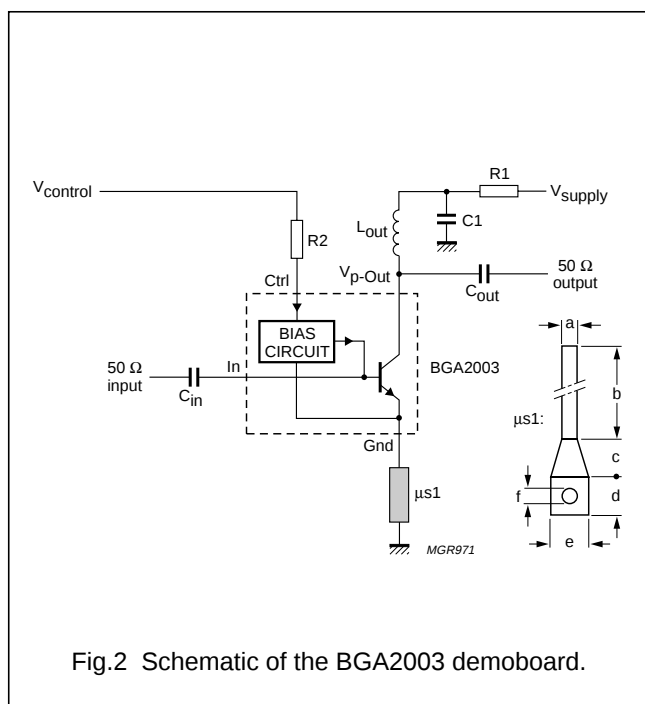


Table 3 Used components for the BGA2003 demoboard

COMPONENT	VALUE	UNIT	SIZE - MANUFACTURER	PURPOSE, COMMENT
R1	120	Ω	0603 Philips	DC-bias
R2	5.1	kΩ	0603 Philips	DC-bias
C1	27	pF	0603 Philips NP0	RF-short to ground
C _{in}	1	nF	0603 Philips X7R	input match, DC-decoupling
L _{out}	18	nH	0603 TDK	output match
C _{out}	1.2	pF	0603 Philips NP0	output match
μs 1	—	—	PCB-stripline 50 Ω, via	a = 0.5 mm, b = 4 mm, c = d = e = 1 mm, f = 0.4 mm
MMIC	BGA2003	—	Philips SOT343R1	
PCB	—	—	FR4	ε _R ~ 4.6, H = 0.5 mm

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Table 4 Measured values

 PCB number 24: $I_{\text{supply}} = 4.04 \text{ mA}$, $V_{\text{supply}} = 3.0 \text{ V}$; see note 1.

S-PARAMETERS	800 MHz	900 MHz	1000 MHz	UNIT
$V_{\text{ctrl}} = 3 \text{ V}$				
NF	1.82	1.81	1.70	dB
IIP3	–	–6.755; see note 2	–	dBm
S_{11}	–6.701	–15.456	–13.286	dB
S_{21}	16.014	15.359	13.707	dB
S_{12}	–22.851	–21.755	–21.472	dB
S_{22}	–10.829	–10.924	–5.441	dB
$V_{\text{ctrl}} = 0 \text{ V}$				
S_{11}	–1.012	–1.137	–1.178	dB
S_{21}	–22.052	–24.122	–26.088	dB
S_{12}	–22.221	–24.258	–26.802	dB
S_{22}	–8.362	–3.293	–1.703	dB

Notes

1. S-parameters measured at –30 dBm input level.
2. 2-tone –26 dBm at 900 and 900.1 MHz.

COMMENTS ON THE PRINTED CIRCUIT BOARD

This Printed Circuit Board (PCB) is developed for a LNA with the BGA2003, with minimum component count. C_{in} is for DC-decoupling the input to the circuit. L_{out} and C_{out} matches the circuit to the 50 Ω output. Decoupling the supply for high frequencies is done by R1 and C1. The value of R1 determines the voltage on $V_{\text{p-Out}}$, which was designed to be 2.5 V with a supply current of 4 mA. The value of R2 and the value of V_{control} determines the control current and thereby the collector current. With $R2 = 5.1 \text{ k}\Omega$ and $V_{\text{control}} = 3.0 \text{ V}$ a supply current of 4 mA was set (see Figs 4 and 5).

Typical $I_{\text{supply}} = 10 \times I_{\text{ctrl}}$. Also I_{bias} can be estimated by calculation with formula: $I_{\text{supply}} = \frac{10 \times (V_{\text{control}} - 0.83)}{(R2 + 152)}$

C_{in} can be omitted in some applications when the input signal is not DC-coupled.

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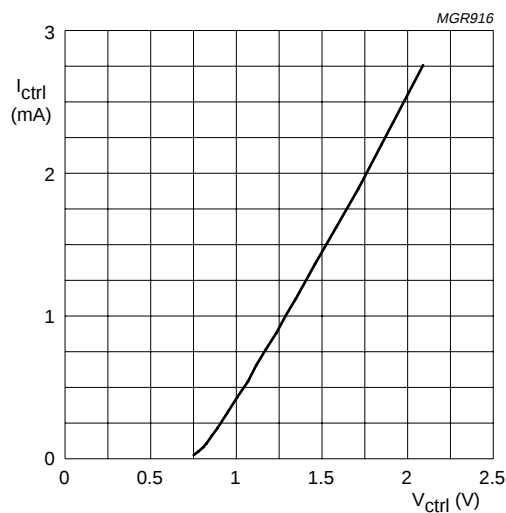


Fig.4 I_{ctrl} as function of voltage on I_{ctrl} -pin.

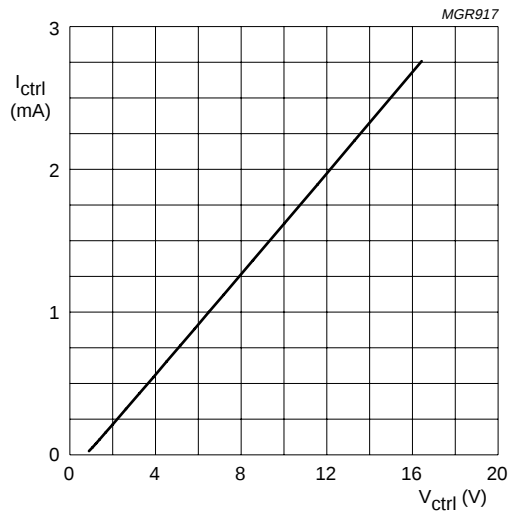


Fig.5 I_{ctrl} as function of control voltage with $R2 = 5.1\text{ k}$.

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NOTES

Philips Semiconductors – a worldwide company

Argentina: see South America

Australia: 34 Waterloo Road, NORTH RYDE, NSW 2113,
Tel. +61 2 9805 4455, Fax. +61 2 9805 4466

Austria: Computerstr. 6, A-1101 WIEN, P.O. Box 213,
Tel. +43 1 60 101 1248, Fax. +43 1 60 101 1210

Belarus: Hotel Minsk Business Center, Bld. 3, r. 1211, Volodarski Str. 6,
220050 MINSK, Tel. +375 172 20 0733, Fax. +375 172 20 0773

Belgium: see The Netherlands

Brazil: see South America

Bulgaria: Philips Bulgaria Ltd., Energoproject, 15th floor,
51 James Bourchier Blvd., 1407 SOFIA,
Tel. +359 2 68 9211, Fax. +359 2 68 9102

Canada: PHILIPS SEMICONDUCTORS/COMPONENTS,
Tel. +1 800 234 7381, Fax. +1 800 943 0087

China/Hong Kong: 501 Hong Kong Industrial Technology Centre,
72 Tat Chee Avenue, Kowloon Tong, HONG KONG,
Tel. +852 2319 7888, Fax. +852 2319 7700

Colombia: see South America

Czech Republic: see Austria

Denmark: Sydhavnsgade 23, 1780 COPENHAGEN V,
Tel. +45 33 29 3333, Fax. +45 33 29 3905

Finland: Sinikalliontie 3, FIN-02630 ESPOO,
Tel. +358 9 615 800, Fax. +358 9 6158 0920

France: 51 Rue Carnot, BP317, 92156 SURESNES Cedex,
Tel. +33 1 4099 6161, Fax. +33 1 4099 6427

Germany: Hammerbrookstraße 69, D-20097 HAMBURG,
Tel. +49 40 2353 60, Fax. +49 40 2353 6300

Greece: No. 15, 25th March Street, GR 17778 TAVROS/ATHENS,
Tel. +30 1 489 4339/4239, Fax. +30 1 481 4240

Hungary: see Austria

India: Philips INDIA Ltd, Band Box Building, 2nd floor,
254-D, Dr. Annie Besant Road, Worli, MUMBAI 400 025,
Tel. +91 22 493 8541, Fax. +91 22 493 0966

Indonesia: PT Philips Development Corporation, Semiconductors Division,
Gedung Philips, Jl. Buncit Raya Kav.99-100, JAKARTA 12510,
Tel. +62 21 794 0040 ext. 2501, Fax. +62 21 794 0080

Ireland: Newstead, Clonskeagh, DUBLIN 14,
Tel. +353 1 7640 000, Fax. +353 1 7640 200

Israel: RAPAC Electronics, 7 Kehilat Saloniki St, PO Box 18053,
TEL AVIV 61180, Tel. +972 3 645 0444, Fax. +972 3 649 1007

Italy: PHILIPS SEMICONDUCTORS, Piazza IV Novembre 3,
20124 MILANO, Tel. +39 2 6752 2531, Fax. +39 2 6752 2557

Japan: Philips Bldg 13-37, Kohnan 2-chome, Minato-ku,
TOKYO 108-8507, Tel. +81 3 3740 5130, Fax. +81 3 3740 5077

Korea: Philips House, 260-199 Itaewon-dong, Yongsan-ku, SEOUL,
Tel. +82 2 709 1412, Fax. +82 2 709 1415

Malaysia: No. 76 Jalan Universiti, 46200 PETALING JAYA, SELANGOR,
Tel. +60 3 750 5214, Fax. +60 3 757 4880

Mexico: 5900 Gateway East, Suite 200, EL PASO, TEXAS 79905,
Tel. +9-5 800 234 7381, Fax +9-5 800 943 0087

Middle East: see Italy

Netherlands: Postbus 90050, 5600 PB EINDHOVEN, Bldg. VB,
Tel. +31 40 27 82785, Fax. +31 40 27 88399

New Zealand: 2 Wagener Place, C.P.O. Box 1041, AUCKLAND,
Tel. +64 9 849 4160, Fax. +64 9 849 7811

Norway: Box 1, Manglerud 0612, OSLO,
Tel. +47 22 74 8000, Fax. +47 22 74 8341

Pakistan: see Singapore

Philippines: Philips Semiconductors Philippines Inc.,
106 Valero St. Salcedo Village, P.O. Box 2108 MCC, MAKATI,
Metro MANILA, Tel. +63 2 816 6380, Fax. +63 2 817 3474

Poland: Ul. Lukiska 10, PL 04-123 WARSZAWA,
Tel. +48 22 612 2831, Fax. +48 22 612 2327

Portugal: see Spain

Romania: see Italy

Russia: Philips Russia, Ul. Usatcheva 35A, 119048 MOSCOW,
Tel. +7 095 755 6918, Fax. +7 095 755 6919

Singapore: Lorong 1, Toa Payoh, SINGAPORE 319762,
Tel. +65 350 2538, Fax. +65 251 6500

Slovakia: see Austria

Slovenia: see Italy

South Africa: S.A. PHILIPS Pty Ltd., 195-215 Main Road Martindale,
2092 JOHANNESBURG, P.O. Box 7430 Johannesburg 2000,
Tel. +27 11 470 5911, Fax. +27 11 470 5494

South America: Al. Vicente Pinzon, 173, 6th floor,
04547-130 SÃO PAULO, SP, Brazil,
Tel. +55 11 821 2333, Fax. +55 11 821 2382

Spain: Balmes 22, 08007 BARCELONA,
Tel. +34 93 301 6312, Fax. +34 93 301 4107

Sweden: Kottbygatan 7, Akalla, S-16485 STOCKHOLM,
Tel. +46 8 5985 2000, Fax. +46 8 5985 2745

Switzerland: Allmendstrasse 140, CH-8027 ZÜRICH,
Tel. +41 1 488 2741 Fax. +41 1 488 3263

Taiwan: Philips Semiconductors, 6F, No. 96, Chien Kuo N. Rd., Sec. 1,
TAIPEI, Taiwan Tel. +886 2 2134 2886, Fax. +886 2 2134 2874

Thailand: PHILIPS ELECTRONICS (THAILAND) Ltd.,
209/2 Sanpavuth-Bangna Road Prakanong, BANGKOK 10260,
Tel. +66 2 745 4090, Fax. +66 2 398 0793

Turkey: Talatpasa Cad. No. 5, 80640 GÜLTEPE/ISTANBUL,
Tel. +90 212 279 2770, Fax. +90 212 282 6707

Ukraine: PHILIPS UKRAINE, 4 Patrice Lumumba str., Building B, Floor 7,
252042 KIEV, Tel. +380 44 264 2776, Fax. +380 44 268 0461

United Kingdom: Philips Semiconductors Ltd., 276 Bath Road, Hayes,
MIDDLESEX UB3 5BX, Tel. +44 181 730 5000, Fax. +44 181 754 8421

United States: 811 East Arques Avenue, SUNNYVALE, CA 94088-3409,
Tel. +1 800 234 7381, Fax. +1 800 943 0087

Uruguay: see South America

Vietnam: see Singapore

Yugoslavia: PHILIPS, Trg N. Pasica 5/v, 11000 BEOGRAD,
Tel. +381 11 62 5344, Fax. +381 11 63 5777

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International Marketing & Sales Communications, Building BE-p, P.O. Box 218,
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