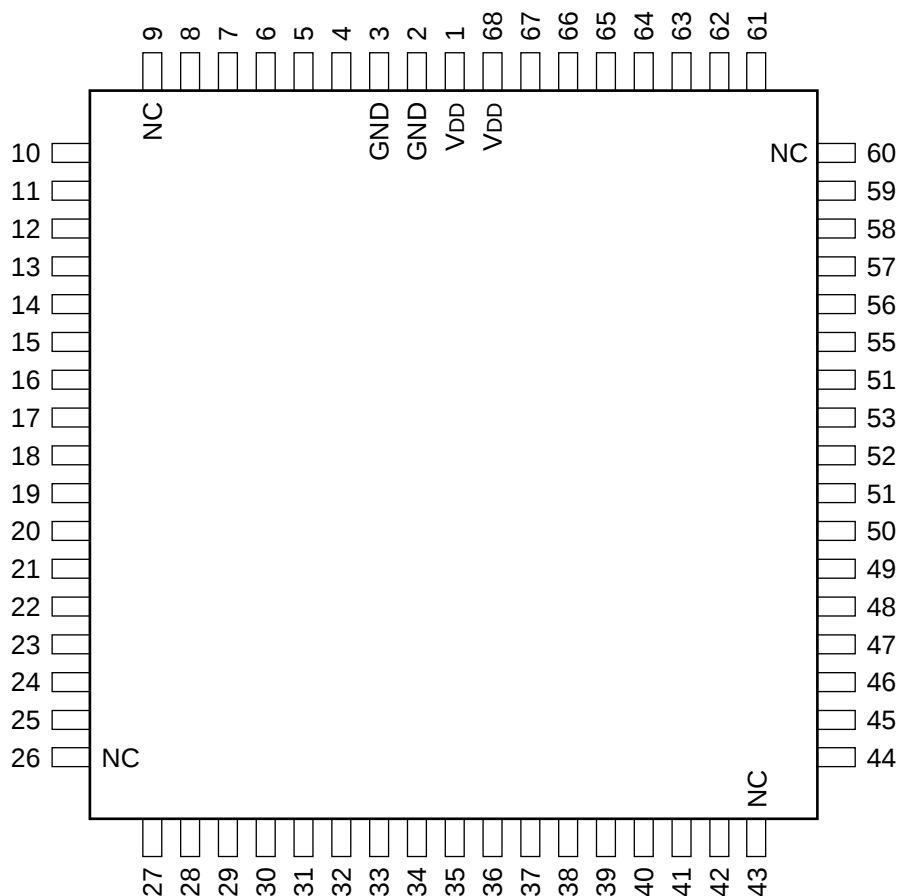


C-MOS (16 K × 16)-BIT PARALLEL MULTIPLIER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{DD}	18	O	R23	35	I/O	R7/B7	52	I	A5
2	—	GND	19	O	R22	36	I/O	R6/B6	53	I	A6
3	—	GND	20	O	R21	37	I/O	R5/B5	54	I	A7
4	I	MSPSEL	21	O	R20	38	I/O	R4/B4	55	I	A8
5	I	FT	22	O	R19	39	I/O	R3/B3	56	I	A9
6	I	RS	23	O	R18	40	I/O	R2/B2	57	I	A10
7	I	OEM	24	O	R17	41	I/O	R1/B1	58	I	A11
8	I	ENR	25	O	R16	42	I/O	R0/B0	59	I	A12
9	—	NC	26	—	NC	43	—	NC	60	—	NC
10	O	R31	27	I/O	R15/B15	44	I	ENB	61	I	A13
11	O	R30	28	I/O	R14/B14	45	I	CLK	62	I	A14
12	O	R29	29	I/O	R13/B13	46	I	OEL	63	I	A15
13	O	R28	30	I/O	R12/B12	47	I	A0	64	I	ENA
14	O	R27	31	I/O	R11/B11	48	I	A1	65	I	RND
15	O	R26	32	I/O	R10/B10	49	I	A2	66	I	TCA
16	O	R25	33	I/O	R9/B9	50	I	A3	67	I	TCB
17	O	R24	34	I/O	R8/B8	51	I	A4	68	—	V _{DD}

LMU217JC45 (2/2)

