

# Three-Terminal Adjustable Output Positive Voltage Regulator

The LM317L is an adjustable 3-terminal positive voltage regulator capable of supplying in excess of 100 mA over an output voltage range of 1.2 V to 37 V. This voltage regulator is exceptionally easy to use and requires only two external resistors to set the output voltage. Further, it employs internal current limiting, thermal shutdown and safe area compensation, making them essentially blow-out proof.

The LM317L serves a wide variety of applications including local, on card regulation. This device can also be used to make a programmable output regulator, or by connecting a fixed resistor between the adjustment and output, the LM317L can be used as a precision current regulator.

- Output Current in Excess of 100 mA
- Output Adjustable Between 1.2 V and 37 V
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe-Area Compensation
- Floating Operation for High Voltage Applications
- Standard 3-Lead Transistor Package
- Eliminates Stocking Many Fixed Voltages

# \* C<sub>in</sub>\* o.1µF \* C<sub>in</sub> is required if regulator is located an appreciable distance from power supply filter. \*\* C<sub>O</sub> is not needed for stability, however, it does improve transient response. Vout = 1.25 V (1 + \frac{R\_2}{R\_1}) + I\_{Adj} R\_2 Since I\_{Adj} is controlled to less than 100 µA, the error associated with this term is negligible in most applications.

## **LM317L**

# LOW CURRENT THREE-TERMINAL ADJUSTABLE POSITIVE VOLTAGE REGULATOR

SEMICONDUCTOR TECHNICAL DATA

**Z SUFFIX** PLASTIC PACKAGE CASE 29



Pin 1. Adjust 2. V<sub>out</sub> 3. V<sub>in</sub>

**D SUFFIX**PLASTIC PACKAGE
CASE 751
(SOP–8\*)



the standard SO-8 package.

Pin 1. V<sub>in</sub>

2. V<sub>out</sub> 3. V<sub>out</sub>

Adjust
 N.C.

6. V<sub>out</sub> 7. V<sub>out</sub> 8. N.C.

\* SOP-8 is an internally modified SO-8 package. Pins 2, 3, 6 and 7 are electrically common to the die attach flag. This internal lead frame modification decreases package thermal resistance and increases power dissipation capability when appropriately mounted on a printed circuit board. SOP-8 conforms to all external dimensions of

### **ORDERING INFORMATION**

Device	Operating Temperature Range	Package
LM317LD	T <sub>.1</sub> = 0° to +125°C	SOP-8
LM317LZ	11 = 0 10 +123 C	Plastic
LM317LBD	$T_{.1} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	SOP-8
LM317LBZ	1j = -40 t0 +123 C	Plastic

### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Input–Output Voltage Differential	VI-VO	40	Vdc
Power Dissipation	PD	Internally Limited	W
Operating Junction Temperature Range	TJ	-40 to +125	°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C

### **ELECTRICAL CHARACTERISTICS** ( $V_I - V_O = 5.0 \text{ V}$ ; $I_O = 40 \text{ mA}$ ; $T_J = T_{low}$ to $T_{high}$ [Note 1]; $I_{max}$ and $P_{max}$ [Note 2]; unless otherwise noted.)

			LM317L, LB		В	
Characteristics	Figure	Symbol	Min	Тур	Max	Unit
Line Regulation (Note 3) $T_A = 25^{\circ}C, 3.0 \text{ V} \le V_I - V_O \le 40 \text{ V}$	1	Reg <sub>line</sub>	-	0.01	0.04	%/V
Load Regulation (Note 3), $T_A = 25^{\circ}C$ 10 mA $\leq I_O \leq I_{max} - LM317L$ $V_O \leq 5.0 \text{ V}$ $V_O \geq 5.0 \text{ V}$	2	Reg <sub>load</sub>	_ _	5.0 0.1	25 0.5	mV % VO
Adjustment Pin Current	3	l <sub>Adj</sub>	_	50	100	μΑ
Adjustment Pin Current Change $2.5 \text{ V} \le \text{V}_I - \text{V}_O \le 40 \text{ V}, P_D \le P_{max}$ $10 \text{ mA} \le I_O \le I_{max} - \text{LM317L}$	1, 2	Δl <sub>Adj</sub>	-	0.2	5.0	μА
Reference Voltage 3.0 V $\leq$ V <sub>I</sub> $-$ V <sub>O</sub> $\leq$ 40 V, P <sub>D</sub> $\leq$ P <sub>max</sub> 10 mA $\leq$ I <sub>O</sub> $\leq$ I <sub>max</sub> $-$ LM317L	3	V <sub>ref</sub>	1.20	1.25	1.30	V
Line Regulation (Note 3) 3.0 $V \le V_I - V_O \le 40 \text{ V}$	1	Reg <sub>line</sub>	_	0.02	0.07	%/V
Load Regulation (Note 3) 10 mA $\leq$ I <sub>O</sub> $\leq$ I <sub>max</sub> – LM317L V <sub>O</sub> $\leq$ 5.0 V V <sub>O</sub> $\geq$ 5.0 V	2	Reg <sub>load</sub>	_ _	20 0.3	70 1.5	mV % VO
Temperature Stability (T <sub>low</sub> ≤ T <sub>J</sub> ≤ T <sub>high</sub> )	3	TS	_	0.7	_	% Vo
Minimum Load Current to Maintain Regulation (V <sub>I</sub> – V <sub>O</sub> = 40 V)	3	I <sub>Lmin</sub>	_	3.5	10	mA
Maximum Output Current $V_I - V_O \le 6.25 \text{ V}, P_D \le P_{max}, Z \text{ Package}$ $V_I - V_O \le 40 \text{ V}, P_D \le P_{max}, T_A = 25^{\circ}\text{C}, Z \text{ Package}$	3	I <sub>max</sub>	100 -	200 20	- -	mA
RMS Noise, % of $V_O$ $T_A = 25^{\circ}C$ , 10 Hz $\leq$ f $\leq$ 10 kHz		N	-	0.003	-	% Vo
Ripple Rejection (Note 4) $V_O = 1.2 \text{ V}, f = 120 \text{ Hz}$ $C_{Adj} = 10 \mu\text{F}, V_O = 10.0 \text{ V}$	4	RR	60 -	80 80	- -	dB
Long Term Stability, T <sub>J</sub> = T <sub>high</sub> (Note 5) T <sub>A</sub> = 25°C for Endpoint Measurements	3	S	-	0.3	1.0	%/1.0 k Hrs.
Thermal Resistance, Junction–to–Case Z Package		R <sub>O</sub> JC	-	83	-	°C/W
Thermal Resistance, Junction–to–Air Z Package		R <sub>⊖</sub> JA	-	160	-	°C/W

<sup>4.</sup> CAdj. when used, is connected between the adjustment pin and ground.
5. Since Long-Term Stability cannot be measured on each device before shipment, this specification is an engineering estimate of average stability from lot to lot.

### **Representative Schematic Diagram**

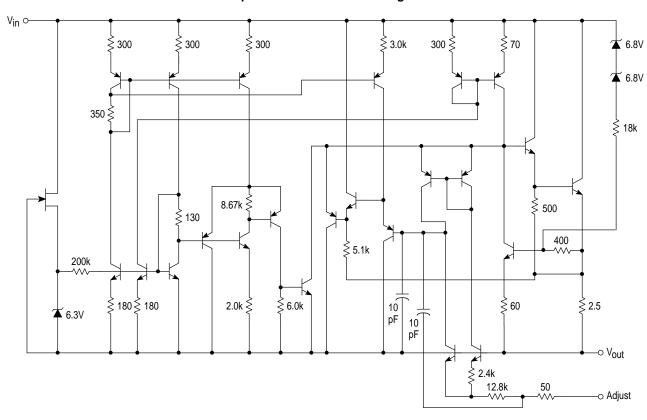


Figure 1. Line Regulation and  $\Delta I_{\mbox{Adj}}/\mbox{Line}$  Test Circuit

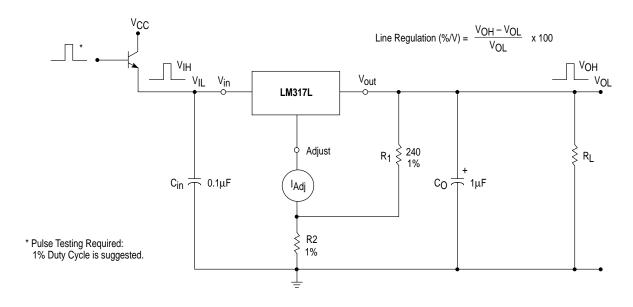


Figure 2. Load Regulation and  $\Delta I_{\mbox{Adj}}/\mbox{Load Test Circuit}$ 

Load Regulation (mV) =  $V_O$  (min Load)  $-V_O$  (max Load) V<sub>O</sub> (min Load) – V<sub>O</sub> (max Load) Load Regulation (% V<sub>O</sub>) = V<sub>O</sub> (min Load) - V<sub>O</sub> (min Load) VO (max Load)  $V_{\text{in}}^*$  $V_{\text{in}}$  $V_{out}$ LM317L ΙL  $\mathsf{R}_\mathsf{L}$ (max Load)  $R_1 \geqslant 240$  $\mathsf{R}_\mathsf{L}$ Adjust (min Load)  $C_{in} \stackrel{+}{\mp} 0.1 \mu F$  $I_{Adj}$ R<sub>2</sub> 1%

\* Pulse Testing Required: 1% Duty Cycle is suggested.

Figure 3. Standard Test Circuit

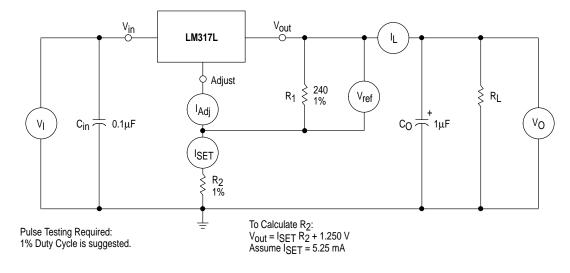


Figure 4. Ripple Rejection Test Circuit

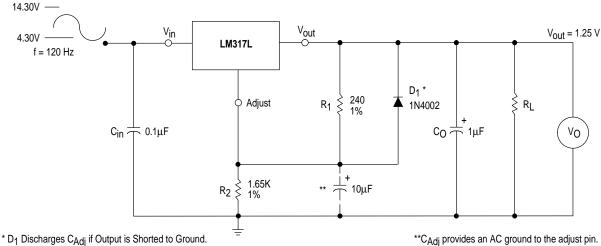
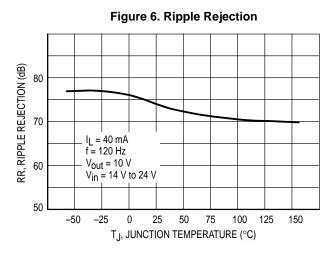
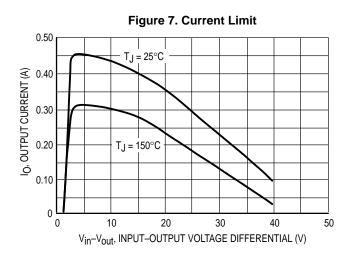
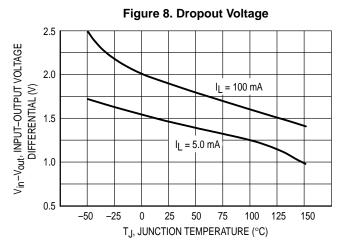
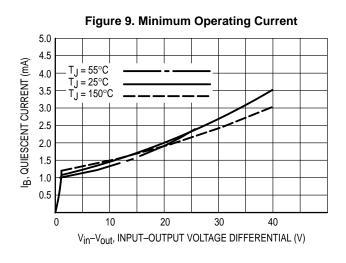


Figure 5. Load Regulation △ Vout, OUTPUT VOLTAGE CHANGE (%) 0.4 V<sub>in</sub> = 45 V  $V_{out} = 5.0 \text{ V}$ 0.2 I<sub>L</sub> = 5.0 mA to 40 mA -0.2V<sub>in</sub> = 10 V -0.4V<sub>out</sub> = 5.0 V I<sub>L</sub> = 5.0 mA to 100 mA -0.6-1.0-50 -25 25 50 75 T<sub>J</sub>, JUNCTION TEMPERATURE (°C)









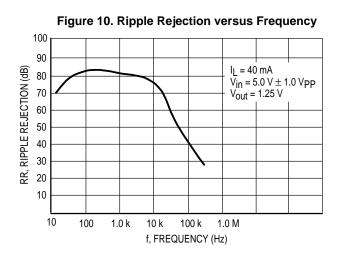
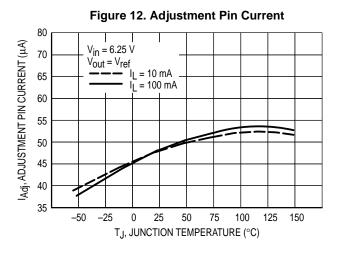
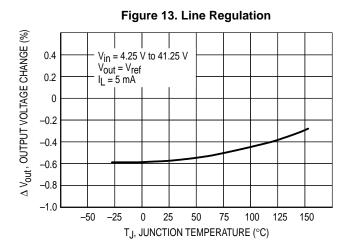
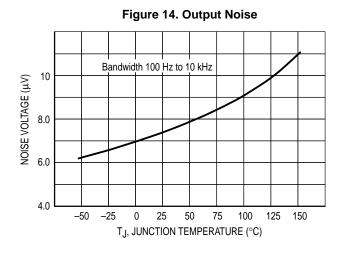
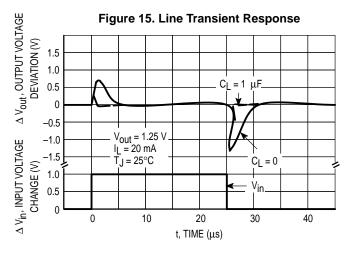


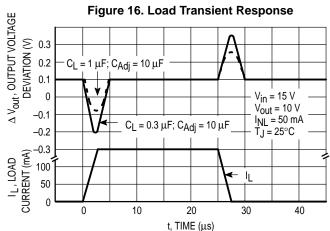
Figure 11. Temperature Stability 1.260 V<sub>ref</sub>, REFERENCE VOLTAGE (V) 1.250 1.240 V<sub>in</sub> = 4.2 V V<sub>out</sub> = V<sub>ref</sub> I<sub>L</sub> = 5.0 mA 1.230 1.220 -50 -25 25 50 75 125 100 150 TJ, JUNCTION TEMPERATURE (°C)











### **APPLICATIONS INFORMATION**

### **Basic Circuit Operation**

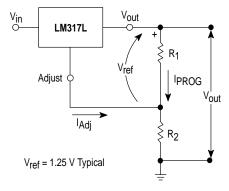
The LM317L is a 3-terminal floating regulator. In operation, the LM317L develops and maintains a nominal 1.25 V reference ( $V_{ref}$ ) between its output and adjustment terminals. This reference voltage is converted to a programming current ( $I_{PROG}$ ) by  $R_1$  (see Figure 13), and this constant current flows through  $R_2$  to ground. The regulated output voltage is given by:

$$V_{out} = V_{ref} (1 + \frac{R_2}{R_1}) + I_{Adj} R_2$$

Since the current from the adjustment terminal ( $I_{Adj}$ ) represents an error term in the equation, the LM317L was designed to control  $I_{Adj}$  to less than 100  $\mu A$  and keep it constant. To do this, all quiescent operating current is returned to the output terminal. This imposes the requirement for a minimum load current. If the load current is less than this minimum, the output voltage will rise.

Since the LM317L is a floating regulator, it is only the voltage differential across the circuit which is important to performance, and operation at high voltages with respect to ground is possible.

Figure 17. Basic Circuit Configuration



### **Load Regulation**

The LM317L is capable of providing extremely good load regulation, but a few precautions are needed to obtain maximum performance. For best performance, the programming resistor (R1) should be connected as close to the regulator as possible to minimize line drops which effectively appear in series with the reference, thereby degrading regulation. The ground end of R2 can be returned near the load ground to provide remote ground sensing and improve load regulation.

### **External Capacitors**

A 0.1  $\mu$ F disc or 1.0  $\mu$ F tantalum input bypass capacitor (C<sub>in</sub>) is recommended to reduce the sensitivity to input line impedance.

The adjustment terminal may be bypassed to ground to improve ripple rejection. This capacitor ( $C_{Adj}$ ) prevents ripple from being amplified as the output voltage is increased. A 10  $\mu F$  capacitor should improve ripple rejection about 15 dB at 120 Hz in a 10 V application.

Although the LM317L is stable with no output capacitance, like any feedback circuit, certain values of external capacitance can cause excessive ringing. An output capacitance (CO) in the form of a 1.0  $\mu F$  tantalum or 25  $\mu F$  aluminum electrolytic capacitor on the output swamps this effect and insures stability.

### **Protection Diodes**

When external capacitors are used with any IC regulator it is sometimes necessary to add protection diodes to prevent the capacitors from discharging through low current points into the regulator.

Figure 14 shows the LM317L with the recommended protection diodes for output voltages in excess of 25 V or high capacitance values ( $C_O > 10~\mu\text{F},~C_{Adj} > 5.0~\mu\text{F}$ ). Diode  $D_1$  prevents  $C_O$  from discharging thru the IC during an input short circuit. Diode  $D_2$  protects against capacitor  $C_{Adj}$  discharging through the IC during an output short circuit. The combination of diodes  $D_1$  and  $D_2$  prevents  $C_{Adj}$  from discharging through the IC during an input short circuit.

Figure 18. Voltage Regulator with Protection Diodes

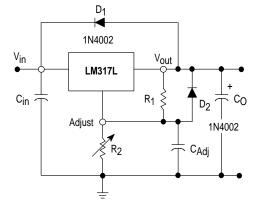


Figure 19. Adjustable Current Limiter

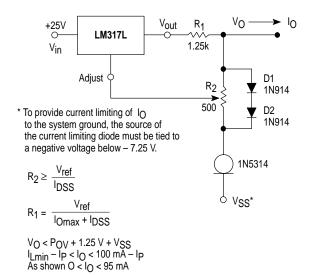
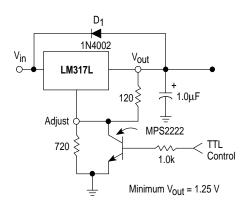


Figure 20. 5 V Electronic Shutdown Regulator



D<sub>1</sub> protects the device during an input short circuit.

Figure 21. Slow Turn-On Regulator

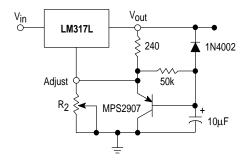
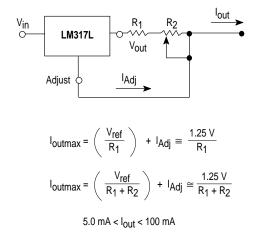
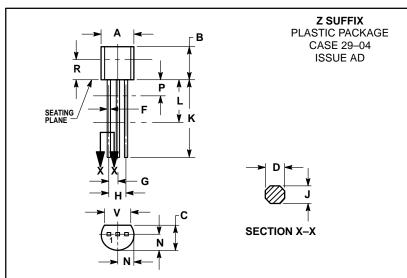


Figure 22. Current Regulator



### **OUTLINE DIMENSIONS**



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- IS UNCONTROLLED.

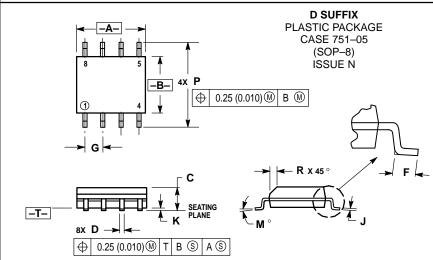
  DIMENSION F APPLIES BETWEEN P AND L.

  DIMENSION D AND J APPLY BETWEEN L AND K

  MINIMUM. LEAD DIMENSION IS UNCONTROLLED

  IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		INCHES MILLIMETER	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
۲	0.015	0.020	0.39	0.50
K	0.500		12.70	_
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	



- (VIES:

  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

  2. CONTROLLING DIMENSION: MILLIMETER.

  3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.

  4. MAXIMUM MOLD PROTRUSION 0.15 (0.006)

- 4. MAXIMUM MOLLD PRO I RUSIUM U. 13 (0.000)
  PER SIDE.
  5. DIMENSION D DOES NOT INCLUDE DAMBAR
  PROTRUSION. ALLOWABLE DAMBAR
  PROTRUSION SHALL BE 0.127 (0.005) TOTAL
  IN EXCESS OF THE D DIMENSION AT
  MAXIMUM MATERIAL CONDITION.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	4.80	5.00	0.189	0.196	
В	3.80	4.00	0.150	0.157	
С	1.35	1.75	0.054	0.068	
D	0.35	0.49	0.014	0.019	
F	0.40	1.25	0.016	0.049	
G	1.27 BSC		0.050 BSC		
J	0.18	0.25	0.007	0.009	
K	0.10	0.25	0.004	0.009	
М	0 °	7 °	0 °	7 °	
Р	5.80	6.20	0.229	0.244	
R	0.25	0.50	0.010	0.019	

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