

Monolithic Linear IC

No. 5162A

LA6517, 6517M, 6518M**SANYO****2-Output Power Operational Amplifier**

Applications

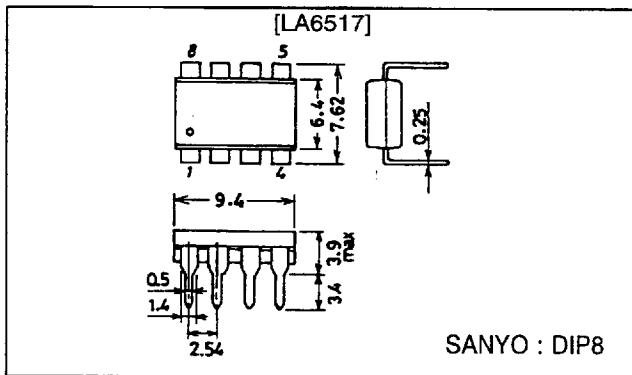
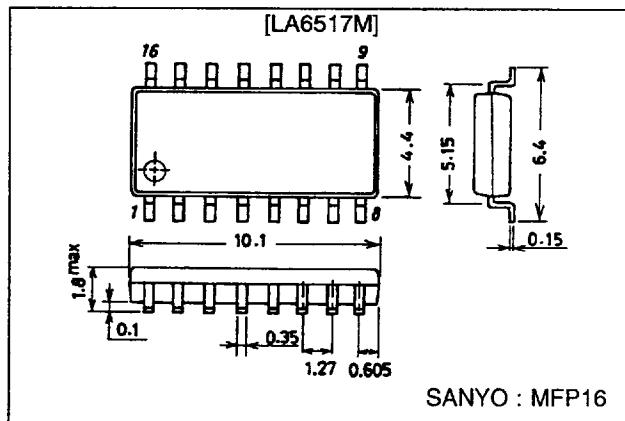
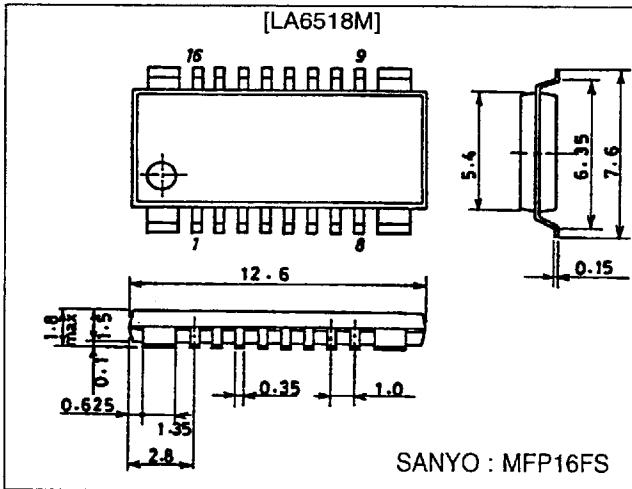
The LA6517, LA6517M, and LA6518M are 2-output power operational amplifiers developed for use in consumer and industrial equipment.

Features and Functions

- High output current (I_O max = 0.5 A).
- High gain.
- Includes a current limiter.
- Wide operating voltage range (± 2 to ± 18 V).
- Single-supply operation possible (4 to 36 V).
- Thermal shutdown built in.

Package Dimensions

unit : mm

3001-DIP8**3035A-MFP16****3097-MFP16FS**

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Specifications

Maximum Ratings at Ta = 25 °C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC/V_{EE}}		±18	V
Differential input voltage	V _{ID}		30	V
Common-mode input voltage	V _{IN}		±15	V
Allowable power dissipation	P _{d max}	LA6517	1000	mW
		LA6517M	350	mW
		LA6518M	700	mW
Operating temperature	T _{opr}		-20 to +75	°C
Storage temperature	T _{stg}		-55 to +150	°C

Operating Conditions at Ta = 25 °C

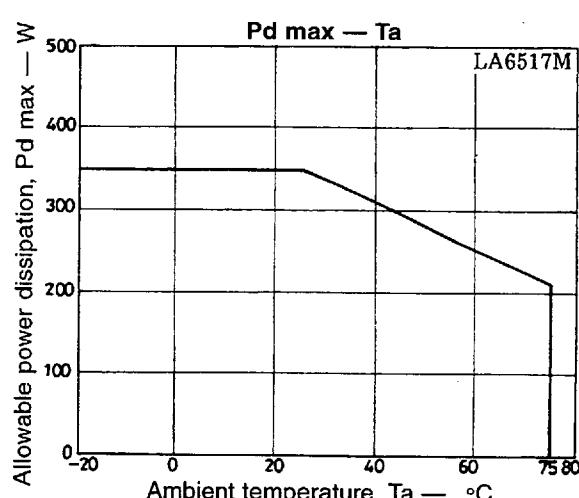
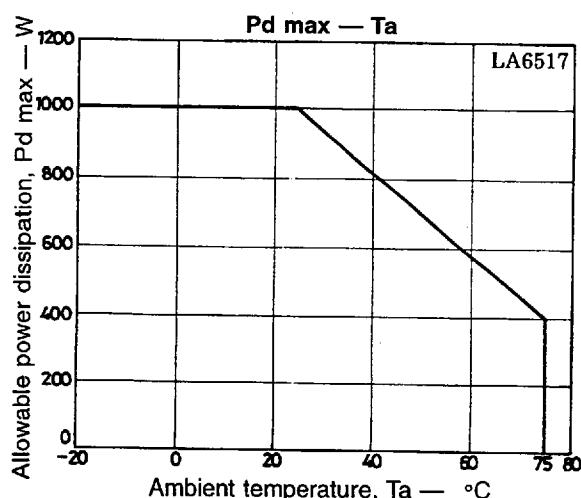
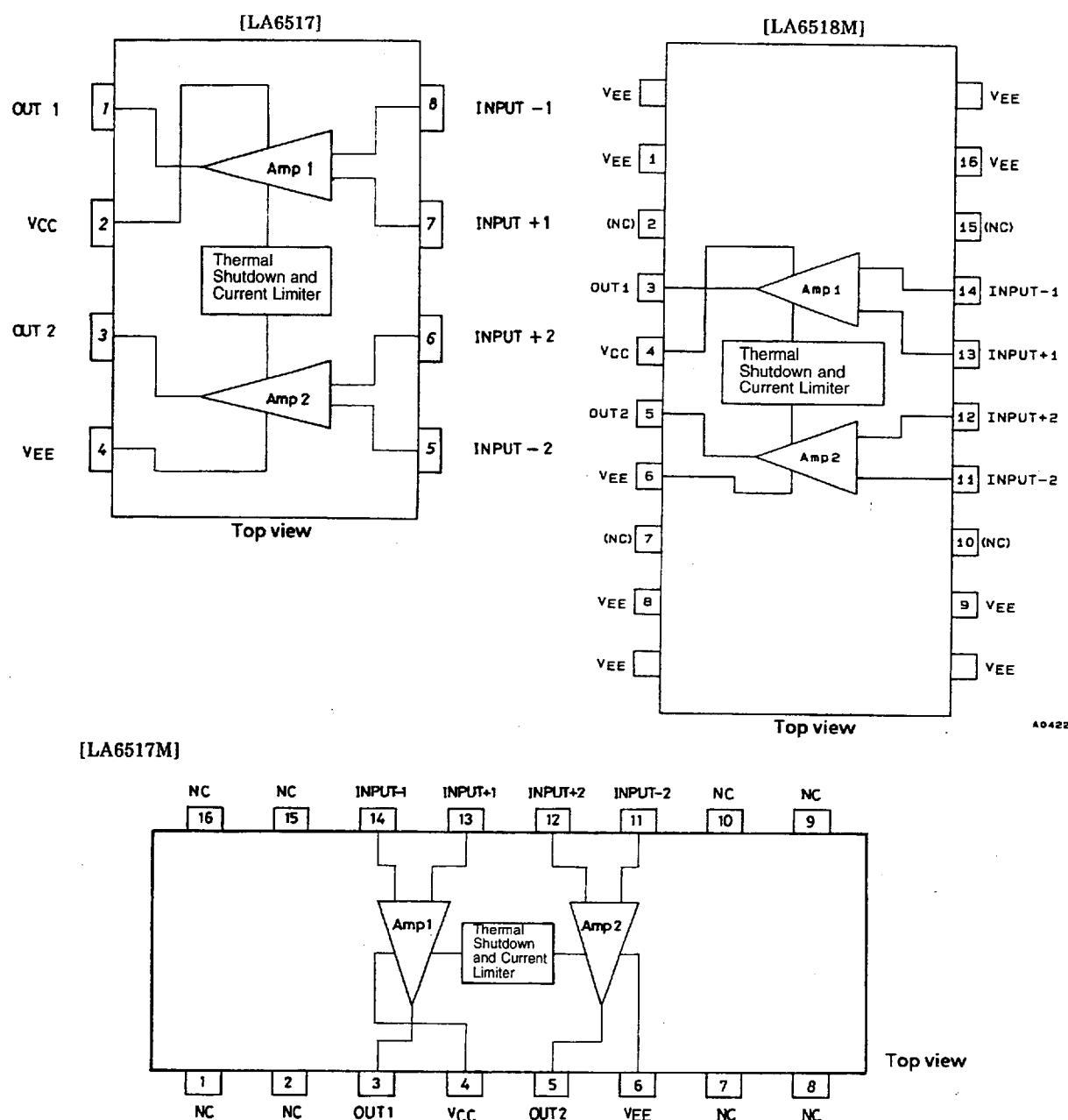
Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V _{CC/V_{EE}}		±2 to ±16	V

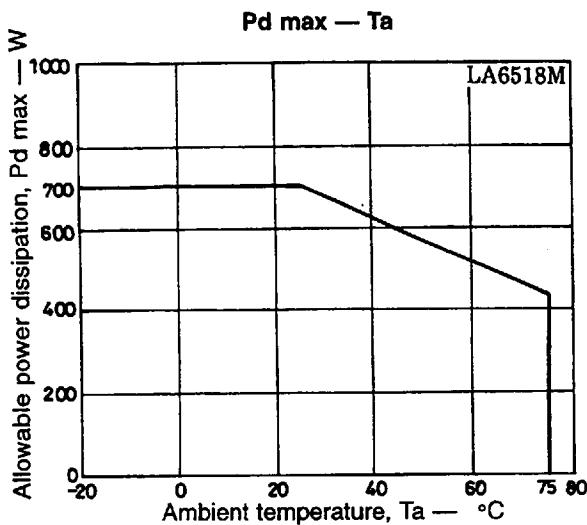
Electrical Characteristics at Ta = 25 °C, V_{CC/V_{EE}} = ±15 V

Parameter	Symbol	Conditions	min	typ	max	Unit
No-load current drain	I _{CC}			8	20	mA
Input offset voltage	V _{IO}	R _S ≤ 10 kΩ		2	7	mV
Input offset current	I _{IO}			10	100	nA
Input bias current	I _B			100	300	nA
Common-mode input voltage range	V _{ICM}	LA6517, 6517M	-15		+13	V
		LA6518M	-14		+13	V
Common-mode signal rejection ratio	CMRR		65	80		dB
Maximum output voltage	V _O	R _L = 33 Ω	±11	±12		V
Voltage gain	V _{GO}			85		dB
Slew rate	SR	G _V = 0, R _L = 33 Ω, R = 10 Ω, L = 0.1 μF		0.15		V/μs
Supply voltage rejection ratio	SVR			30	300	μV/V
Limiting current (built in)	I _{SC}			0.5		A

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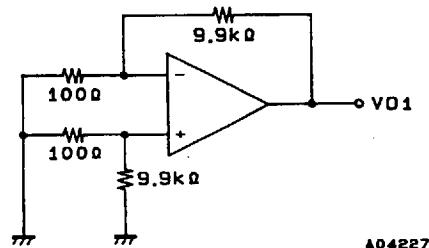
Block Diagram and Pin Assignments





Test Circuits

1. V_{IO}, SVRR



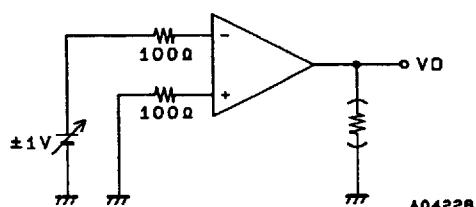
V_{IO}: V_{CC}/V_{EE} = ±15V

SVRR [$\frac{V_{CC} = 15V, 5V}{V_{EE} = -5V, -15V}$]

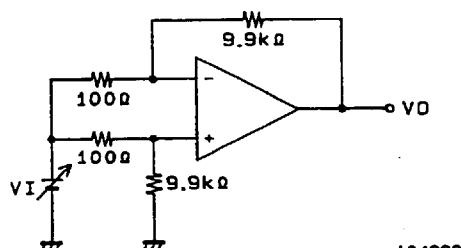
$$V_{IO} = V_{O1}/100$$

$$SVR(+)=\left| \frac{\Delta V_{O1}}{100 \times 10V} \right|$$

2. V_O



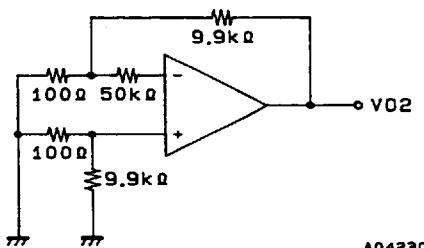
3. CMRR, V_{ICM}



CMRR: V_I = ±7.5V

$$CMR = 20 \log \frac{15 \times 100}{|\Delta V_O|}$$

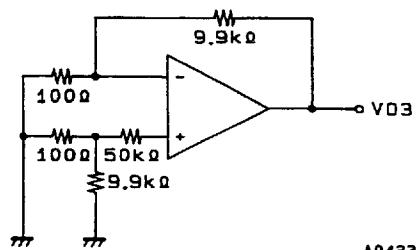
4. I_B(-)



$$I_B(-) = \frac{|V_{O2} - V_{O1}|}{50k\Omega \times 100}$$

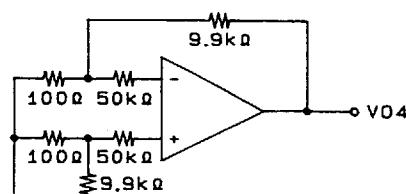
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5. $I_B(+)$



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6. I_{IO}

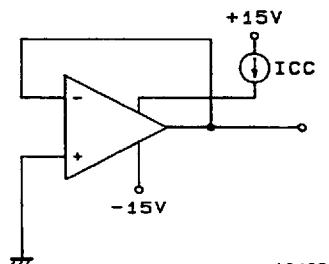


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$$I_B(+) = \frac{|V_{O3} - V_O1|}{50k\Omega \times 100}$$

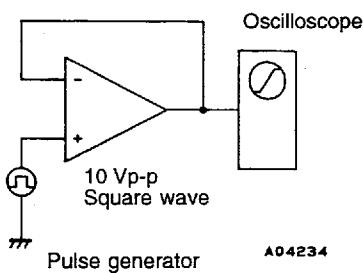
$$I_{IO} = \frac{|V_{O4} - V_O1|}{50k\Omega \times 100}$$

7. I_{CC}



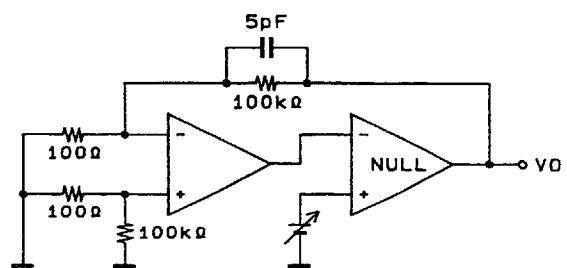
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8. SR



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9. V_{GO}



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$$V_{GO} = 20 \log \frac{1000 \times 20}{\Delta V_O}$$

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