



Dual Operational Amplifier

Preliminary

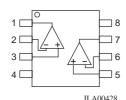
Overview

LA6210M is a low supply voltage and low saturation output voltage ($\pm 2.0 \text{Vp-p}$ at supply voltage $\pm 2.5 \text{V}$) operational amplifier. It is applicable to handy type CD, radio cassette CD, and portable DAT, that are digital audio apparatus which require the 5V single supply operation and high output voltage.

Features

- · Single supply operation.
- Operating voltage. $(\pm 1.0 \text{V to } \pm 3.5 \text{V})$
- · Low saturation output voltage.
- High slew rate. $(4.5V / \mu s \text{ typ.})$
- Package outline. MFP8
- · Bipolar technology.

Pin Configuration



Pin function

- 1. A OUTPUT 2. A –INPUT 3. A +INPUT
- 4. V-
- 5. B +INPUT 6. B -INPUT
- 6. B INPUT 7. B OUTPUT
- 7. B UU

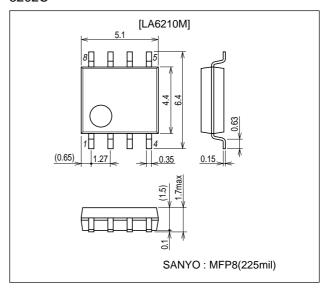
Specifications

Absolute Maximum Ratings at Ta = 25 °C

Parameter	Symbol	Ratings	Unit	
Supply voltage	V+/V-	±3.5	V	
Differential input voltage	VID	±7	V	
Power dissipation	PD	300	mW	
Operating temperature range	Topr	-40 to +85	°C	
Storage temperature range	Tstg	-40 to +150	°C	

Package Dimensions

unit : mm 3202C



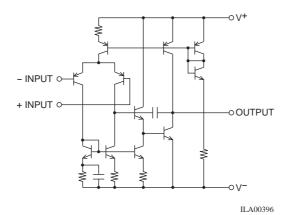
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Electrical Characteristics at Ta = 25 °C, $V^+/V^- = \pm 2.5V$

Parameter Symbol	Symbol	Conditions	Ratings			Unit
	Conditions	min	typ	max	Offic	
Input offset voltage	VIO	R _S ≤10kΩ	-	0.3	6	mV
Input offset current	lio		-	1.5	200	nA
Input bias current	IB		-	75	300	nA
Large signal voltage gain	Ay	R _L ≥10kΩ	60	90	1	dB
Maximum output voltage swing	Vом	R _L ≥2.5kΩ	±2	±2.2	1	V
Input common mode voltage range	VICM		±1.5	-	1	V
Common mode rejection ratio	CMR		60	80	1	dB
Supply voltage rejection ratio	SVR(+)		60	92	ı	dB
	SVR(-)		60	72	ı	dB
Operating current	Icc	V _{IN} =0, R _L =∞	-	3.4	5	mA
Slew rate	SR	A _V =1, V _{IN} =±1V	-	4.5	ı	V/μS
Gain-bandwidth product	GB		-	12	1	MHz

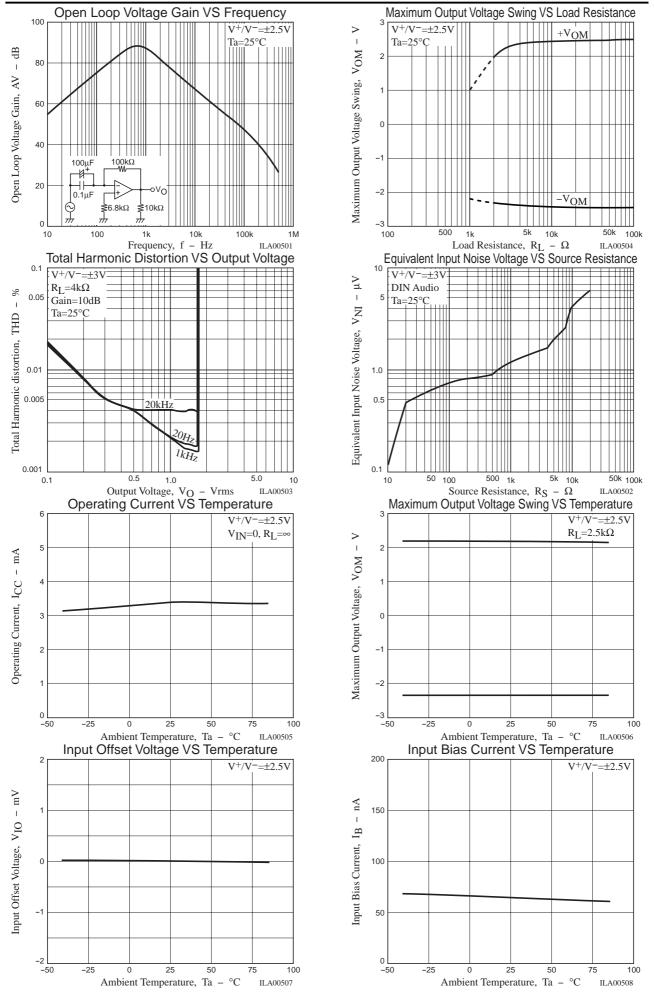
⁽Note 1) Applied circuit voltage gain is desired to be operated within the range of 3 dB to 30 dB.

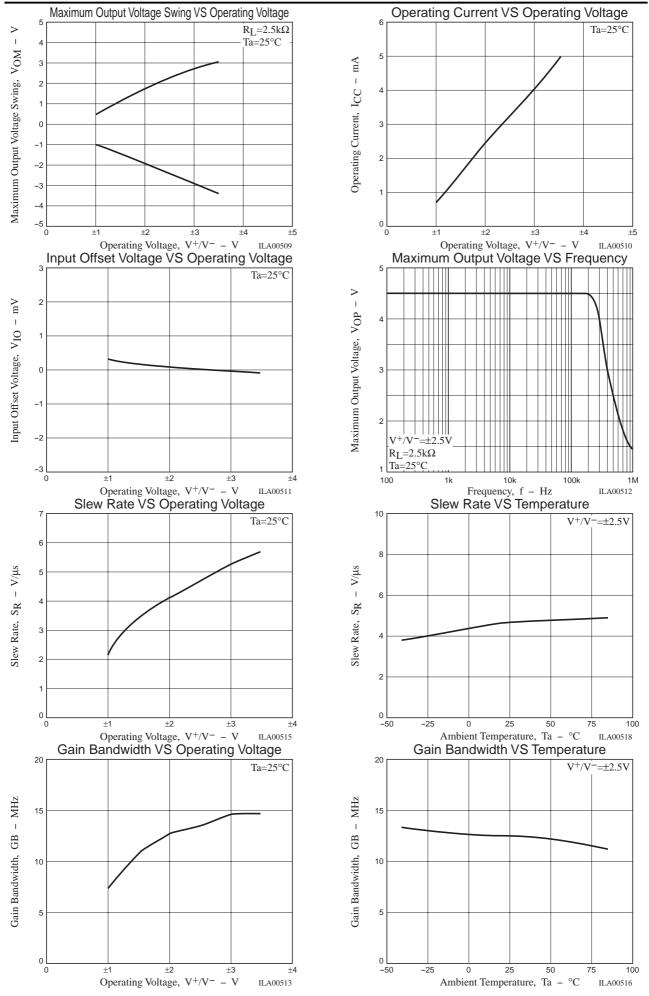
Equivalent Circuit

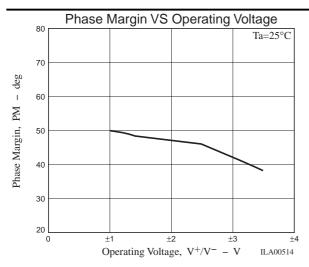


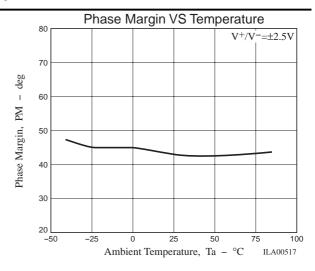
⁽Note 2) Special care being required for input common mode voltage range and the oscillation due to the capacitive load when operating on voltage follower.

⁽Note 3) Special care being required for the oscillation, yet having the gain when the supply voltage is applied at more than ±2.5V (single supply voltage 5V).



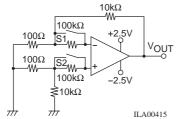






Test Circuits(V±=2.5V, Ta=25°C, TYP) :

Input Offset Voltage / Input Offset Current / Input Bias Current

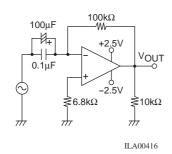


$$V_{IO} = \frac{V_{O}1}{Gain}$$

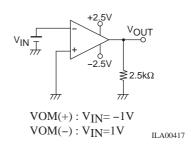
$$I_{IO} = \frac{\left|V_{O}2 - V_{O}1\right|}{100k\Omega XGain}$$

$$I_{B} = \frac{|VO3 - VO4|}{2 \times 100 \text{k} \Omega \times \text{Gain}}$$

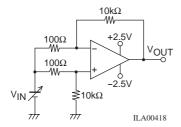
Large Signal Voltage Gain



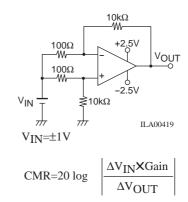
Maximum Output Voltage Swing



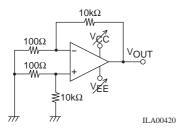
Input Common Mode Voltage Range



Common Mode Rejection Ratio



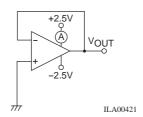
Supply Voltage Rejection Ratio



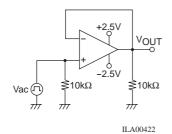
 $\begin{aligned} &SVR(+): V_{CC} = 1.25V, \, V_{EE} = -2.5V \\ &SVR(-): V_{CC} = 2.5V, \, V_{EE} = -1.25V \end{aligned}$

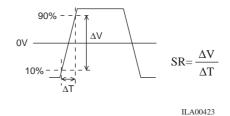
$$SVR=20 \log \quad \left| \frac{Gain X \Delta V_{SUP}}{\Delta V_{OUT}} \right|$$

Operating Current

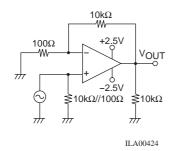


Slew Rate

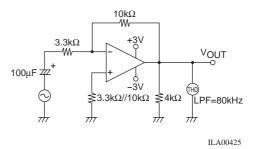




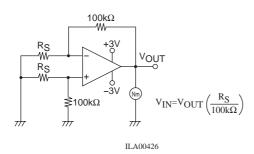
Gain Bandwidth Product



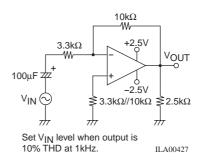
Total Harmonic Distortion



Equivalent Input Noise Voltage



Maximum Output Voltage vs. Frequency



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