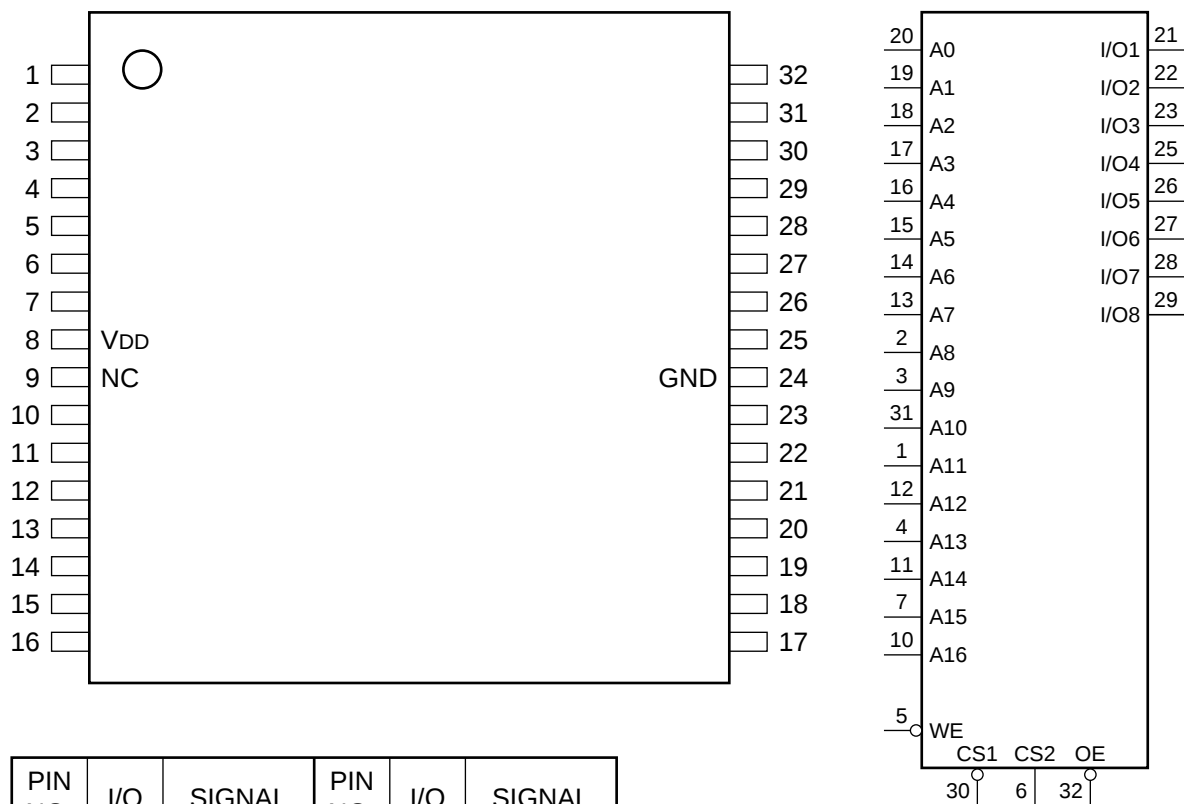


C-MOS 128 K (131,072 × 8)-BIT SRAM

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	A11	17	I	A3
2	I	A8	18	I	A2
3	I	A9	19	I	A1
4	I	A13	20	I	A0
5	I	WE	21	I/O	I/O1
6	I	CS2	22	I/O	I/O2
7	I	A15	23	I/O	I/O3
8	—	VDD	24	—	GND
9	—	NC	25	I/O	I/O4
10	I	A16	26	I/O	I/O5
11	I	A14	27	I/O	I/O6
12	I	A12	28	I/O	I/O7
13	I	A7	29	I/O	I/O8
14	I	A6	30	I	CS1
15	I	A5	31	I	A10
16	I	A4	32	I	OE

