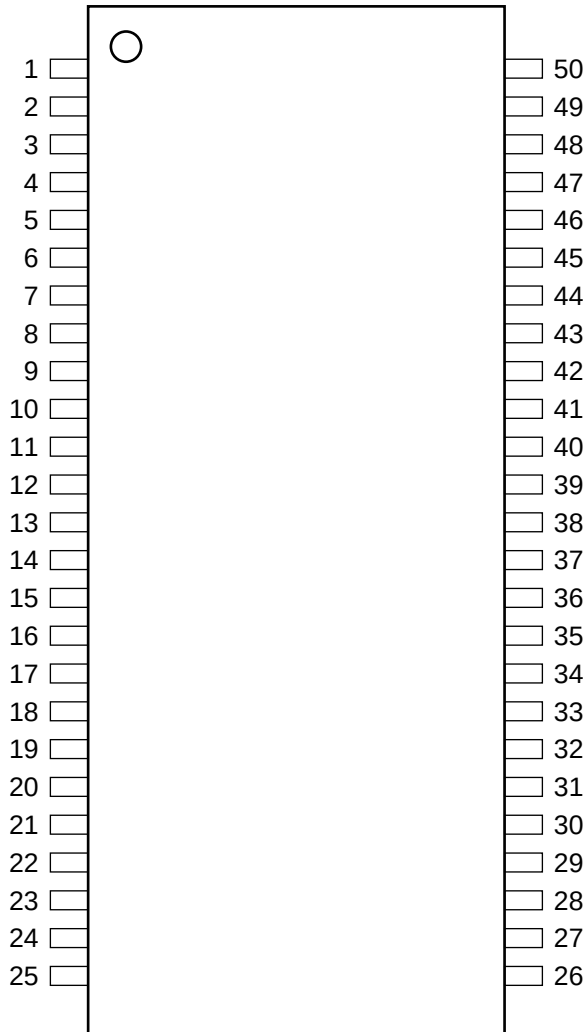


64 M (4,194,304 × 16)-BIT DRAM WITH EXTENDED DATA OUT

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	V _{CC}	26	—	GND
2	I/O	DQ0	27	I	A6
3	I/O	DQ1	28	I	A7
4	I/O	DQ2	29	I	A8
5	I/O	DQ3	30	I	A9
6	—	V _{CC}	31	I	A10
7	I/O	DQ4	32	I	A11
8	I/O	DQ5	33	I	A12 (NC)*
9	I/O	DQ6	34	—	NC
10	I/O	DQ7	35	—	NC
11	—	NC	36	O	$\overline{\text{OE}}$
12	—	V _{CC}	37	I	$\overline{\text{UCAS}}$
13	I	$\overline{\text{W}}$	38	I	$\overline{\text{LCAS}}$
14	I	$\overline{\text{RAS}}$	39	—	GND
15	—	NC	40	—	NC
16	—	NC	41	I/O	DQ8
17	—	NC	42	I/O	DQ9
18	—	NC	43	I/O	DQ10
19	I	A0	44	I/O	DQ11
20	I	A1	45	—	GND
21	I	A2	46	I/O	DQ12
22	I	A3	47	I/O	DQ13
23	I	A4	48	I/O	DQ14
24	I	A5	49	I/O	DQ15
25	—	V _{CC}	50	—	GND

* (NC) : NC FOR 4 K PRODUCT

INPUTS

A0 - A12 : ADDRESS (8 K PRODUCT: KM416V4004B)

A0 - A11 : ADDRESS (4 K PRODUCT: KM416V4104B)

 $\overline{\text{LCAS}}$: LOWER COLUMN ADDRESS STROBE $\overline{\text{RAS}}$: ROW ADDRESS STROBE $\overline{\text{UCAS}}$: UPPER COLUMN ADDRESS STROBE $\overline{\text{W}}$: READ/WRITE**OUTPUT** $\overline{\text{OE}}$: DATA ENABLE**INPUTS/OUTPUTS**

DQ0 - DQ15 : DATA