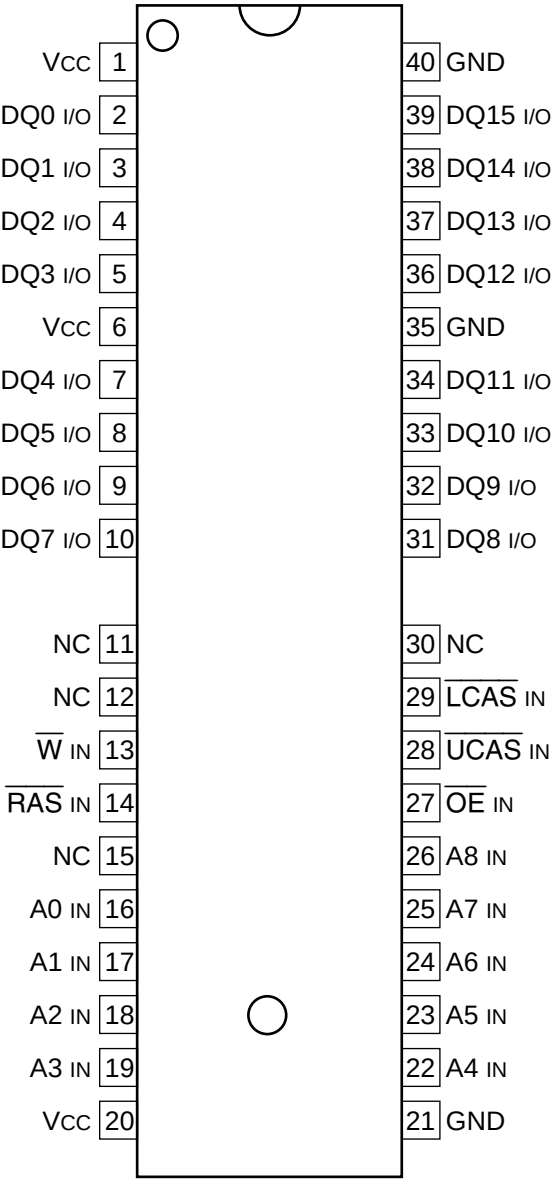


4 M (256 K × 16)-BIT DRAM WITH EXTENDED DATA OUT

—TOP VIEW—



- INPUTS**
A0 - A8 : ADDRESS
LCAS : LOWER COLUMN ADDRESS STROBE
OE : DATA OUTPUT ENABLE
RAS : ROW ADDRESS STROBE
UCAS : UPPER COLUMN ADDRESS STROBE
W : READ/WRITE
- INPUTS/OUTPUTS**
DQ0 - DQ15 : DATA
- OTHER**
NC : NO CONNECTION

