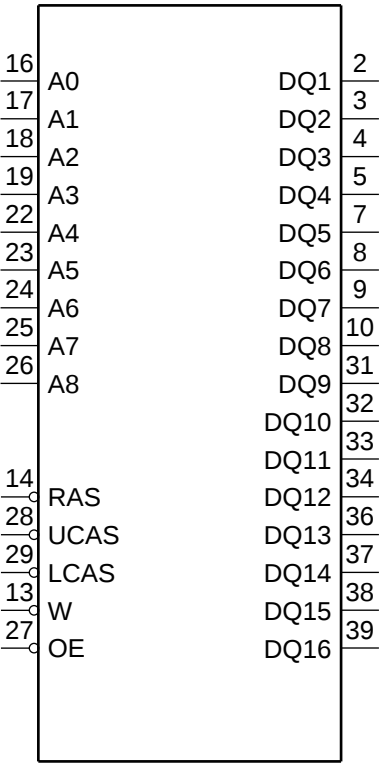
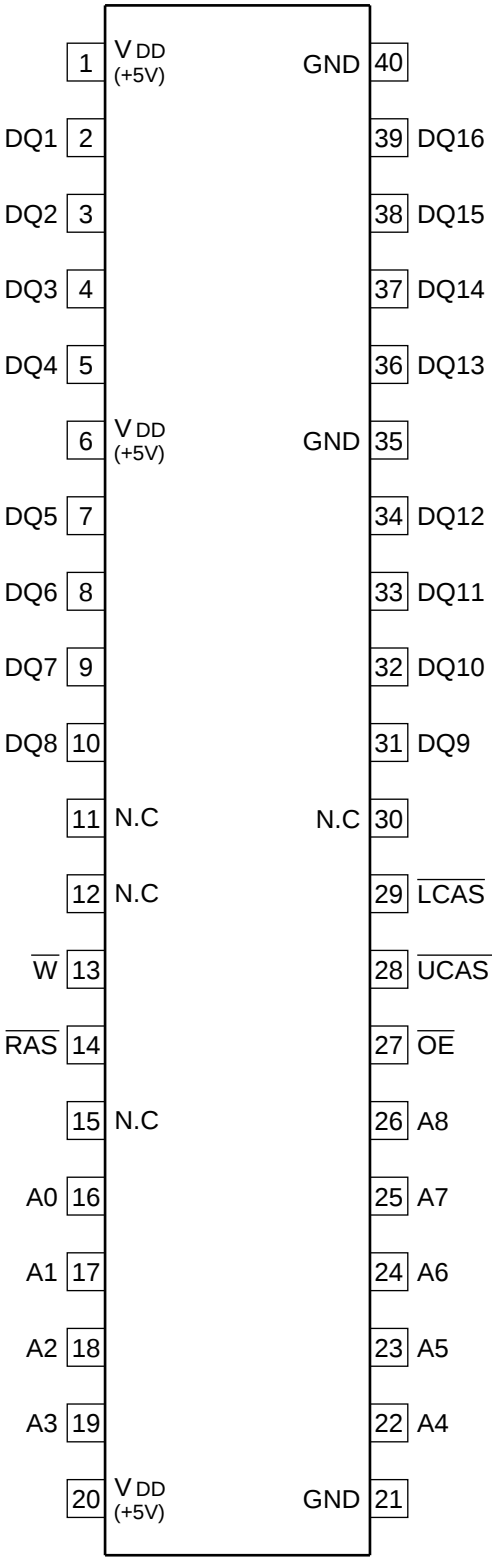

C-MOS 4M(256K x 16)-BIT DYNAMIC RAM WITH EXTENDED DATA OUT
- TOP VIEW -



INPUT
A0-A8 ; ADDRESS
 $\overline{RAS}$; ROW ADDRESS STROBE
 $\overline{UCAB}$; UPPER COLUMN ADDRESS STROBE
 $\overline{LCAS}$; LOWER COLUMN ADDRESS STROBE
 $\overline{W}$; READ/WRITE
 $\overline{OE}$; DATA OUTPUT ENABLE

INPUT/OUTPUT
DQ0-DQ16 ; DATA

