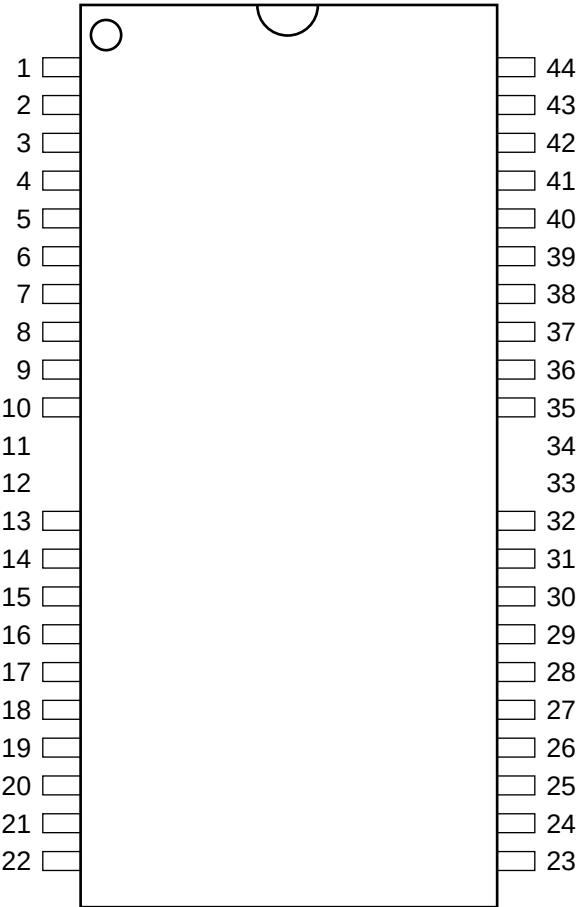


8 M × 8-BIT NAND FLASH MEMORY
—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	—	GND	23	—	Q.Vcc
2	I	CLE	24	I/O	I/O4
3	I	ALE	25	I/O	I/O5
4	I	$\overline{\text{WE}}$	26	I/O	I/O6
5	I	$\overline{\text{WP}}$	27	I/O	I/O7
6	—	NC	28	—	NC
7	—	NC	29	—	NC
8	—	NC	30	—	NC
9	—	NC	31	—	NC
10	—	NC	32	—	NC
11			33		
12			34		
13	—	NC	35	—	NC
14	—	NC	36	—	NC
15	—	NC	37	—	NC
16	—	NC	38	—	NC
17	—	NC	39	—	NC
18	I/O	I/O0	40	I	$\overline{\text{SE}}$
19	I/O	I/O1	41	O	$\overline{\text{R/B}}$
20	I/O	I/O2	42	I	$\overline{\text{RE}}$
21	I/O	I/O3	43	I	$\overline{\text{CE}}$
22	—	GND	44	—	VDD

INPUTS

ALE : ADDRESS LATCH ENABLE
 $\overline{\text{CE}}$: CHIP ENABLE
 $\overline{\text{WE}}$: WRITE ENABLE
CLE : COMMAND LATCH ENABLE
 $\overline{\text{RE}}$: READ ENABLE
 $\overline{\text{SE}}$: SPARE AREA ENABLE
 $\overline{\text{WP}}$: WRITE PROTECT

OUTPUT

$\overline{\text{R/B}}$: READY/BUSY

INPUTS/OUTPUT

I/O0 - I/O7 : DATA

OTHER

NC : NO CONNECTION

