



ChipCorder
TECHNOLOGY BY ISD

ISD1510

Single-Chip, Single-Message Voice Record/Playback Device 8- to 16-Second Durations

GENERAL DESCRIPTION

Information Storage Devices' ISD1510 ChipCorder® provides high-quality, single-chip, single-message, record/playback solutions. The CMOS devices include an on-chip oscillator (with external control), microphone preamplifier, automatic gain control, anti-aliasing filter, multilevel storage array, smoothing filter, and speaker amplifier. A minimum record/playback subsystem can be configured with a microphone, a speaker, several passive components, two push-buttons, and a power source.

Recordings are stored in on-chip nonvolatile memory cells, providing zero-power message storage. This unique, single-chip solution is made possible through ISD's patented multilevel storage technology. Voice and audio signals are stored directly into memory in their natural form, providing high-quality, solid-state voice reproduction.

FEATURES

- Easy-to-use single-chip, single-message voice Record/Playback solution
- High-quality, natural voice/audio reproduction
- Push-button interface
 - Playback can be edge- or level-activated
- Variable Record/Playback duration controlled by external resistor selection which sets sample rate.

8 KHz sample rate	= 8 seconds
6.4 KHz sample rate	= 10 seconds
5.3 KHz sample rate	= 12 seconds
4.0 KHz sample rate	= 16 seconds
- Automatic power-down mode
 - Enters standby mode immediately following a Record or Playback cycle
 - 0.5 μ A standby current (typical)

NOTE: An asterisk (*) indicates that a resistor value of 100K Ω at the ROSC pin provides this specified duration. Only this duration is guaranteed and tested.

Part Number	Minimum Duration* (Seconds)	Maximum Input Sample Rate (KHz)*	Typical Filter Upper Pass Band (KHz)*
ISD1510	10	6.4	2.6

- Zero-power message storage
 - Eliminates battery backup circuits
- 100-year message retention (typical)
- 10 K record cycles (typical)
- On-chip oscillator
- No algorithm development required
- Single +5 volt power supply
- Available in wafer form, die form, and 28 pin 600 mil DIP, 28 pin 300 mil SOIC

ISD1510 BLOCK DIAGRAM

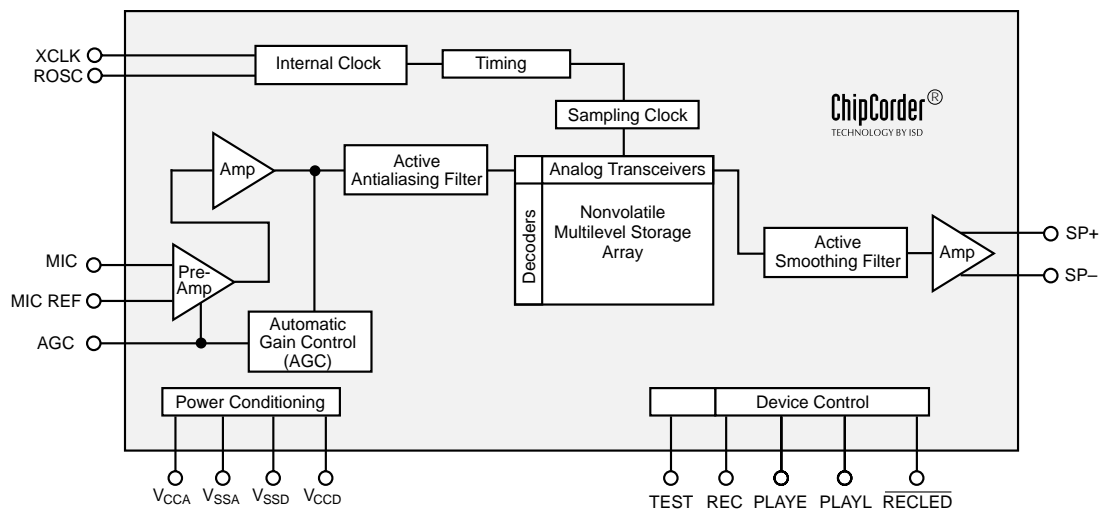


Table of Contents

ISD1510 BLOCK DIAGRAM	1
ISD1510 PINOUTS	1
DETAILED DESCRIPTION	1
Speech/Sound Quality	1
Duration	1
Table 1.1 ISD1510 PIN/PAD Designations with Respect to DIE Center	2
EEPROM Storage	3
Basic Operation	3
Automatic Power-Down Mode	3
PIN DESCRIPTIONS	3
Voltage Supplies (V_{CCA} , V_{CCD})	3
Ground Supplies (V_{SSA} , V_{SSD})	3
Record (REC)	4
Playback, Edge-Activated (PLAYE)	4
Playback, Level-Activated (PLAYL)	4
Record LED Output (RECLD)	4
Microphone Input (MIC)	4
Microphone Reference (MIC REF)	4
Automatic Gain Control (AGC)	4
Speaker Outputs (SP+, SP-)	5
External Clock (XCLK)	5
Test (TEST)	5
Resistor Controlled Oscillator (ROSC)	5
SPECIFICATIONS	5
Table 1.2 Absolute Maximum Ratings (Packaged Parts)	5
Table 1.3 Operating Conditions (Packaged Parts)	5
Table 1.4 DC Parameters (Packaged Parts)	6
Table 1.5 AC Parameters (Packaged Parts)	7
Table 1.6 Absolute Maximum Ratings (Die)	7
Table 1.7 Operating Conditions (Die)	7
Table 1.8 DC Parameters (Die)	8
Table 1.9 AC Parameters (Die)	9
APPLICATION EXAMPLE	10
FUNCTIONAL DESCRIPTION EXAMPLE	10
TIMING DIAGRAMS	12
ORDERING INFORMATION	15

ISD1510 BLOCK DIAGRAM

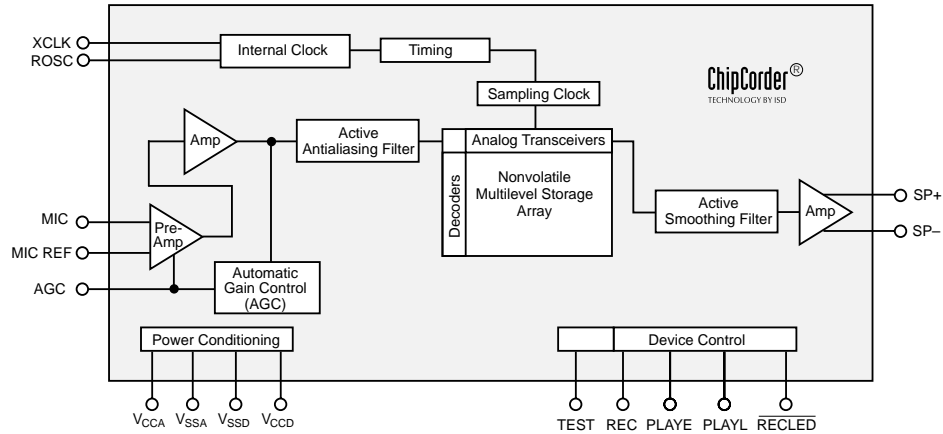
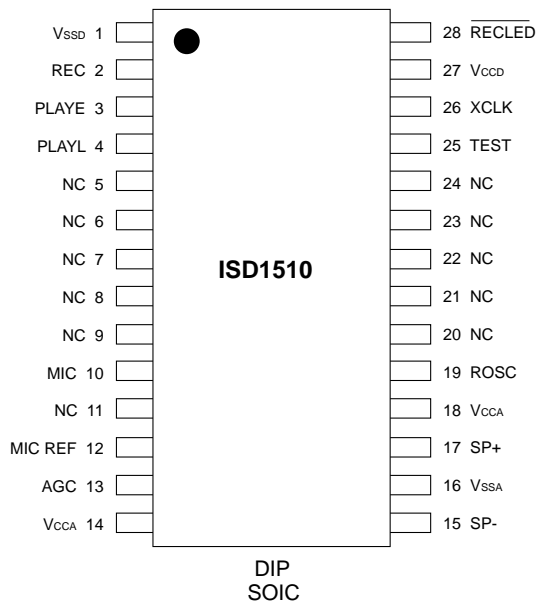


Figure 1-1. ISD1510 Block Diagram—Basic Configuration

ISD1510 PINOUTS



DETAILED DESCRIPTION

Speech/Sound Quality

ISD's patented ChipCorder technology provides natural Record and Playback. The input voice signals are stored directly in nonvolatile EEPROM cells and are reproduced without the synthetic effect often heard with digital solid-state speech solutions. A complete sample is stored in a single cell, minimizing the memory necessary to store a single message.

Duration

The ISD1510 devices offer single-chip solutions with 8 to 16 seconds of Record/Playback duration capacity. Sampling rate and duration are determined by an external resistor connected to the ROSC pin. These specifications apply with the required resistor value for 10-second minimum playback duration.

NOTE: Only the 10 second duration is guaranteed and tested.

Table 1-1. ISD1510 PIN/PAD Designations with Respect to DIE Center

PIN	PIN Name	X Axis (um)	Y Axis (um)	X Axis (mil)	Y Axis (mil)
1	VSSD	-342.40	1462.80	-13.48	57.59
2	REC	-579.80	1477.80	-22.83	58.18
3	PE	-1053.40	1477.80	-41.47	58.18
4	PL	-1201.80	1138.00	-47.31	44.80
5	N/C				
6	N/C				
7	N/C				
8	N/C				
9	N/C				
10	MIC	-1197.60	-1212.20	-47.15	-47.72
11	N/C				
12	MIC REF	-1190.40	-1473.00	-46.87	-57.99
13	AGC	-953.00	-1443.00	-37.52	-56.81
14	VCCA	-247.40	-1459.40	-9.74	-57.46
15	SP-	72.40	-1459.40	2.85	-57.46
16	VSSA	392.20	-1459.40	15.44	-57.46
17	SP+	712.00	-1459.40	28.03	-57.46
18	VCCA	1031.80	-1459.40	40.62	-57.46
19	ROSC	1198.00	-1207.20	47.17	-47.53
20	N/C				
21	N/C				
22	N/C				
23	N/C				
24	N/C				
25	TEST	1196.60	1293.35	47.11	50.92
26	XCLK	752.20	1474.20	29.61	58.04
27	VCCD	563.80	1474.20	22.20	58.04
28	RECLED	150.80	1462.80	5.94	57.59

ISD1510

I. Die Dimensions

X: 2710 ± 25.4 microns
 106.7 ± 1 mil
 Y: 3260 ± 25.4 microns
 128.3 ± 1 mil

II. Die Thickness*

16 ± 1 mil (typ)

III. PAD Opening

100 microns
 3.9 mils

**Die thickness is subject to change, please contact ISD factory for status and availability.*

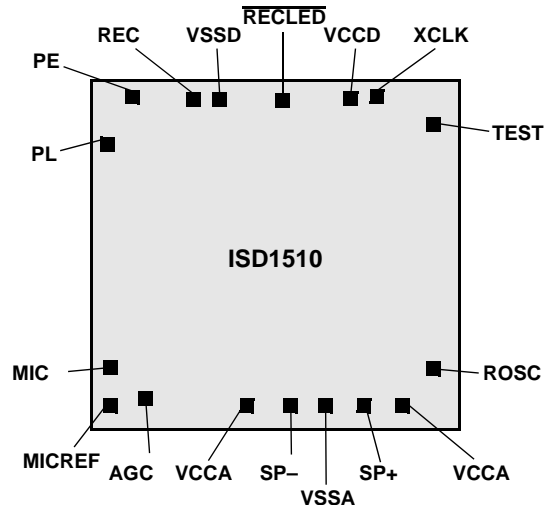


Figure 1-2. ISD1510 Bonding Physical Layout (Unpackaged DIE)

EEPROM Storage

One of the benefits of ISD's ChipCorder technology is the use of on-chip nonvolatile memory, providing zero-power message storage. The message is retained for up to 100 years without power. In addition, the device can be re-recorded typically over 10,000 times.

Basic Operation

The ISD1510 ChipCorder device is controlled by the REC pin, and either of two Playback pins, PLAYE (edge-activated Playback), and PLAYL (level-activated Playback). The ISD1510 parts are configured for design simplicity in a single-message application. Device operation is explained on page 10.

Automatic Power-Down Mode

At the end of a Playback or Record cycle, the ISD1510 device automatically returns to a low-power standby mode, consuming typically 0.5 μ A, provided that PLAYE, PLAYL, REC, XCLK, and TEST pins are LOW (see DC parameters). During a Playback cycle, the device powers down auto-

matically at the end of the message. During a Record cycle, the device powers down immediately after REC is released LOW.

PIN DESCRIPTIONS

Voltage Supplies (V_{CCA} , V_{CCD})

Analog and digital circuits internal to the ISD1510 device use separate power buses to minimize noise on the chip. These power buses are brought out to separate pins on the package and should be tied together as close to the supply as possible. It is important that the power supply be decoupled as close as possible to the package.

Ground Supplies (V_{SSA} , V_{SSD})

Similar to V_{CCA} and V_{CCD} , the analog and digital circuits internal to the ISD1510 device use separate ground buses to minimize noise. These pins should be tied together as close as possible to the device.

Record (REC)

The REC input is an active-HIGH Record signal. The device records whenever REC is HIGH. This pin must remain HIGH for the duration of the Recording. REC takes precedence over either Playback (PLAYE or PLAYL) signal. If REC is pulled HIGH during a Playback cycle, the playback immediately ceases and Recording begins.

NOTE *The REC signal is internally debounced on the rising edge to prevent a false retriggering from a push-button switch.*

A Record cycle is completed when REC is pulled LOW. An end-of-message (EOM) marker is internally recorded, enabling a subsequent Playback cycle to terminate appropriately. The device automatically powers down to standby mode when REC goes LOW. This pin has an internal pull-down device. Holding this pin HIGH will increase standby current consumption.

Playback, Edge-Activated (PLAYE)

When a HIGH-going transition is detected on this input pin, a Playback cycle begins. Playback continues until an end-of-message marker is encountered or the end of the memory space is reached. Upon completion of the Playback cycle, the device automatically powers down into standby mode. Taking PLAYE LOW during a Playback cycle will not terminate the current cycle. This pin has an internal pull-down device. Holding this pin HIGH will increase standby current consumption.

Playback, Level-Activated (PLAYL)

When this input pin transitions from LOW to HIGH, a Playback cycle is initiated. Playback continues until PLAYL is pulled LOW, an end-of-message marker is detected, or the end of the memory space is reached. The device automatically powers down to standby mode upon completion of the playback cycle. This pin has an internal pull-down device. Holding this pin HIGH will increase standby current consumption.

Record LED Output ($\overline{\text{RECLED}}$)

The $\overline{\text{RECLED}}$ output is LOW during a Record cycle. It can be used to drive an LED to provide feedback that a Record cycle is in progress. In addition, $\overline{\text{RECLED}}$ pulses LOW momentarily when an end-of-message or end-of-memory marker is encountered in a Playback cycle.

Microphone Input (MIC)

The microphone input transfers its signal to the on-chip preamplifier. An on-chip Automatic Gain Control (AGC) circuit controls the gain of the preamplifier. An external microphone should be AC coupled to this pin via a series capacitor. The capacitor value, together with the internal 10 K ohm resistance on this pin, determine the low-frequency cutoff for the ISD1510 passband. Internal AC coupling connects the preamplifier to the amplifier.

Microphone Reference (MIC REF)

The MIC REF input is the inverting input to the microphone preamplifier. This provides input noise-cancellation, or common-mode rejection, when the microphone is connected differentially to the device.

Automatic Gain Control (AGC)

The AGC dynamically adjusts the gain of the preamplifier to compensate for the wide range of microphone input levels. The AGC allows the full range of sound, from whispers to loud sounds, to be recorded with minimal distortion. The "attack" time is determined by the time constant of a 5 K Ω internal resistance and an external capacitor (C6 on the schematic on page 10) connected from the AGC pin to V_{SSA} analog ground. The "release" time is determined by the time constant of an external resistor (R5) and an external capacitor (C6) connected in parallel between the AGC pin and V_{SSA} analog ground. Nominal values of 470 K Ω and 4.7 μ F give satisfactory results in most cases.

Speaker Outputs (SP+, SP–)

The SP+ and SP– pins provide direct drive for loudspeakers with impedances as low as 8 ohms. A single output may be used, but, for direct-drive loudspeakers, the two opposite-polarity outputs provide an improvement in output power of up to four times over a single-ended connection. Furthermore, when SP+ and SP– are used, a speaker coupling capacitor is not required. A single-ended connection will require an AC-coupling capacitor between the SP pin and the speaker.

The SP+ pin and the SP– pin are internally connected through a 50K Ω resistance. When not in Playback mode, they are floating.

External Clock (XCLK)

The external clock input for the ISD1510 devices has an internal pull-down resistor. This pin is used for test purposes only. For die, do not bond this pad. For packages, this pin should be connected to ground.

Test (TEST)

The test input for the ISD1510 devices has an internal pull-down resistor. This pin is used for test purposes only. For die, do not bond this pad. For packages, this pin should be connected to ground.

Resistor Controlled Oscillator (ROSC)

The resistor controlled oscillator input enables the user to vary the ISD1510 device Record and Playback duration. The resistor connected between the ROSC pin and V_{SS} (R2) determines the sample frequency and the filter upper pass band for the ISD1510 device. ISD recommends an ROSC resistor value of 100K Ω .

SPECIFICATIONS

Table 1-2. Absolute Maximum Ratings¹
(Packaged Parts)

Condition	Value
Junction temperature	150°C
Storage temperature range	–65°C to +150°C
Voltage applied to any pin	($V_{SS} - 0.3$ V) to ($V_{CC} + 0.3$ V)
Lead temperature (soldering—10 seconds)	300°C
$V_{CC} - V_{SS}$	–0.3 V to + 7.0 V

1. Stresses above those listed may cause permanent damage to the device. Exposure to the absolute maximum ratings may affect device reliability. Functional operation is not implied at these conditions.

Table 1-3. Operating Conditions
(Packaged Parts)

Condition	Value
Operating temperature range ⁽¹⁾	0°C to +50°C
Supply voltage (V_{CC}) ⁽²⁾	+4.5 V to +6.5 V
Ground voltage (V_{SS}) ⁽³⁾	0 V

1. Case Temperature
2. $V_{CC} = V_{CCA} = V_{CCD}$.
3. $V_{SS} = V_{SSA} = V_{SSD}$.

Table 1-4. DC Parameters (Packaged Parts)

Symbol	Parameters	Min ⁽²⁾	Typ ⁽¹⁾	Max ⁽²⁾	Units	Conditions
V _{IL}	Input Low Voltage			0.8	V	
V _{IH}	Input High Voltage	2.0			V	
V _{OL}	Output Low Voltage			0.4	V	I _{OL} = 4.0 mA ⁽³⁾
V _{OH}	Output High Voltage	2.4			V	I _{OH} = -1.6 mA ⁽³⁾
I _{CC}	V _{CC} Current (Operating)			30	mA	V _{CC} = 5.5 V ⁽⁴⁾ , R _{EXT} ∞
I _{SB}	V _{CC} Current (Standby)		0.5	10	μA	⁽⁴⁾ ⁽⁵⁾
I _{ILPD1}	Input Leakage Current			±1	μA	Force V _{SS} ⁽⁶⁾
I _{ILPD2}	Input Current HIGH		200	400	μA	Force V _{CC} ⁽⁷⁾
I _{ILPD3}	Input Current HIGH			130	μA	Force V _{CC} ⁽⁸⁾
R _{EXT}	Output Load Impedance	8			Ω	Speaker Load, SP ⁺ to SP ⁻
R _{MIC} , R _{MICREF}	Preamplifier Input Resistance		10		K Ω	
A _{MSP}	MIC SP +/– Gain		45		dB	AGC = 0.0 V
R _{AGC}	AGC Output Resistance		5		K Ω	

1. Typical values @ T_A = 25°C and 5.0 V.
2. All Min/Max limits are guaranteed by ISD via electrical testing or characterization. Not all specifications are 100% tested.
3. Record LED output, RECLED.
4. V_{CCA} and V_{CCD} connected together.
5. REC, PLAYL, PLAYE, XCLK, and TEST must be at V_{SSD}.
6. REC, PLAYL, and PLAYE.
7. REC.
8. PLAYL and PLAYE.

Table 1-5. AC Parameters (Packaged Parts)¹

Symbol	Characteristic	Min ⁽³⁾	Typ ⁽²⁾	Max ⁽³⁾	Units	Conditions
F _S	Sampling Frequency			6.4	KHz	(4) (7)
F _{CF}	Filter Pass Band		2.6		KHz	3 dB Roll-Off Point ⁽⁵⁾⁽⁶⁾
T _{REC}	Record Duration	10		11	sec	(4) (7)
T _{PLAY}	Playback Duration	10		11	sec	(4) (7)
T _{EOM}	EOM Pulse Width		84		msec	
T _{DB}	Debounce Time		84		msec	
THD	Total Harmonic Distortion		1		%	@ 1 KHz. V _{IN} = 15 mV Peak to Peak
P _{OUT}	Speaker Output Power		24.4		mW	R _{EXT} = 8 Ω
V _{OUT}	Voltage Across Speaker Pins		1.25	2.5	V p-p	R _{EXT} = 600 Ω
V _{IN}	MIC Input Voltage		15	300	mV	Peak-to-Peak

1. These specifications apply with ROSC equaling 100K Ω. Only these specifications are guaranteed and tested.
2. Typical values @ T_A = 25°C and 5.0 V.
3. All Min/Max limits are guaranteed by ISD via electrical testing or characterization. Not all specifications are 100% tested.
4. Oscillator stability may vary as much as ±5% over the operating temperature and voltage ranges.
5. Low-frequency cutoff depends upon value of external capacitors (see Pin Descriptions)
6. Filter specification applies to the anti-aliasing filter and to the smoothing filter.
7. These specifications apply to usage of a 100K Ω resistor.

Table 1-6. Absolute Maximum Ratings¹ (Die)

Condition	Value
Junction temperature	150°C
Storage temperature range	–65°C to +150°C
Voltage applied to any pad	(V _{SS} – 0.3 V) to (V _{CC} + 0.3 V)
V _{CC} – V _{SS}	–0.3 V to + 7.0 V

1. Stresses above those listed may cause permanent damage to the device. Exposure to the absolute maximum ratings may affect device reliability. Functional operation is not implied at these conditions.

Table 1-7. Operating Conditions (Die)

Condition	Value
Operating temperature range	0°C to +50°C
Supply voltage (V _{CC}) ⁽¹⁾	+4.5 V to +6.5 V
Ground voltage (V _{SS}) ⁽²⁾	0 V

1. V_{CC} = V_{CCA} = V_{CCD}.
2. V_{SS} = V_{SSA} = V_{SSD}.

Table 1-8. DC Parameters (Die)

Symbol	Parameters	Min ⁽²⁾	Typ ⁽¹⁾	Max ⁽²⁾	Units	Conditions
V _{IL}	Input Low Voltage			0.8	V	
V _{IH}	Input High Voltage	2.0			V	
V _{OL}	Output Low Voltage			0.4	V	I _{OL} = 4.0 mA ⁽³⁾
V _{OH}	Output High Voltage	2.4			V	I _{OH} = -1.6 mA ⁽³⁾
I _{CC}	V _{CC} Current (Operating)			30	mA	V _{CC} = 5.5 V ⁽⁴⁾ , R _{EXT} ∞
I _{SB}	V _{CC} Current (Standby)		0.5	10	μA	⁽⁴⁾ ⁽⁵⁾
I _{ILPD1}	Input Leakage Current			±1	μA	Force V _{SS} ⁽⁶⁾
I _{ILPD2}	Input Current HIGH		200	400	μA	Force V _{CC} ⁽⁷⁾
I _{ILPD3}	Input Current HIGH			130	μA	Force V _{CC} ⁽⁸⁾
R _{EXT}	Output Load Impedance	8			Ω	Speaker Load, SP ⁺ to SP ⁻
R _{MIC} , R _{MICREF}	Preamplifier Input Resistance		10		KΩ	
A _{MSP}	MIC SP+/- Gain		45		dB	AGC = 0.0 V
R _{AGC}	AGC Output Resistance		5		KΩ	

1. Typical values @ T_A = 25°C and 5.0 V.
2. All Min/Max limits are guaranteed by ISD via electrical testing or characterization. Not all specifications are 100% tested.
3. Record LED output, $\overline{\text{RECLED}}$.
4. V_{CCA} and V_{CCD} connected together.
5. REC, PLAYL, PLAYE, XCLK, and TEST must be at V_{SSD}.
6. REC, PLAYL, and PLAYE.
7. REC.
8. PLAYL and PLAYE.

Table 1-9. AC Parameters (Die)¹

Symbol	Characteristic	Min ⁽³⁾	Typ ⁽²⁾	Max ⁽³⁾	Units	Conditions
F _S	Sampling Frequency			6.4	KHz	(4) (7)
F _{CF}	Filter Pass Band		2.6		KHz	3 dB Roll-OffPoint ⁽⁵⁾⁽⁶⁾
T _{REC}	Record Duration	10		11	sec	(4) (7)
T _{PLAY}	Playback Duration	10		11	sec	(4) (7)
T _{EOM}	EOM Pulse Width		84		msec	
T _{DB}	Debounce Time		84		msec	
THD	Total Harmonic Distortion		1		%	@ 1 KHz, V _{IN} = 15 mV Peak to Peak
P _{OUT}	Speaker Output Power		24.4		mW	R _{EXT} = 8 Ω
V _{OUT}	Voltage Across Speaker Pins		1.25	2.5	V p-p	R _{EXT} = 600 Ω
V _{IN}	MIC Input Voltage		15	300	mV	Peak-to-Peak

1. These specifications apply with ROSC equaling 100K Ω. Only these specifications are guaranteed and tested.
2. Typical values @ T_A = 25°C and 5.0 V.
3. All Min/Max limits are guaranteed by ISD via electrical testing or characterization. Not all specifications are 100% tested.
4. Oscillator stability may vary as much as ±5% over the operating temperature and voltage ranges. For greater accuracy, an external clock can be utilized (see Pin Descriptions).
5. Low-frequency cutoff depends upon value of external capacitors (see Pin Descriptions).
6. Filter specification applies to the antialiasing filter and to the smoothing filter.
7. These specifications apply to usage of a 100K Ω ideal resistor.

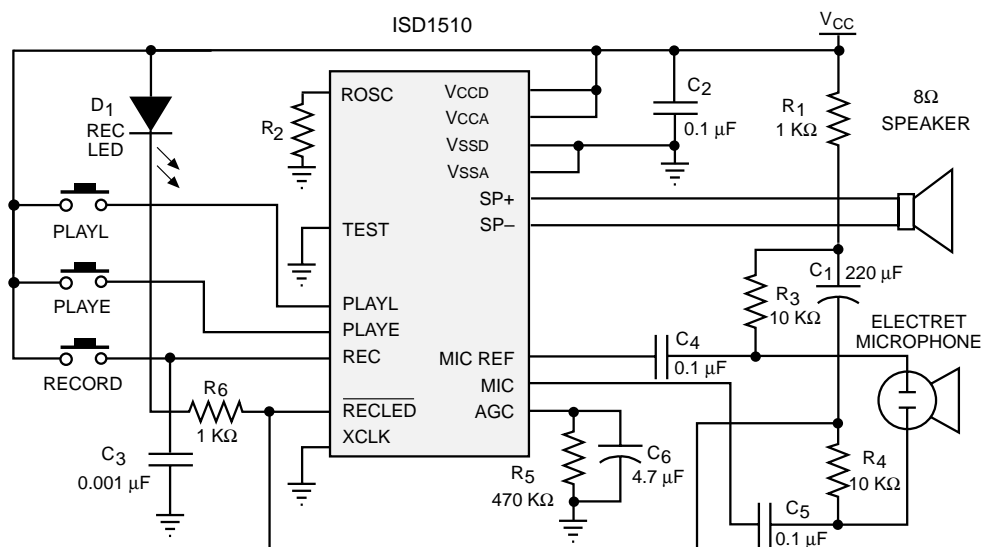


Figure 1-3. ISD1510 Application Example—Design Schematic

The following example operating sequence demonstrates the functionality of the ISD1510 devices.

- Pulling the REC pin HIGH initiates a Record cycle from the beginning of the message space. The device will automatically power down after REC is released LOW. An EOM marker is written at the end of message. If REC is held HIGH, the recording continues until the message space has been filled. Once the message space is filled, recording ceases.

- Pulling the PLAYE pin HIGH initiates a Playback cycle from the beginning of the message space. The falling edge of PLAYE has no effect on operation. When the device reaches the EOM marker, it automatically powers down. If a Recording has filled the message space, the entire message is played. A subsequent rising edge on PLAYE

3. Level-activated playback.

- Pulling the PLAYL pin HIGH initiates a Playback cycle from the beginning of the message space. When the device reaches the EOM marker, it automatically powers down. If Recording has filled the message space, the entire message is played. A subsequent rising edge on PLAYL initiates a new play cycle from the beginning of the memory.

- If PLAYL is pulled LOW any time during the Playback cycle, the device stops playing and enters the power-down mode. A subsequent rising edge on PLAYL initiates a new play cycle from the beginning of memory.

- Record (interrupting playback).

The REC pin takes precedence over other operations. Any HIGH-going transition on REC initiates a new Record operation from the beginning of the memory, regardless of any current operation in progress.

6. Record a message, partially filling the memory.

A Record operation need not fill the entire memory. Releasing the REC pin LOW before filling the message space causes the recording to stop and an EOM marker to be placed. The device powers down automatically.

7. Play back a message that partially fills the memory.

Pulling the PLAYE or PLAYL pin HIGH initiates a Playback cycle. The Playback cycle ceases when the EOM marker is encountered and the device then powers down.

8. RECLE $\overline{\text{D}}$ operation.

The RECLE $\overline{\text{D}}$ output pin provides an active-LOW signal which can be used to drive an LED as a "Record-in-progress" indicator. It returns to a HIGH state when the REC pin is released LOW or when the recording is completed due to the memory being filled. This pin also pulses LOW to indicate the end of a message has been played.

9. ROSC Operation

The duration of the device can be varied by changing the value of R2 (ROSC). This means the ISD1510 device can actually be between 8 to 16 seconds duration. See the curve below which charts typical durations when the ROSC is varied from 80K Ω to 160K Ω .

This feature allows frequency shifting where a recorded voice or sound can be played back faster or slower than normal for special effects. For example, use a 100K Ω resistor to make the recording and then playback with either an 80K Ω resistor for faster "chipmunk" talk or with a 120K Ω resistor for a slower, lower voice.

Another feature is a "Pause" or interrupt function that can be done by taking the ROSC resistor to V_{CC} to stop playback momentarily, resuming when the resistor is connected back to ground.

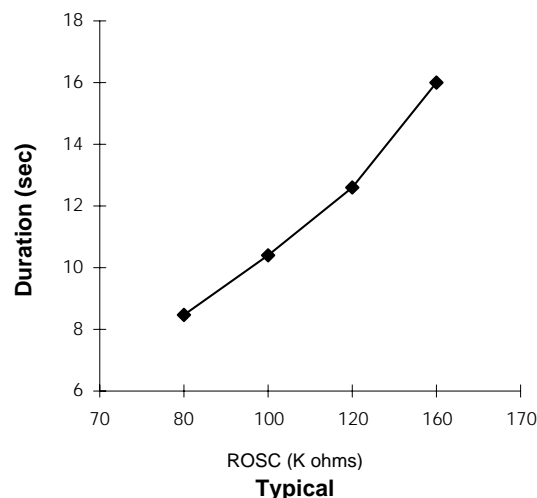


Figure 1-4. ISD1510 Duration vs. ROSC at $T_A = 25^\circ\text{C}$ and 5.0 V

TIMING DIAGRAMS

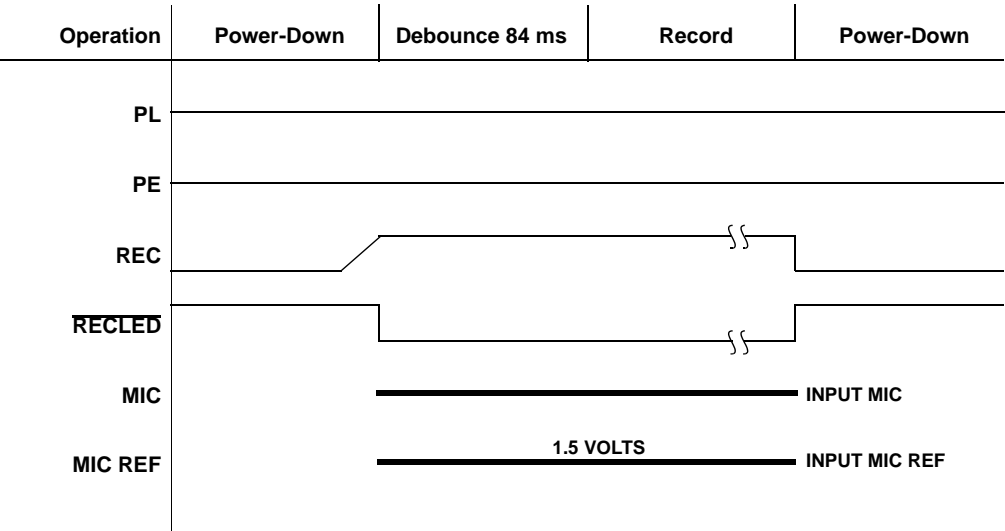


Figure 1-5. Record Message Until Record Goes LOW

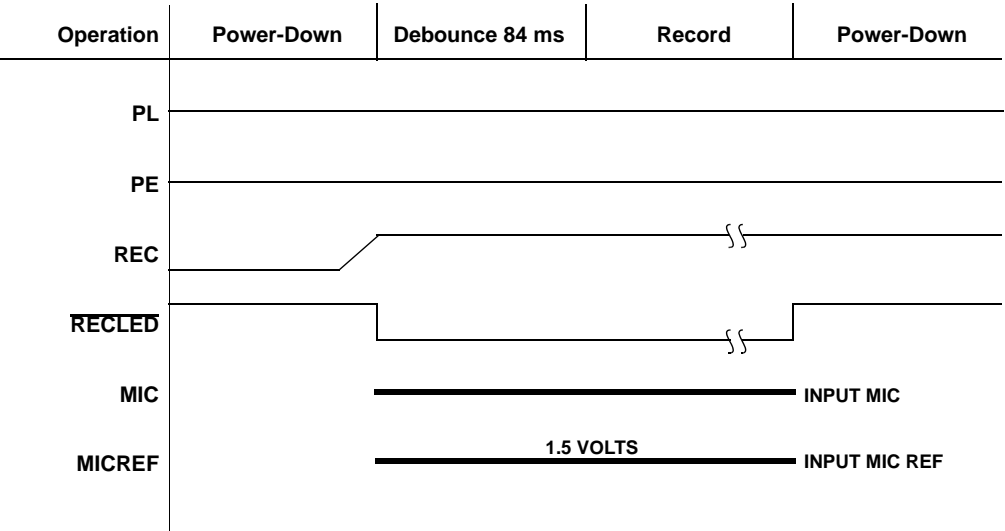


Figure 1-6. Record Message Until Array is Full

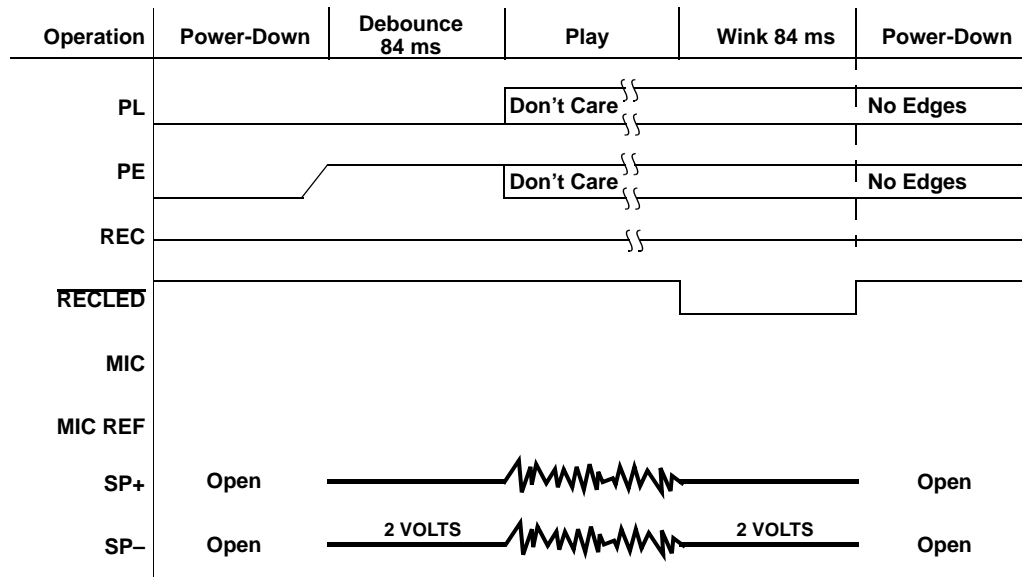


Figure 1-7. Play Edge (PE) Play Until End of Recording

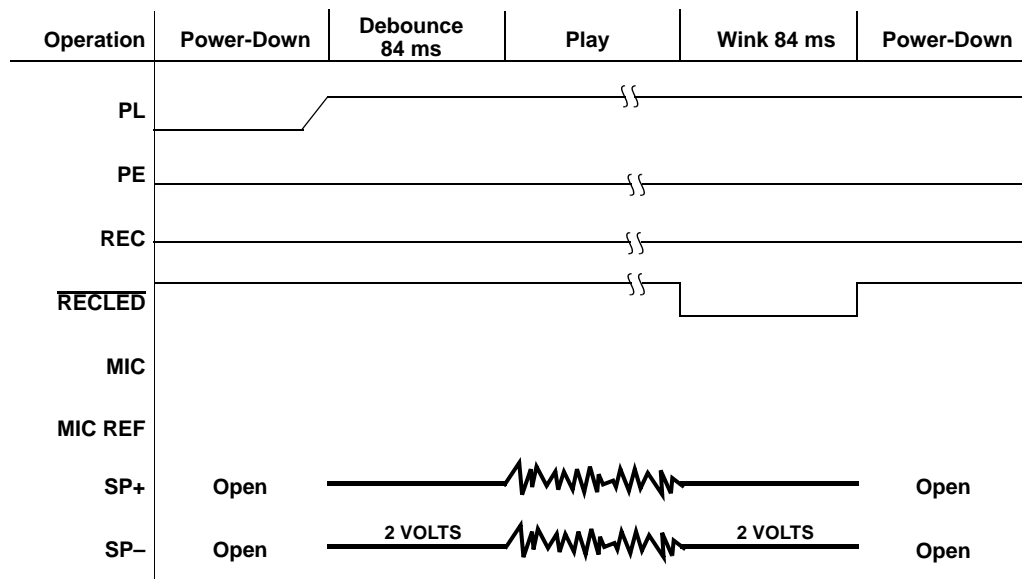


Figure 1-8. Play Level (PL) Play Until End of Recording

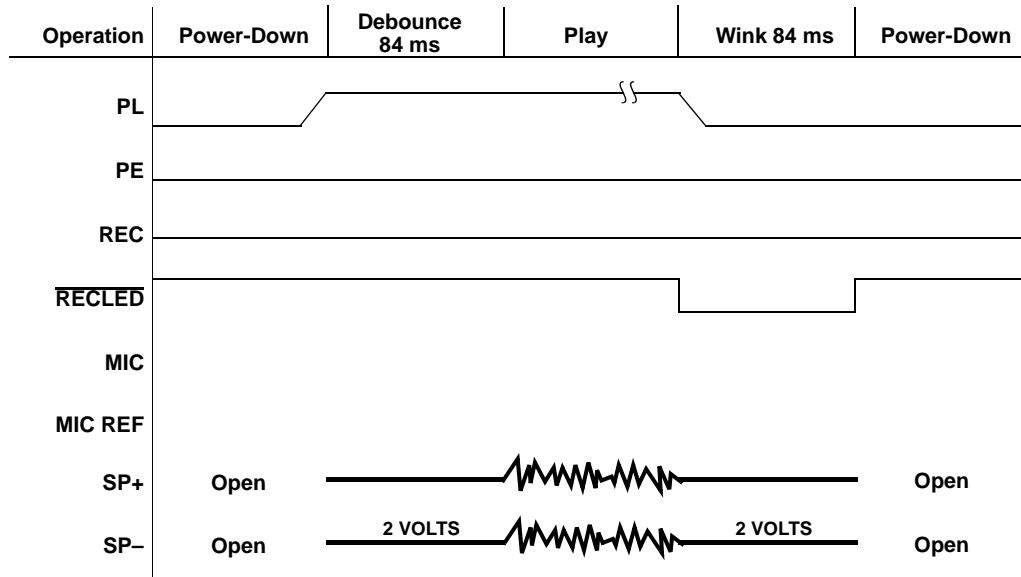


Figure 1-9. Play Until Play Level (PL) Falls

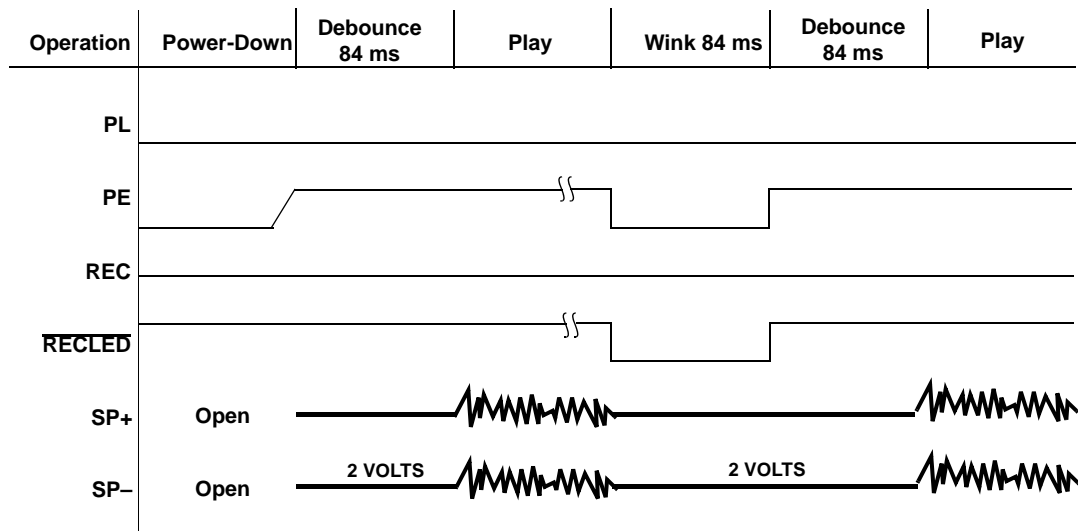


Figure 1-10. Looping Play, PE to RECLED

ORDERING INFORMATION

Product Number Descriptor Key

ISD1510 _____

Duration: _____

10 = 10 Seconds minimum
duration

Package Type:

P = 28-Lead 0.600-Inch Plastic Dual Inline Package (PDIP)
W = Wafer
X = Die
S = 28-lead 0.300-inch SOIC

When ordering, please refer to the following part numbers which are planned to be supported in volume for this product series. Consult the local ISD Sales Representative or Distributor for availability information.

Part Number
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