

General purpose transistor (dual transistors)

UMZ1N / IMZ1A

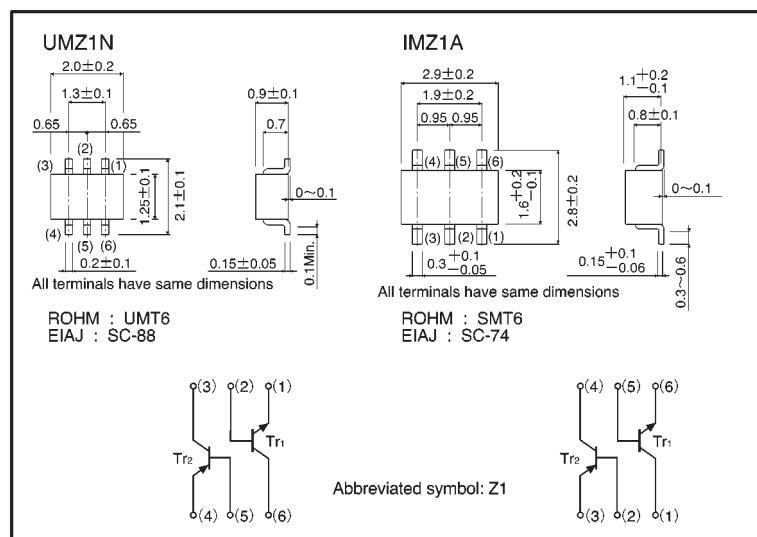
●Features

- 1) Both a 2SA1037AK chip and 2SC241ZK chip in a UMT or SMT package.
- 2) Mounting possible with UMT3 or SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

●Structure

NPN/PNP epitaxial planar silicon transistor

●External dimensions (Units: mm)



●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits		Unit
		Tr ₁	Tr ₂	
Collector-base voltage	V _{CBO}	60	-60	V
Collector-emitter voltage	V _{CEO}	50	-50	V
Emitter-base voltage	V _{EBO}	7	-6	V
Collector current	I _C	150	-150	mA
Collector power dissipation	P _C	150(TOTAL)		mW *1
		300(TOTAL)		*2
Junction temperature	T _j	150		°C
Storage temperature	T _{stg}	-55~+150		°C

*1 120mW per element must not be exceeded.

*2 200mW per element must not be exceeded.

●Electrical characteristics ($T_a = 25^\circ\text{C}$)Tr₁ (NPN)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV_{CBO}	60	—	—	V	$I_c=50 \mu\text{A}$
Collector-emitter breakdown voltage	BV_{CEO}	50	—	—	V	$I_c=1\text{mA}$
Emitter-base breakdown voltage	BV_{EBO}	7	—	—	V	$I_e=50 \mu\text{A}$
Collector cutoff current	I_{CBO}	—	—	0.1	μA	$V_{\text{CB}}=60\text{V}$
Emitter cutoff current	I_{EBO}	—	—	0.1	μA	$V_{\text{EB}}=7\text{V}$
Collector-emitter saturation voltage	$V_{\text{CE}}(\text{sat})$	—	—	0.4	V	$I_c/I_b=50\text{mA}/5\text{mA}$
DC current transfer ratio	h_{FE}	120	—	560	—	$V_{\text{CE}}/I_c=6\text{V}/1\text{mA}$
Transition frequency	f_T	—	180	—	MHz	$V_{\text{CE}}=12\text{V}, I_c=-2\text{mA}, f=100\text{MHz}$
Output capacitance	C_{ob}	—	2	3.5	pF	$V_{\text{CB}}=12\text{V}, I_e=0\text{A}, f=1\text{MHz}$

Tr₂ (PNP)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-base breakdown voltage	BV_{CBO}	-60	—	—	V	$I_c=-50 \mu\text{A}$
Collector-emitter breakdown voltage	BV_{CEO}	-50	—	—	V	$I_c=-1\text{mA}$
Emitter-base breakdown voltage	BV_{EBO}	-6	—	—	V	$I_e=-50 \mu\text{A}$
Collector cutoff current	I_{CBO}	—	—	-0.1	μA	$V_{\text{CB}}=-60\text{V}$
Emitter cutoff current	I_{EBO}	—	—	-0.1	μA	$V_{\text{EB}}=-6\text{V}$
Collector-emitter saturation voltage	$V_{\text{CE}}(\text{sat})$	—	—	-0.5	V	$I_c/I_b=-50\text{mA}/-5\text{mA}$
DC current transfer ratio	h_{FE}	120	—	560	—	$V_{\text{CE}}/I_c=-6\text{V}/-1\text{mA}$
Transition frequency	f_T	—	140	—	MHz	$V_{\text{CE}}=-12\text{V}, I_c=2\text{mA}, f=100\text{MHz}$
Output capacitance	C_{ob}	—	4	5	pF	$V_{\text{CB}}=-12\text{V}, I_e=0\text{A}, f=1\text{MHz}$

●Packaging specifications

Part No.	Packaging type	Taping	
	Code	TR	T108
	Basic ordering unit (pieces)	3000	3000
UMZ1N		○	—
IMZ1A		—	○

● Electrical characteristic curves

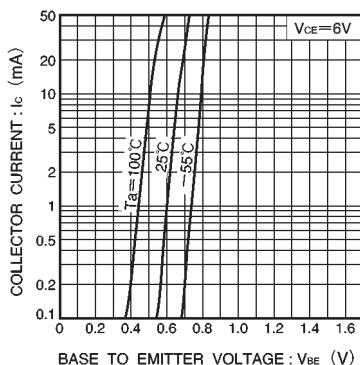
Tr₁ (NPN)

Fig.1 Grounded emitter propagation characteristics

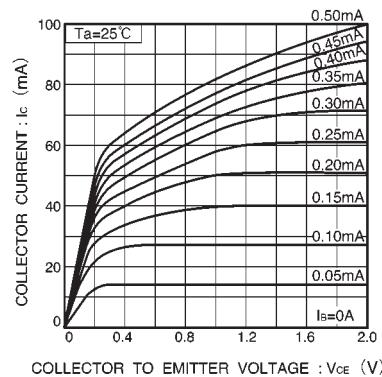


Fig.2 Grounded emitter output characteristics (I)

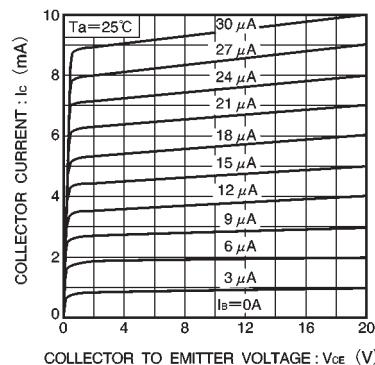


Fig.3 Grounded emitter output characteristics (II)

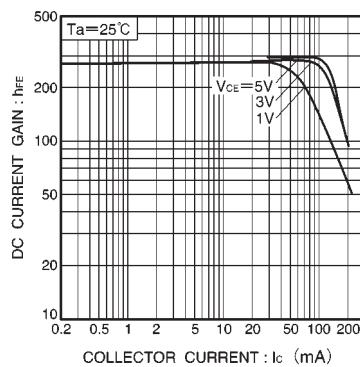


Fig.4 DC current gain vs. collector current (I)

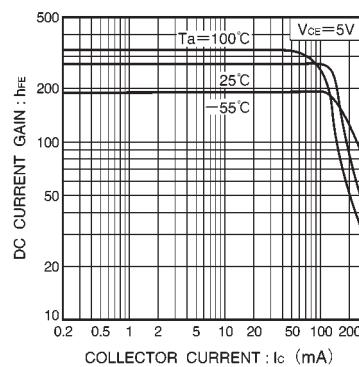


Fig.5 DC current gain vs. collector current (II)

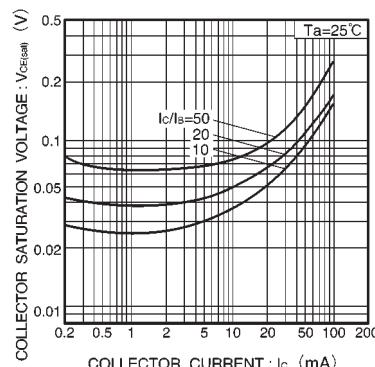


Fig.6 Collector-emitter saturation voltage vs. collector current (I)

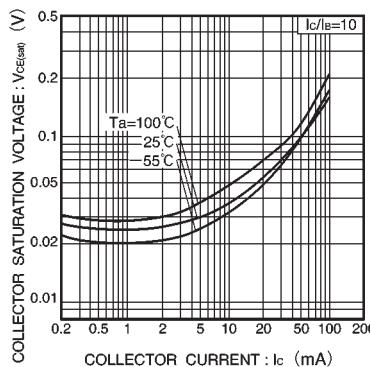


Fig.7 Collector-emitter saturation voltage vs. collector current (II)

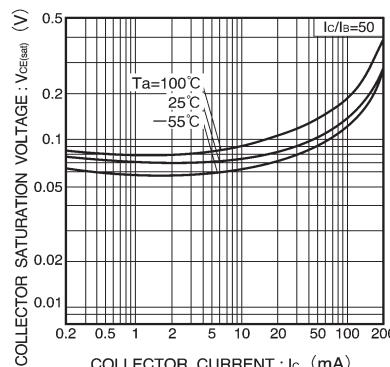


Fig.8 Collector-emitter saturation voltage vs. collector current (III)

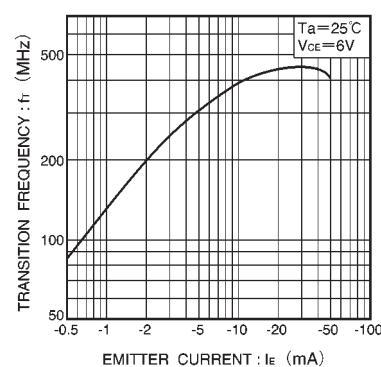


Fig.9 Gain bandwidth product vs. emitter current

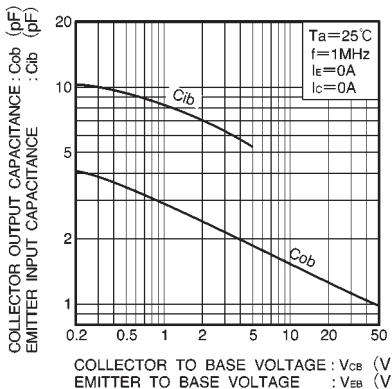


Fig.10 Collector output capacitance vs.
collector-base voltage
Emitter input capacitance vs.
emitter-base voltage

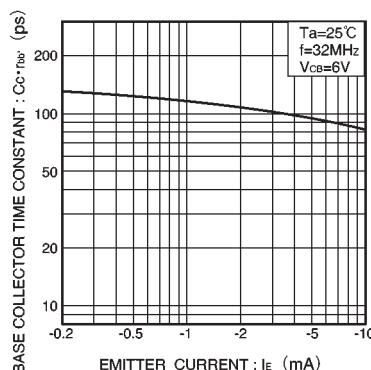


Fig.11 Base-collector time constant vs.
emitter current

● Electrical characteristic curves

Tr_2 (PNP)

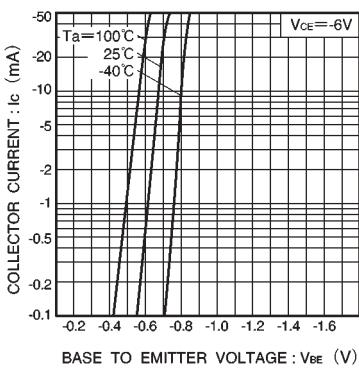


Fig.12 Grounded emitter propagation
characteristics

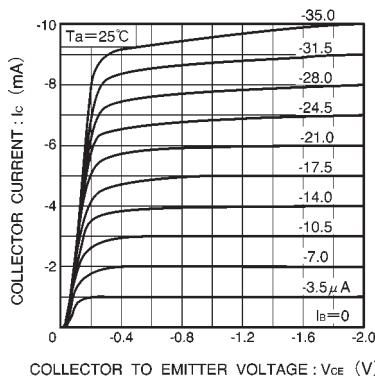


Fig.13 Grounded emitter output
characteristics (I)

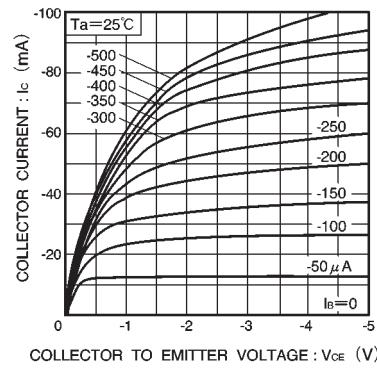


Fig.14 Grounded emitter output
characteristics (II)

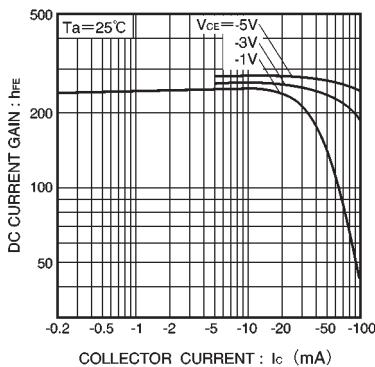


Fig.15 DC current gain vs. collector
current (I)

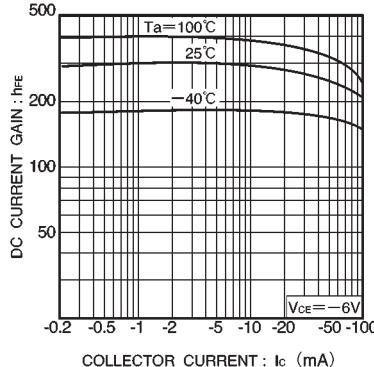


Fig.16 DC current gain vs. collector
current (II)

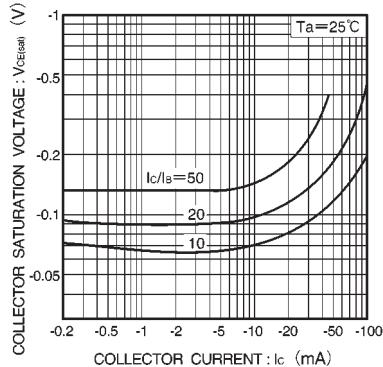


Fig.17 Collector-emitter saturation
voltage vs. collector current (I)

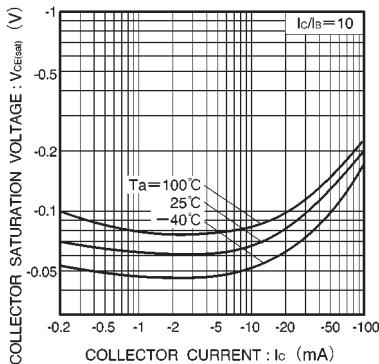


Fig.18 Collector-emitter saturation voltage vs. collector current (II)

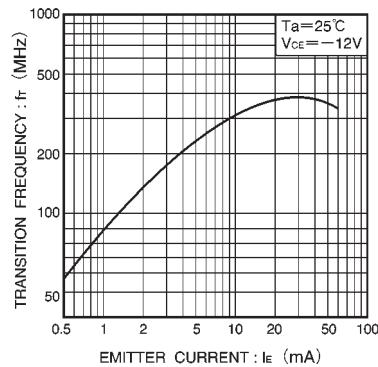


Fig.19 Gain bandwidth product vs. emitter current

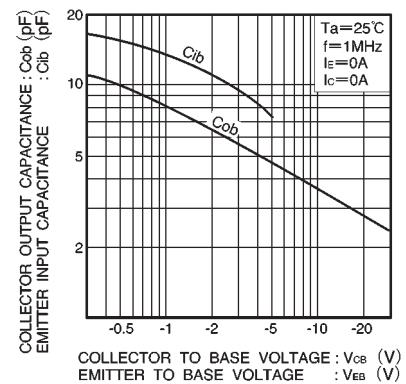


Fig.20 Collector output capacitance vs. collector-base voltage
Emitter input capacitance vs. emitter-base voltage