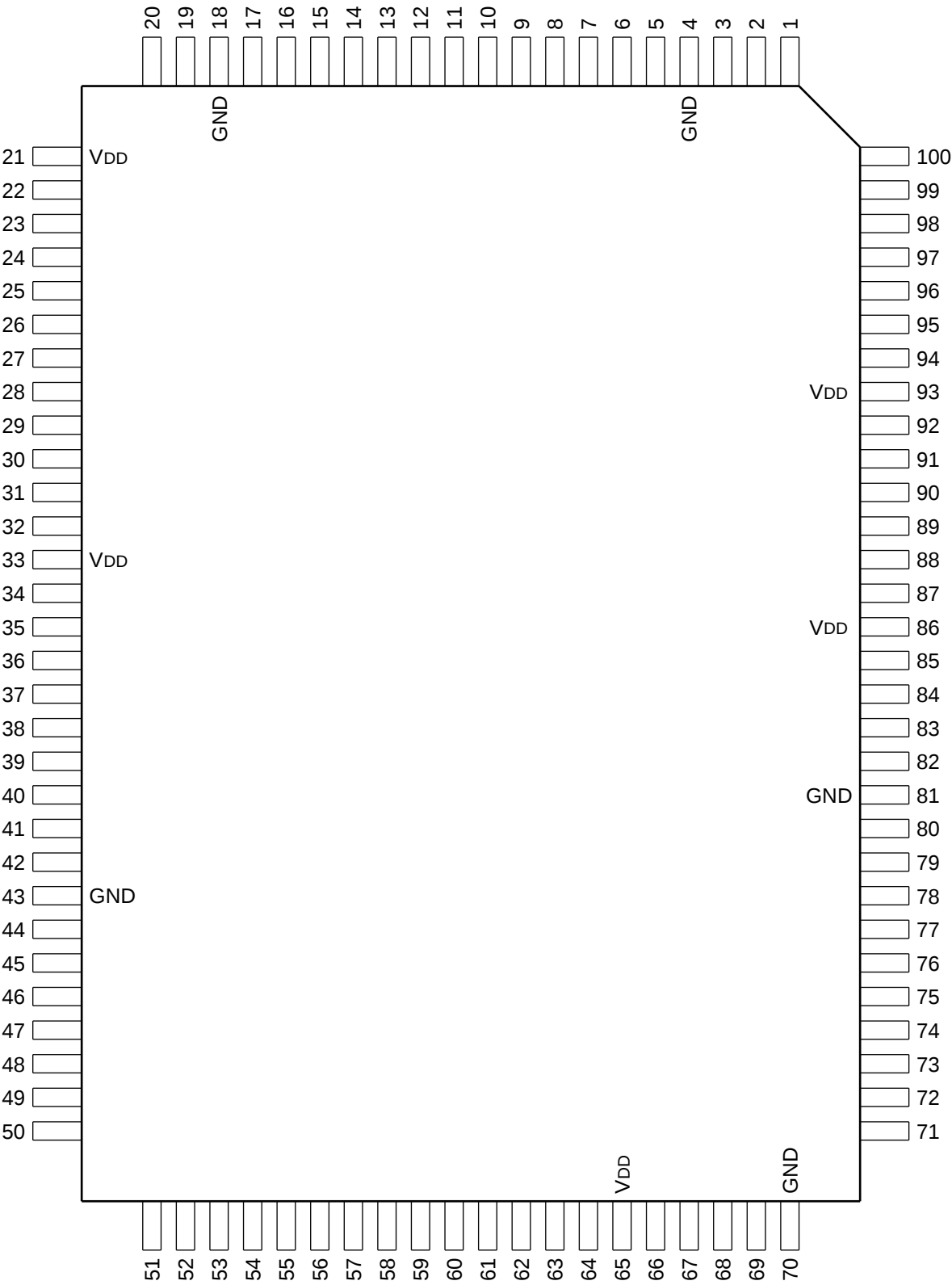
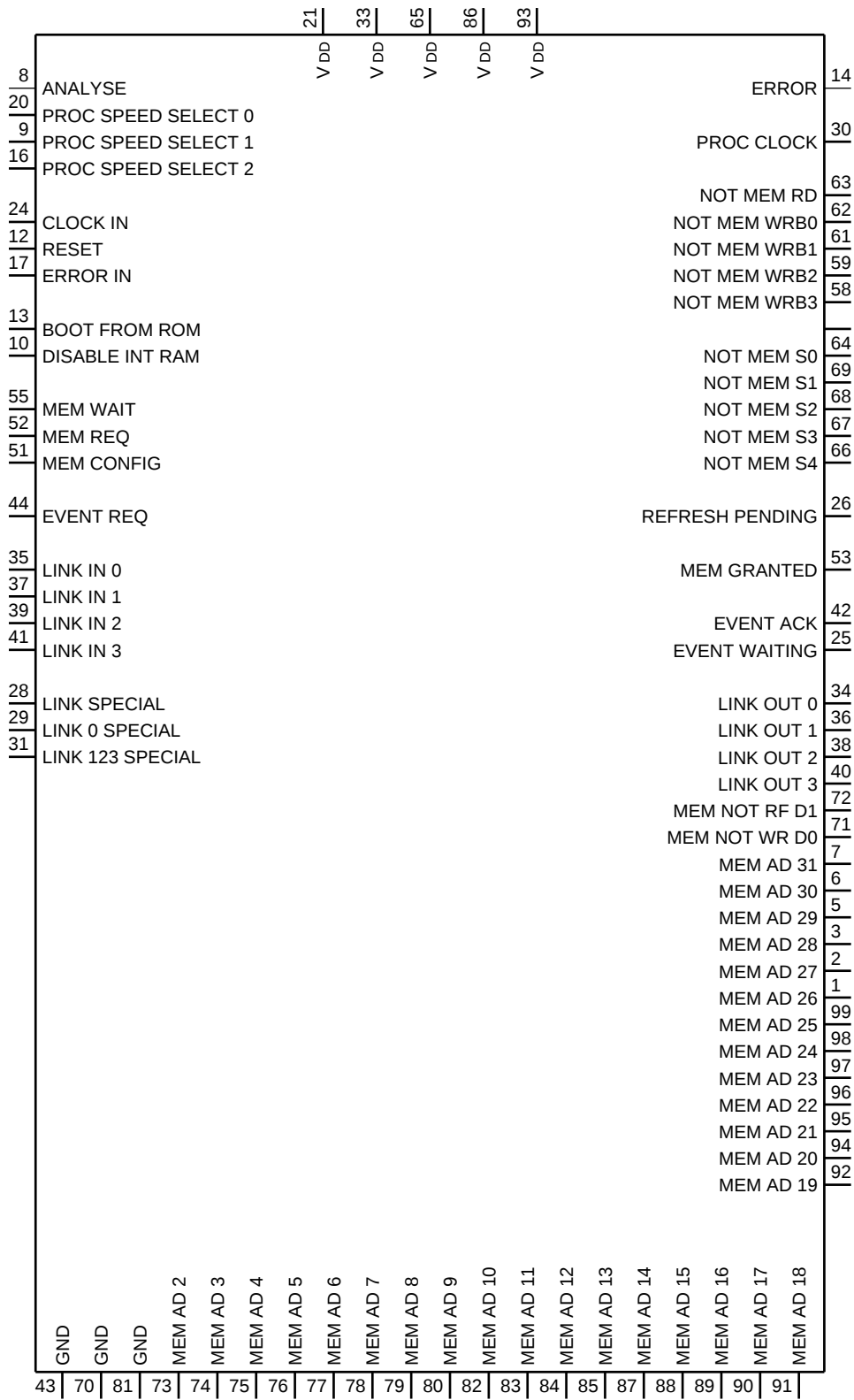

32-BIT RISC PROCESSOR
- TOP VIEW -





INPUT

CLOCK IN	; CLOCK
PROC SPEED SELECT 0-2	; PROCESSOR SPEED SELECTOR
RESET	; SYSTEM RESET
ERROR IN	; ERROR DAISYCHAIN
ANALYSE	; ERROR ANALYSIS
BOOT FROM ROM	; BOOT FROM EXTERNAL ROM
DISABLE INT RAM	; DISABLE INTERNAL RAM
MEM WAIT	; MEMORY CYCLE EXTENDER
MEM REQ	; DIRECT MEMORY ACCESS REQUEST
MEM CONFIG	; MEMORY CONFIGURATION DATA
EVENT REQ	; EVENT REQUEST
LINK IN0-3	; SERIAL DATA
LINK SPECIAL	; SELECT NON-STANDARD SPEED
LINK 0-3 SPECIAL	; SELECT SPECIAL SPEED

OUTPUT

ERROR	; ERROR INDICATOR
PROC CLOCK OUT	; PROCESSOR CLOCK
NOT MEM RD	; REED STROBE
NOT MEM WRB0-3	; BYTE-ADDRESSING WRITE STROBES
NOT MEM S0-4	; FIVE GENERAL PURPOSE STROBES
NOT MEM RF	; DYNAMIC MEMORY REFRESH INDICATOR
REFRESH PENDING	; DYNAMIC REFRESH IS PENDING
MEM GRANTED	; DIRECT MEMORY ACCESS GRANTED
EVENT ACK	; EVENT REQUEST ACKNOWLEDGE
EVENT WAITING	; EVENT INPUT REQUESTED BY SOFTWARE
LINK OUT 0-3	; SERIAL DATA

INPUT/OUTPUT

MEM NOT WR D0	; MULTIPLEXED DATA BIT 0
MEM NOT RF D1	; MULTIPLEXED DATA BIT 1
MEM AD2-31	; MULTIPLEXED DATA AND ADDRESS BUS

