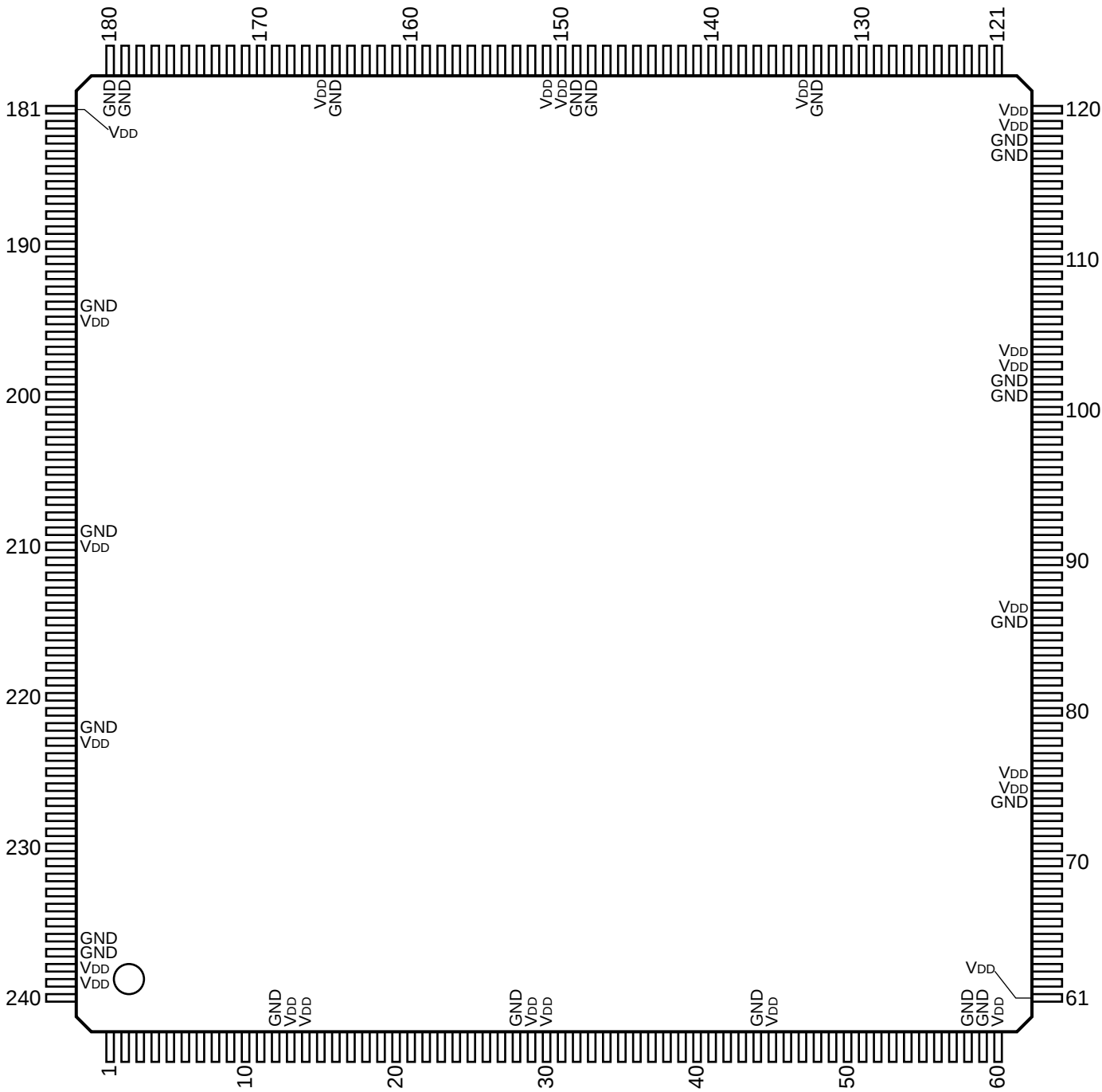

C-MOS SINGLE CHIP VIDEO CODEC AND
MULTIMEDIA COMMUNICATIONS PROCESSOR
-TOP VIEW-



INPUT/OUTPUT

AUX0-3 ; AUXILIARY CONTROL LINES

DBUS0-31 ; REFERENCE DRAM DATA BUS

HD0-15 ; HOST DATA BUS

HSYNCSN ; HORIZONTAL SYNC FOR SCREEN VIDEO PORT

LD0-31 ; RISC PORT DATA BUS

VSYNCSN ; VERTICAL SYNC FOR SCREEN VIDEO PORT

(VDD = +5V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	LA17	41	I	ARFS	81	I	HSYNCCAM	121	O	UVSCN0	161	I	UVCAM3	201	I/O	LD4
2	O	LA16	42	I	ATFS	82	O	CAS1	122	O	UVSCN1	162	I/O	HD10	202	I/O	LD12
3	O	LA15	43	I/O	AUX0	83	O	RAS	123	O	UVSCN2	163	I/O	HD11	203	I/O	LD20
4	O	LA14	44	—	GND	84	O	DWE	124	O	UVSCN3	164	I/O	HD12	204	I/O	LD28
5	O	LA13	45	—	VDD	85	I	TEST1L	125	I	UVCAM0	165	—	GND	205	I/O	LD5
6	O	LA12	46	I/O	AUX1	86	—	GND	126	O	UVSCN4	166	—	VDD	206	I/O	LD13
7	O	LA11	47	I/O	AUX2	87	—	VDD	127	O	UVSCN5	167	I/O	HD13	207	I/O	LD21
8	O	LA10	48	I/O	AUX3	88	I/O	DBUS5	128	O	UVSCN6	168	I/O	HD14	208	I/O	LD29
9	O	LA9	49	I	YCAME	89	I/O	DBUS21	129	O	UVSCN7	169	I/O	HD15	209	—	GND
10	O	LA8	50	O	DA0	90	I/O	DBUS6	130	O	YSCN0	170	I	HREAD	210	—	VDD
11	I	YCAME	51	O	DA1	91	I/O	DBUS22	131	O	YSCN1	171	I	HWRITE	211	I	UVCAM6
12	—	GND	52	O	DA2	92	I/O	DBUS7	132	O	YSCN2	172	O	HIRQ	212	O	LWRLH
13	—	VDD	53	O	DA3	93	I/O	DBUS23	133	—	GND	173	I	UVCAM4	213	O	LWRLH
14	—	VDD	54	O	DA4	94	I	PCLK2XCAM	134	—	VDD	174	I	HA0	214	O	LWRHL
15	O	LA7	55	O	DA5	95	I/O	DBUS8	135	O	YSCN3	175	I	HA1	215	O	LWRHH
16	O	LA6	56	O	DA6	96	I/O	DBUS24	136	O	YSCN4	176	I	HA2	216	O	LOE
17	O	LA5	57	O	DA7	97	I/O	DBUS9	137	O	YSCN5	177	O	HWRREQ	217	O	LCE0
18	O	LA4	58	—	GND	98	I/O	DBUS25	138	I	UVCAM1	178	O	HRDREQ	218	O	LCE1
19	O	LA3	59	—	GND	99	I/O	DBUS10	139	O	YSCN6	179	—	GND	219	O	LCE2
20	O	LA2	60	—	VDD	100	I/O	DBUS26	140	O	YSCN7	180	—	GND	220	O	LCE3
21	O	LA1	61	—	VDD	101	—	GND	141	I	PCLK2XSCN	181	—	VDD	221	I	UVCAM7
22	O	LA0	62	I	YCAME	102	—	GND	142	I	PCLKQSCN	182	I/O	LD0	222	—	GND
23	I	YCAME	63	O	DA8	103	—	VDD	143	I/O	VSYNCSN	183	I/O	LD8	223	—	VDD
24	I	CPUCLK	64	O	DA9	104	—	VDD	144	I/O	HSYNCSN	184	I/O	LD16	224	I/O	LD6
25	I	RESET	65	I/O	DBUS0	105	I/O	DBUS11	145	I	UVCAM2	185	I/O	LD24	225	I/O	LD14
26	I	DEBUGIRQ	66	I/O	DBUS16	106	I/O	DBUS27	146	O	BLANKSCN	186	I/O	LD1	226	I/O	LD22
27	O	TEST2	67	I/O	DBUS1	107	I	PCLKQCAM	147	I/O	HD0	187	I/O	LD9	227	I/O	LD30
28	—	GND	68	I/O	DBUS17	108	I/O	DBUS12	148	—	GND	188	I/O	LD17	228	I/O	LD7
29	—	VDD	69	I/O	DBUS2	109	I/O	DBUS28	149	—	GND	189	I/O	LD25	229	I/O	LD15
30	—	VDD	70	I/O	DBUS18	110	I/O	DBUS13	150	—	VDD	190	I/O	LD2	230	I/O	LD23
31	I	ROMDIS	71	I	VSYNCCAM	111	I/O	DBUS29	151	—	VDD	191	I/O	LD10	231	I/O	LD31
32	O	TDMDX	72	I/O	DBUS3	112	I/O	DBUS14	152	I/O	HD1	192	I/O	LD18	232	I	YCAME
33	I	TDMDR	73	I/O	DBUS19	113	I/O	DBUS30	153	I/O	HD2	193	I/O	LD26	233	O	LA19
34	I	TDMCLK	74	—	GND	114	I/O	DBUS15	154	I/O	HD3	194	—	GND	234	O	LA18
35	I	TDMFS	75	—	VDD	115	I/O	DBUS31	155	I/O	HD4	195	—	VDD	235	I	YCAME
36	O	TDMTSC	76	—	VDD	116	I	ODDCAM	156	I/O	HD5	196	I	UVCAM5	236	—	GND
37	I	YCAME	77	I/O	DBUS4	117	—	GND	157	I/O	HD6	197	I/O	LD3	237	—	GND
38	O	AOUT	78	I/O	DBUS20	118	—	GND	158	I/O	HD7	198	I/O	LD11	238	—	VDD
39	I	AIN	79	O	DOE	119	—	VDD	159	I/O	HD8	199	I/O	LD19	239	—	VDD
40	I	ACLK	80	O	CAS0	120	—	VDD	160	I/O	HD9	200	I/O	LD27	240	I	YCAME

