













6-Pin DIP Optoisolators Logic Output

The H11L1 and H11L2 have a gallium arsenide IRED optically coupled to a high–speed integrated detector with Schmitt trigger output. Designed for applications requiring electrical isolation, fast response time, noise immunity and digital logic compatibility.

- Guaranteed Switching Times t_{on}, t_{off} < 4 μs
- Built-In On/Off Threshold Hysteresis
- High Data Rate, 1 MHz Typical (NRZ)
- · Wide Supply Voltage Capability
- Microprocessor Compatible Drive
- To order devices that are tested and marked per VDE 0884 requirements, the suffix "V" must be included at end of part number. VDE 0884 is a test option.

Applications

- Interfacing Computer Terminals to Peripheral Equipment
- · Digital Control of Power Supplies
- Line Receiver Eliminates Noise
- Digital Control of Motors and Other Servo Machine Applications
- · Logic to Logic Isolator
- Logic Level Shifter Couples TTL to CMOS

MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
INPUT LED			
Reverse Voltage	٧R	6	Volts
Forward Current — Continuous — Peak Pulse Width = 300 μs, 2% Duty Cycle	lF	60 1.2	mA Amp
LED Power Dissipation @ T _A = 25°C Derate above 25°C	PD	120 1.41	mW mW/°C
OUTDUT DETECTOR	•		•

OUTPUT DETECTOR

Output Voltage Range	Vo	0–16	Volts
Supply Voltage Range	VCC	3–16	Volts
Output Current	lo	50	mA
Detector Power Dissipation @ T _A = 25°C Derate above 25°C	PD	150 1.76	mW mW/°C

TOTAL DEVICE

Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	250 2.94	mW mW/°C
Maximum Operating Temperature(2)	T _A	-40 to +85	°C
Storage Temperature Range(2)	T _{stg}	-55 to +150	°C
Soldering Temperature (10 s)	TL	260	°C
Isolation Surge Voltage (Pk ac Voltage, 60 Hz, 1 Second Duration)(1)	VISO	7500	Vac(pk)

- 1. Isolation surge voltage is an internal device dielectric breakdown rating. For this test, Pins 1 and 2 are common, and Pins 4, 5 and 6 are common.
- 2. Refer to Quality and Reliability Section in Opto Data Book for information on test conditions.

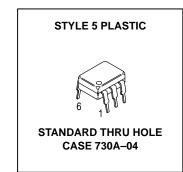
Preferred devices are Motorola recommended choices for future use and best overall value.

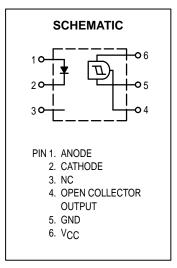
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H11L1* [IF(on) = 1.6 mA Max] H11L2

[IF(on) = 10 mA Max]

*Motorola Preferred Device







H11L1 H11L2

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)(1)

	Symbol	Min	Typ (1)	Max	Unit	
INPUT LED		-				-
Reverse Leakage Current ($V_R = 3 \text{ V}, R_L = 1 \text{ M}\Omega$)		IR	_	0.05	10	μΑ
Forward Voltage (I _F = 10 mA) (I _F = 0.3 mA)		VF	— 0.75	1.2 0.95	1.5 —	Volts
Capacitance (V _R = 0 \	Capacitance (V _R = 0 V, f = 1 MHz)			18	_	pF
OUTPUT DETECTOR		•				
Operating Voltage		Vcc	3	_	15	Volts
Supply Current (I _F = 0	, V _{CC} = 5 V)	ICC(off)	_	1	5	mA
Output Current, High (I _F = 0, V _{CC} = V ₀ = 15 V)		IOH	_	_	100	μΑ
COUPLED		-				
Supply Current (IF = IF(on), VCC = 5 V)		ICC(on)	_	1.6	5	mA
Output Voltage, Low (F	Output Voltage, Low (R _L = 270 Ω , V _{CC} = 5 V, I _F = I _{F(on)})		_	0.2	0.4	Volts
· ·	Threshold Current, ON H11L1 $(R_L = 270 \Omega, V_{CC} = 5 V)$ H11L2		_	1.2 —	1.6 10	mA
Threshold Current, OFF H11L1 $(R_L = 270 \Omega, V_{CC} = 5 V)$ H11L2		^I F(off)	0.3 0.3	0.75 —	_ _	mA
Hysteresis Ratio (R _L = 270 Ω , V _{CC} = 5 V)		IF(off) IF(on)	0.5	0.75	0.9	
Isolation Voltage ⁽²⁾ 60 Hz, AC Peak, 1 second, T _A = 25°C		VISO	7500	_	_	Vac(pk)
Turn-On Time	D. 970 o(3)	t _{on}	_	1.2	4	μs
Fall Time	$R_L = 270 \Omega(3)$ $V_{CC} = 5 V$,	t _f	_	0.1	_	
Turn-Off Time	n–Off Time $IF = IF(on)$ $T_{\Delta} = 25^{\circ}C$		_	1.2	4	
Rise Time			_	0.1	_	

- 1. Always design to the specified minimum/maximum electrical limits (where applicable).
- 2. For this test, IRED Pins 1 and 2 are common and Output Gate Pins 4, 5, 6 are common.
- 3. $\rm R_{L}$ value effect on switching time is negligible.

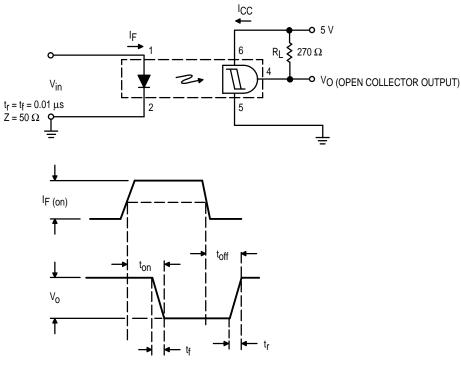


Figure 1. Switching Test Circuit

TYPICAL CHARACTERISTICS

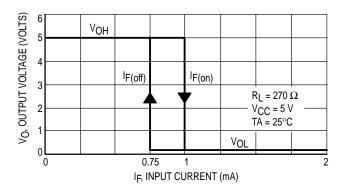
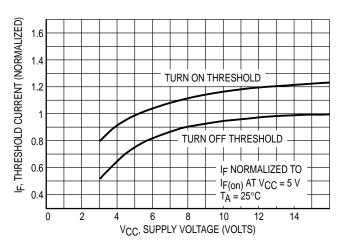


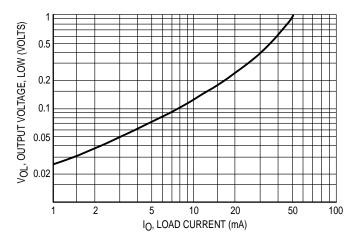
Figure 2. Transfer Characteristics for H11L1



IF(on), IF(off), THRESHOLD CURRENT (NORMALIZED) 1.6 1.4 1.2 8.0 NORMALIZED TO 0.6 V_{CC} = 5 V $T_{\Delta} = 25^{\circ}C$ -50 -25 25 75 100 50 T_A, TEMPERATURE (°C)

Figure 3. Threshold Current versus Supply Voltage

Figure 4. Threshold Current versus Temperature



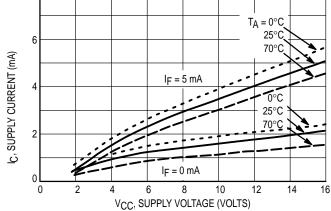
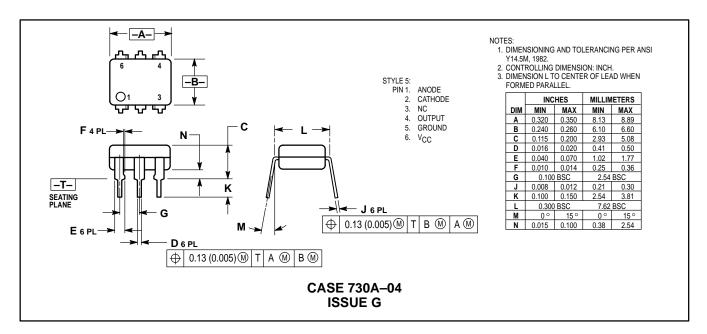
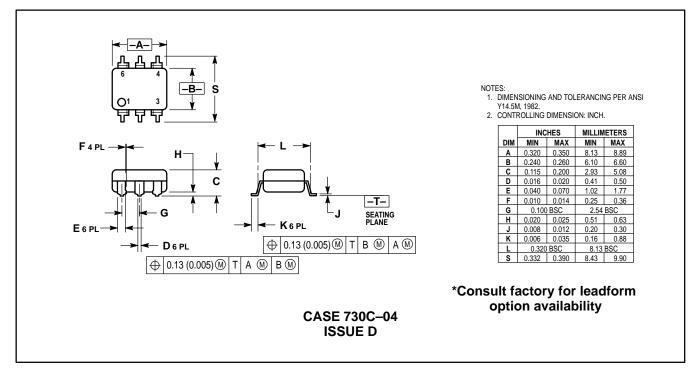


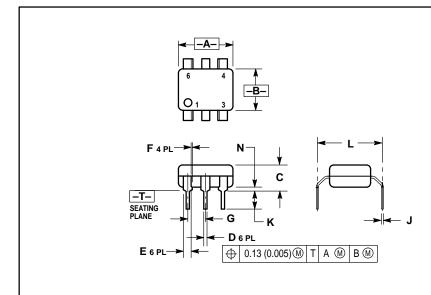
Figure 5. Output Voltage, Low versus Load Current

Figure 6. Supply Current versus Supply Voltage

PACKAGE DIMENSIONS







CASE 730D-05 ISSUE D

- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: INCH.

 3. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.320	0.350	8.13	8.89
В	0.240	0.260	6.10	6.60
С	0.115	0.200	2.93	5.08
D	0.016	0.020	0.41	0.50
Е	0.040	0.070	1.02	1.77
F	0.010	0.014	0.25	0.36
G	0.100 BSC		2.54 BSC	
J	0.008	0.012	0.21	0.30
K	0.100	0.150	2.54	3.81
L	0.400	0.425	10.16	10.80
N	0.015	0.040	0.38	1.02

*Consult factory for leadform option availability

H11L1 H11L2

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