



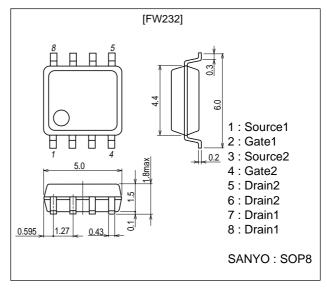
Load Switching Applications

Features

- · Low ON-resistance.
- 2.5V drive.

Package Dimensions

unit : mm 2129



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ID		8	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	52	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (1000mm²X0.8mm) 1unit	1.7	W
Total Dissipation	PT	Mounted on a ceramic board (1000mm ² X0.8mm)	2.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

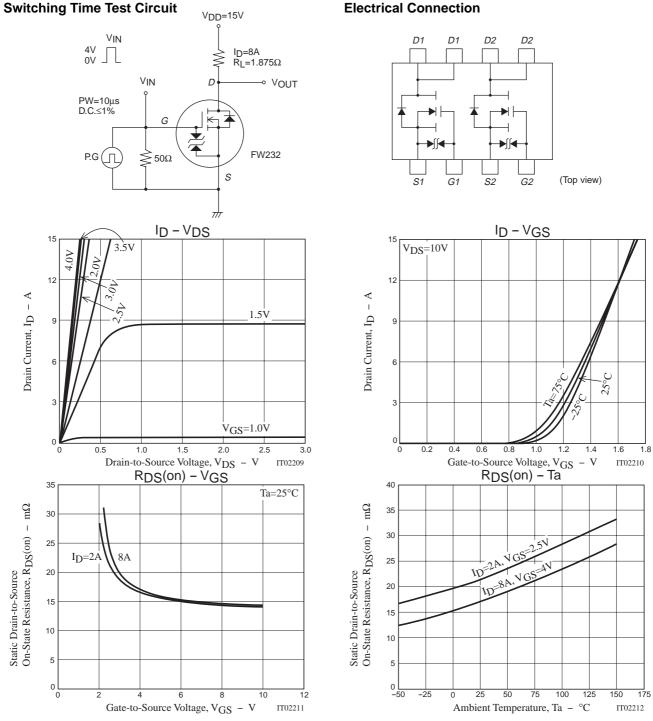
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0	30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VDS=0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	0.4		1.3	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =8A	16	22		S

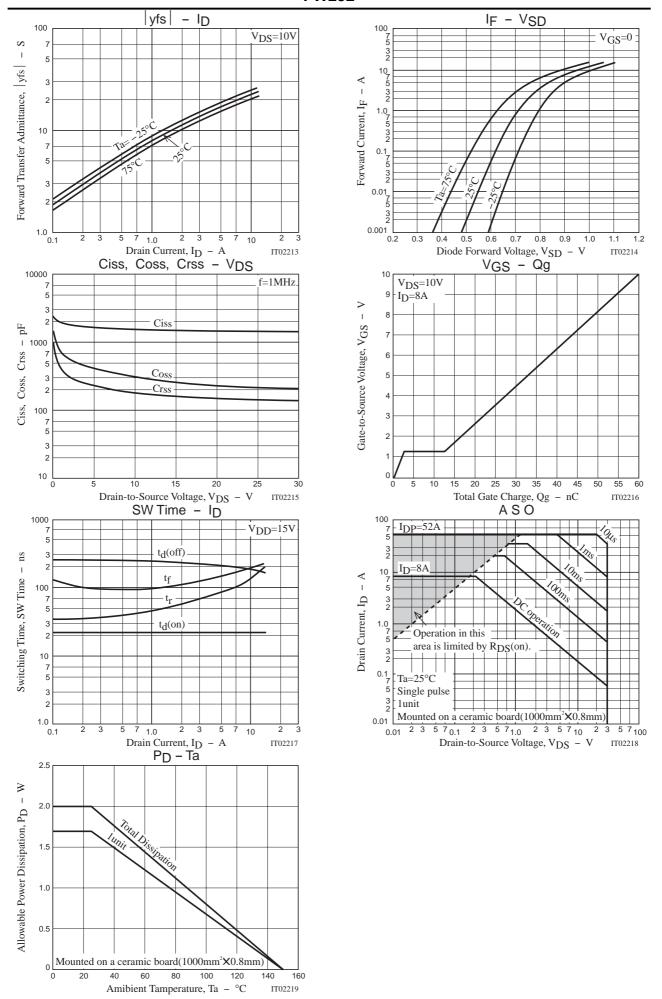
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =8A, V _G S=4V		17	23	mΩ
	RDS(on)2	I _D =2A, V _{GS} =2.5V		23	32	mΩ
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		1550		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		310		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		190		pF
Turn-ON Delay Time	td(on)	See specified Test Circuit		22		ns
Rise Time	t _r	See specified Test Circuit		110		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		200		ns
Fall Time	tf	See specified Test Circuit		170		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =8A		60		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =8A		2.6		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =8A		10		nC
Diode Forward Voltage	V _{SD}	I _S =8A, V _{GS} =0		0.9	1.2	V





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