



Load Switching Applications

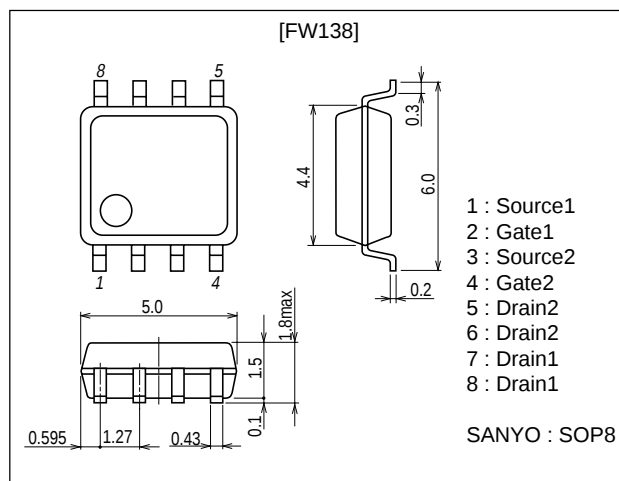
Features

- 4V drive.
- Low ON-resistance.

Package Dimensions

unit : mm

2129



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		-30	V
Gate-to-Source Voltage	V_{GSS}		± 20	V
Drain Current (DC)	I_D		-4	A
Drain Current (Pulse)	I_{DP}	$PW \leq 10\mu s$, duty cycle $\leq 1\%$	-32	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board (1500mm ² X0.8mm) 1unit	1.7	W
Total Dissipation	P_T	Mounted on a ceramic board (1500mm ² X0.8mm)	2.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1mA$, $V_{GS} = 0$	-30			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30V$, $V_{GS} = 0$			-1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 16V$, $V_{DS} = 0$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10V$, $I_D = -1mA$	-1.0		-2.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10V$, $I_D = -4A$	4	5.8		S

Marking : W138

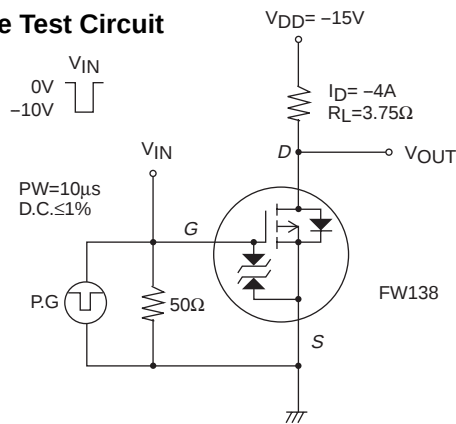
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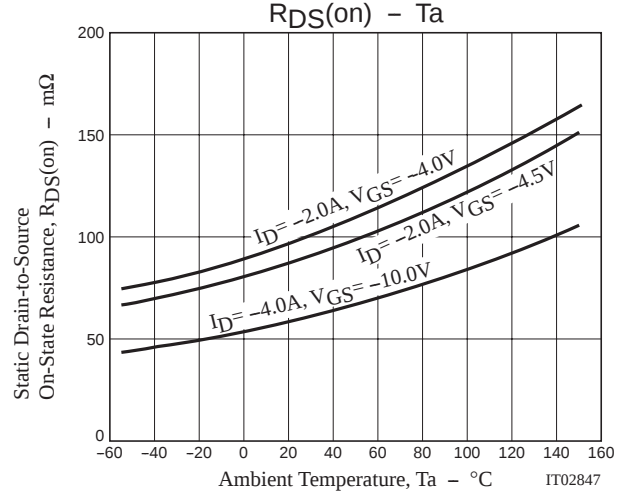
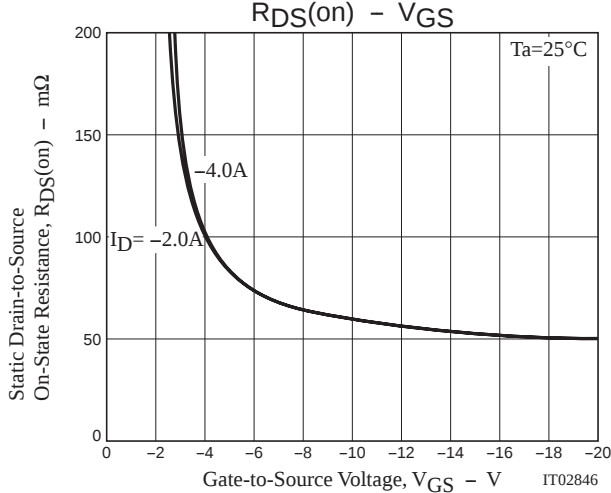
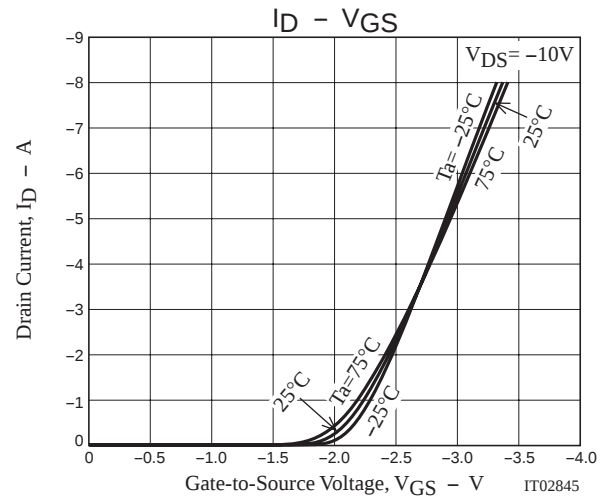
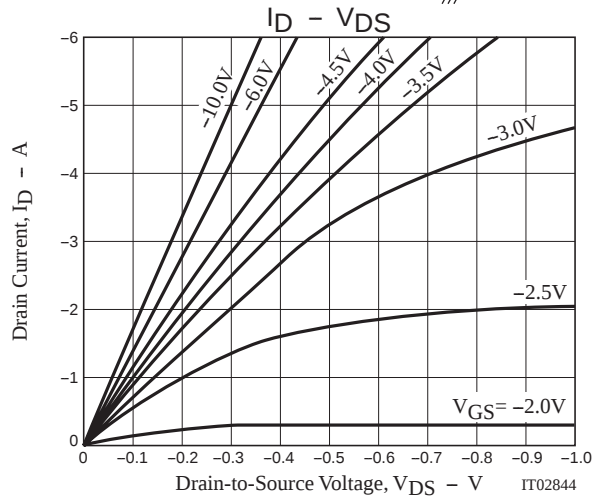
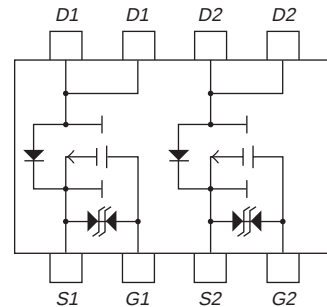
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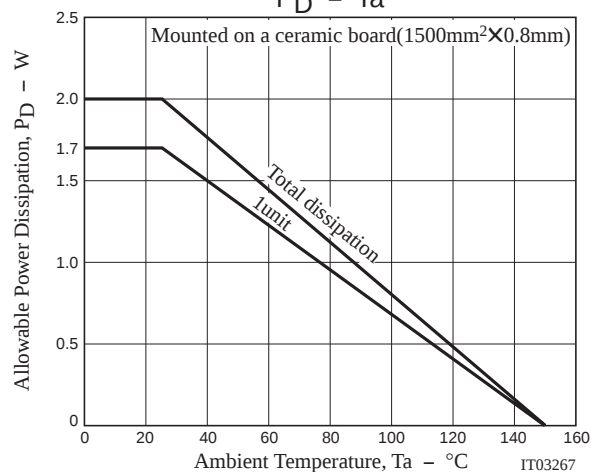
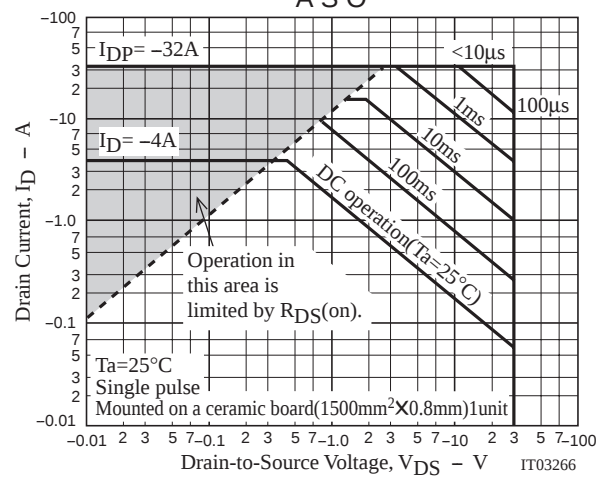
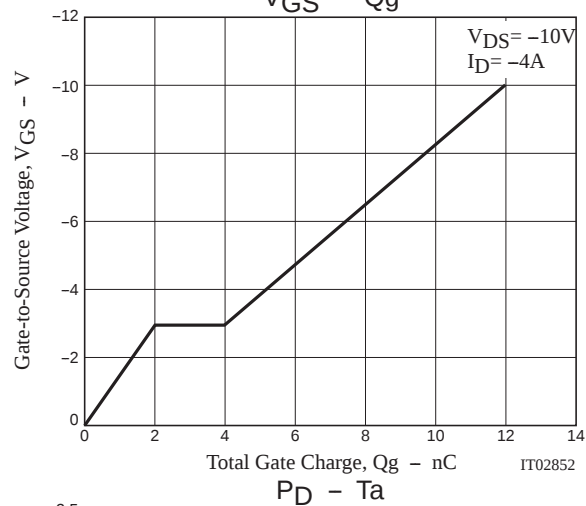
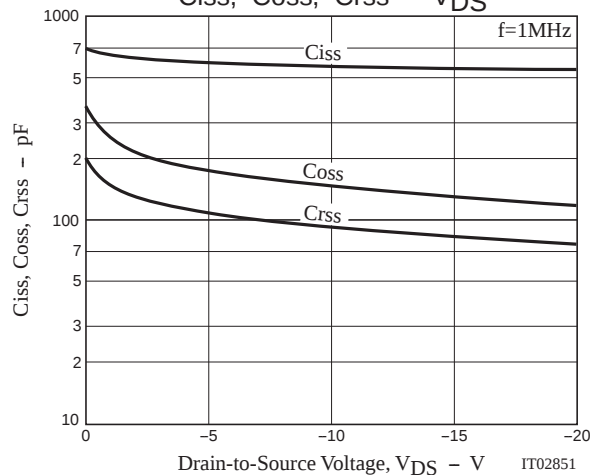
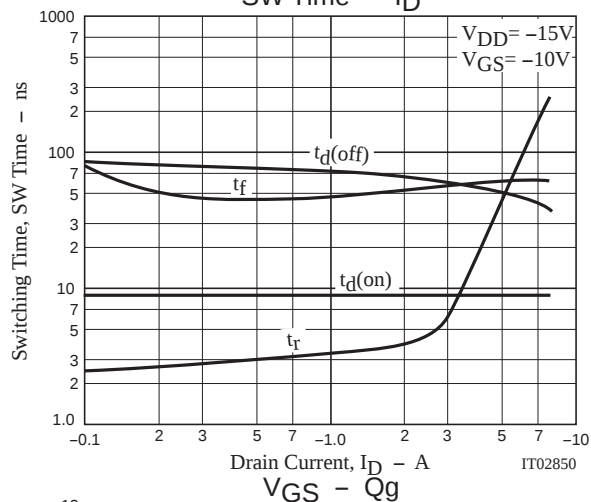
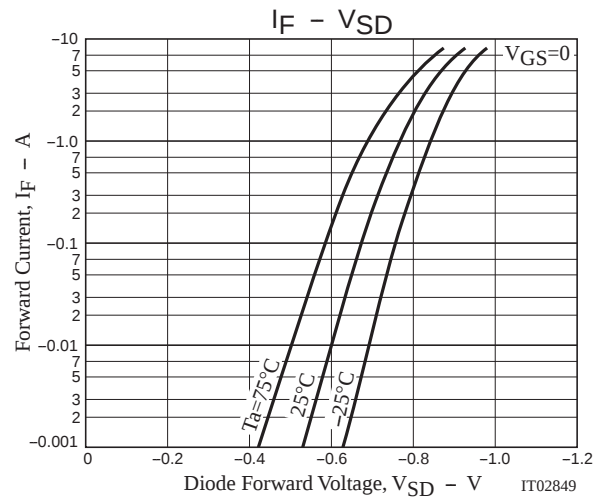
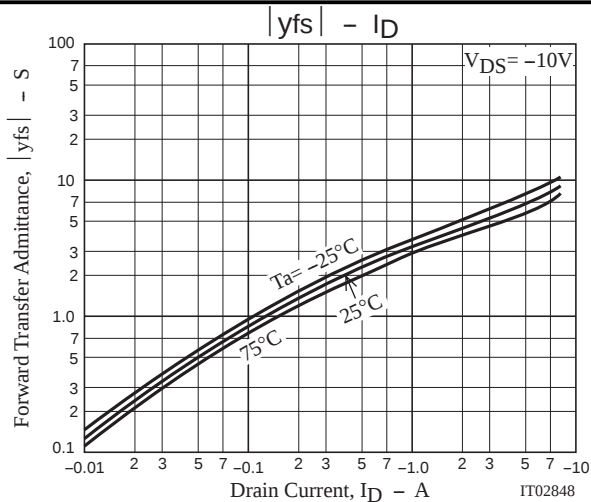
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -4A, V_{GS} = -10V$		64	84	$m\Omega$
	$R_{DS(on)2}$	$I_D = -2A, V_{GS} = -4.5V$		94	132	$m\Omega$
	$R_{DS(on)3}$	$I_D = -2A, V_{GS} = -4V$		104	146	$m\Omega$
Input Capacitance	C_{iss}	$V_{DS} = -10V, f = 1MHz$		560		pF
Output Capacitance	C_{oss}	$V_{DS} = -10V, f = 1MHz$		150		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS} = -10V, f = 1MHz$		95		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit		9		ns
Rise Time	t_r	See specified Test Circuit		18		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit		55		ns
Fall Time	t_f	See specified Test Circuit		60		ns
Total Gate Charge	Q_g	$V_{DS} = -10V, V_{GS} = -10V, I_D = -4A$		12		nC
Gate-to-Source Charge	Q_{gs}	$V_{DS} = -10V, V_{GS} = -10V, I_D = -4A$		2		nC
Gate-to-Drain "Miller" Charge	Q_{gd}	$V_{DS} = -10V, V_{GS} = -10V, I_D = -4A$		2		nC
Diode Forward Voltage	V_{SD}	$I_S = -4A, V_{GS} = 0$		-0.85	-1.5	V

Switching Time Test Circuit



Electrical Connection (Top view)





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