



# **Load Switching Applications**

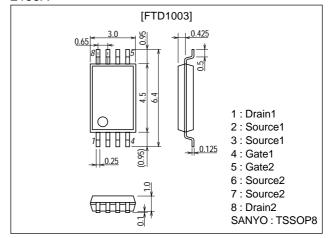
#### **Features**

- · Low ON resistance.
- · 2.5V drive.
- · Mounting height 1.1mm.
- · Composite type, facilitating high-density mounting.

### **Package Dimensions**

unit:mm

2155A



## **Specifications**

**Absolute Maximum Ratings** at  $Ta = 25^{\circ}C$ 

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		-20	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±10	V
Drain Current (DC)	I <sub>D</sub>		-1.4	Α
Drain Current (pulse)	I <sub>DP</sub>	PW≤10µs, duty cycle≤1%	-5.6	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (1000mm²×0.8mm) 1unit	0.8	W
Total Dissipation	PT	Mounted on a ceramic board (1000mm <sup>2</sup> ×0.8mm)	1.0	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

### **Electrical Characteristics** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Uill
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0	-20			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0			-10	μΑ
Gate-to-Source Leakage Current	IGSS	$V_{GS}=\pm 8V$ , $V_{DS}=0$			±10	μΑ
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-0.4		-1.4	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1.4A	2.1	3		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)1	I <sub>D</sub> =-1.4A, V <sub>GS</sub> =-4V		235	315	mΩ
	R <sub>DS</sub> (on)2	I <sub>D</sub> =-0.7A, V <sub>GS</sub> =-2.5V		340	480	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, f=1MHz		180		pF
Output Capacitance	Coss	V <sub>DS</sub> =-10V, f=1MHz		90		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-10V, f=1MHz		43		pF

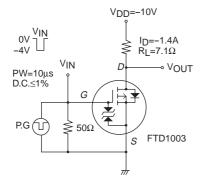
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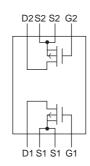
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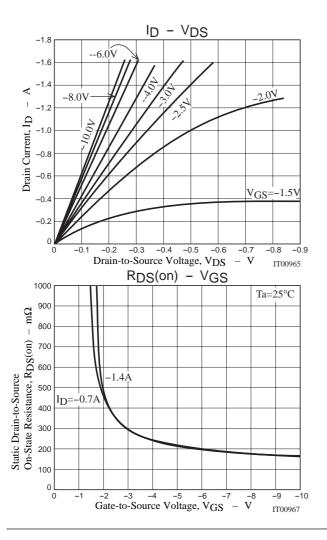
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	OTILE
Turn-ON Delay Time	t <sub>d</sub> (on)	See Specified Test Circuit		10		ns
Rise Time	t <sub>r</sub>	See Specified Test Circuit		380		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See Specified Test Circuit		280		ns
Fall Time	t <sub>f</sub>	See Specified Test Circuit		310		ns
Total Gate Charge	Qg	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-1.4A		9.5		nC
Gate-to-Source Charge	Qgs			1		nC
Gate-to-Drain "Miller" Charge	Qgd			1.5		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1.4A, V <sub>GS</sub> =0		-0.83	-1.2	V

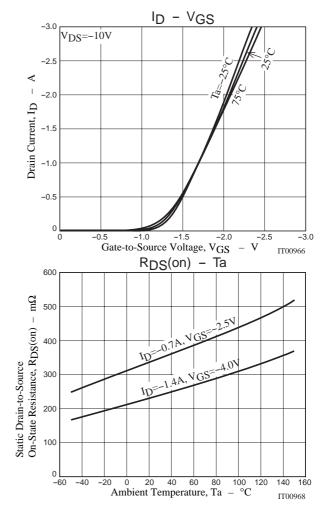
### **Switching Time Test Circuit**

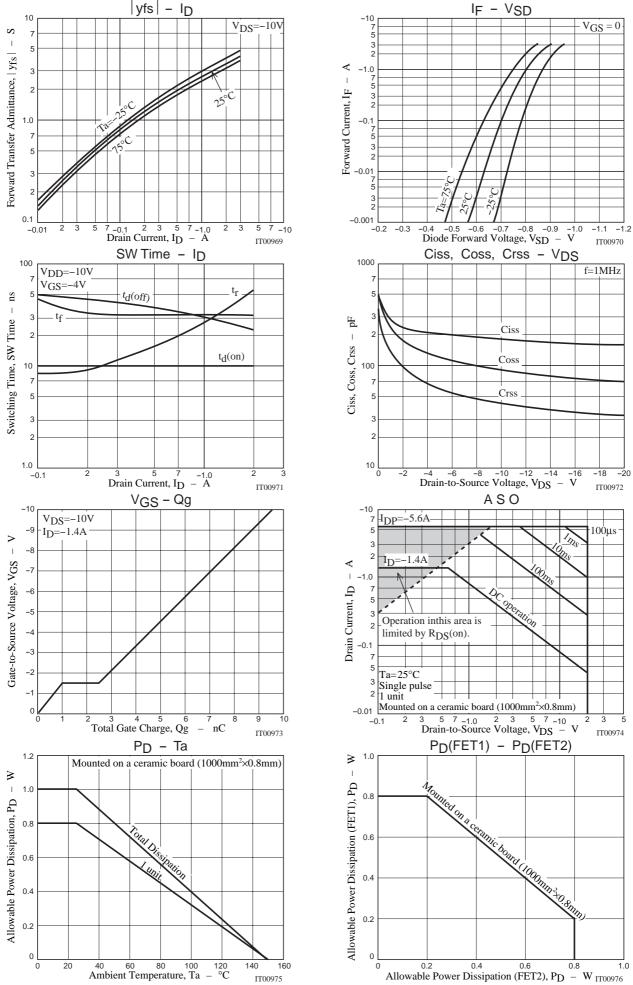


### **Electrical Connection**









### FTD1003

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